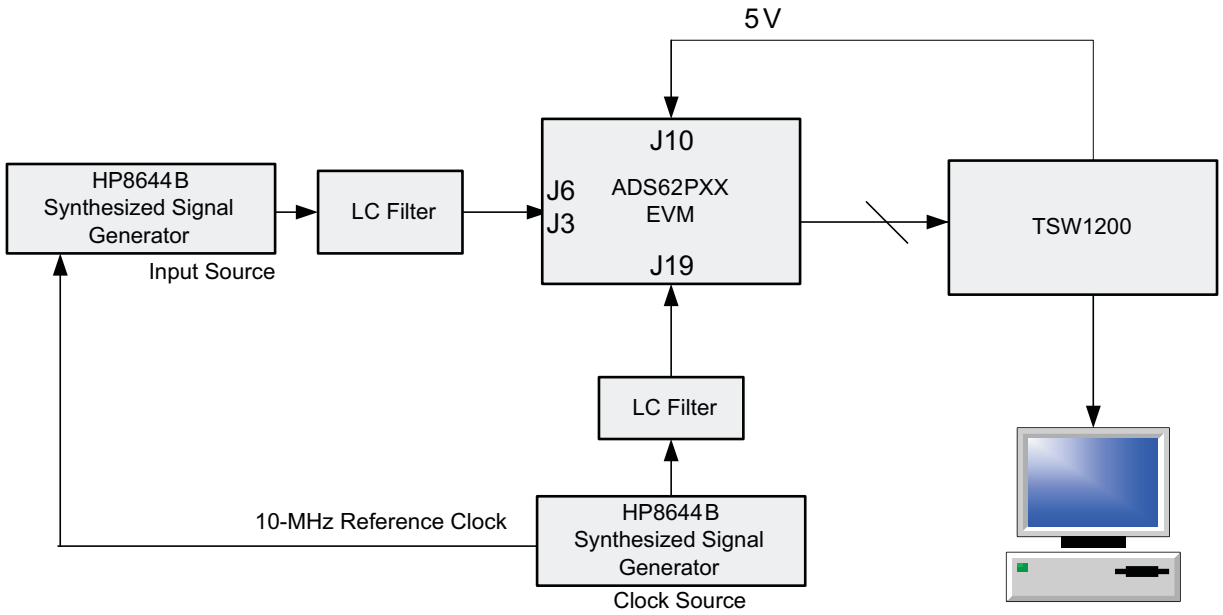
ADS62PxxEVM Quick Start Guide (Board Rev C)



1. The ADS62Pxx evaluation module (EVM) contains a linear power supply solution to provide the necessary voltage rails to the analog-to-digital converter (ADC) and associated circuits. Connect +5V to J10 and GND to J12.
   a. If you are using the TSW1200 for capture, it also can be used to source 5 V for the EVM. On the TSW1200, configure JP8 to short 1-2 and J22 to short 1-2 and jumper over 5V from the banana jacks on the TSW1200 to P5 on the ADC EVM.
2. Connector J19 (SMA) is connected to the path of the ADC clock input. Connect a filtered, low phase noise, 2-V_{PP} clock source.
3. Connectors J6 (SMA) and J3 (SMA) are used to connect to ADC channel A and channel B, respectively. Connect a filtered, low-phase noise CW signal generator with the amplitude set to 10 dBm to either J3 or J6.
4. Connect the TSW1200 or suitable logic analyzer to J8 to capture the resulting digital data. If you connect a TSW1200 to capture data, follow the additional alphabetically labeled steps.
   a. After installing the TSW1200 software and connecting the TSW1200 to the USB port, open the TSW1200 software.
   b. Select the ADC under evaluation, from the *TI ADC Selection* pulldown menu.
   c. Change the *ADC Sample Rate* and *ADC Input Frequency* to match those of the signal generator.
   d. After selecting a Single Tone FFT test, press the *Capture Data* button.

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