

Title: **Video Interface**

Section: **video interface.prj**

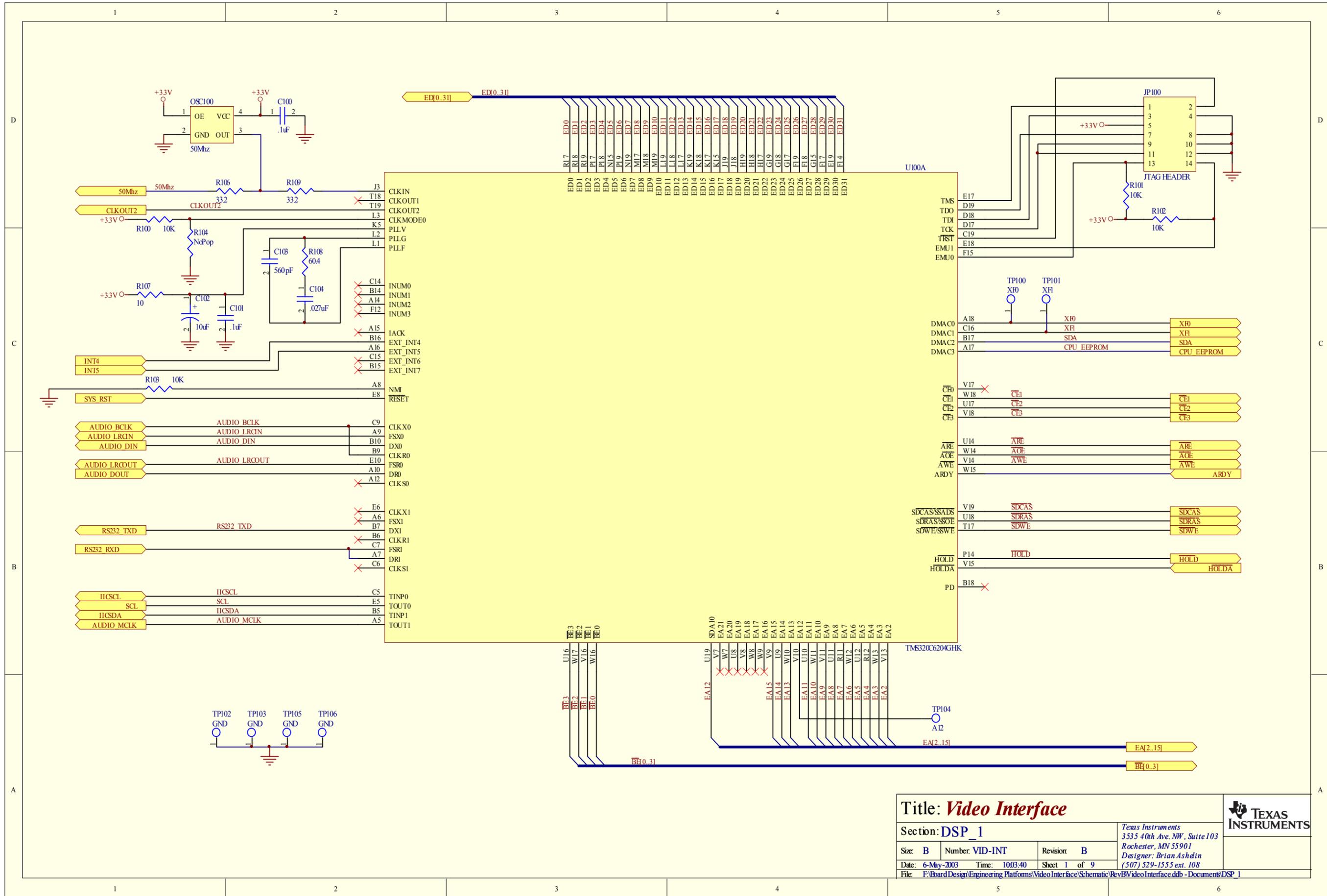
Size: B Number: VID-INT Revision: B

Date: 6-May-2003 Time: 1000:25 Sheet 1 of 1

File: F:\Board Design\Engineering Platforms\Video Interface\Schematic\RevB\Video Interface.ddb - Documents\video interface.prj

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Rochester, MN 55901
Designer: Brian Ashdin
(507) 529-1555 ext. 108





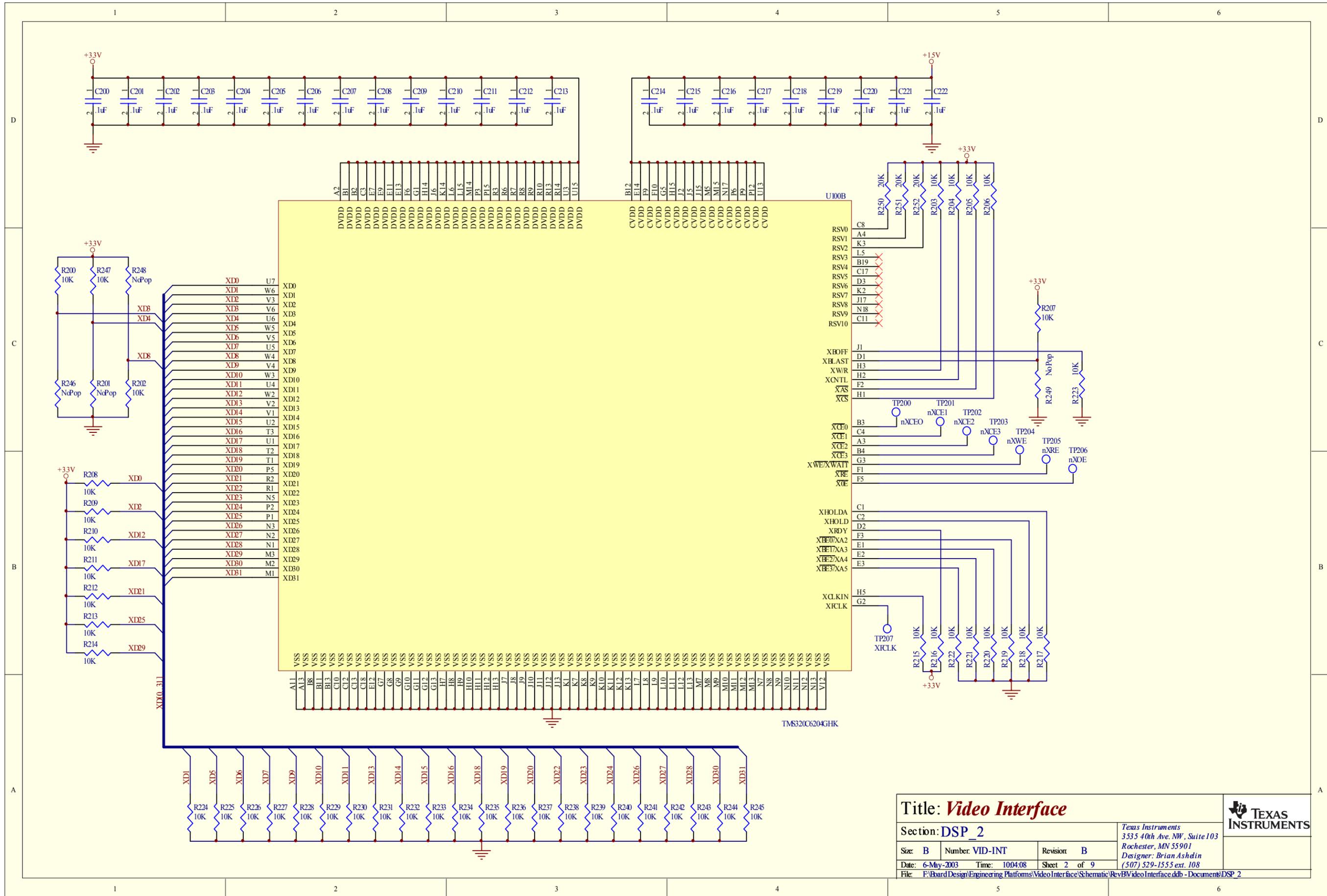
Title: **Video Interface**

Section: **DSP_1**

Size: **B** Number: **VID-INT** Revision: **B**
 Date: **6-May-2003** Time: **1003:40** Sheet **1** of **9**

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Title: Video Interface

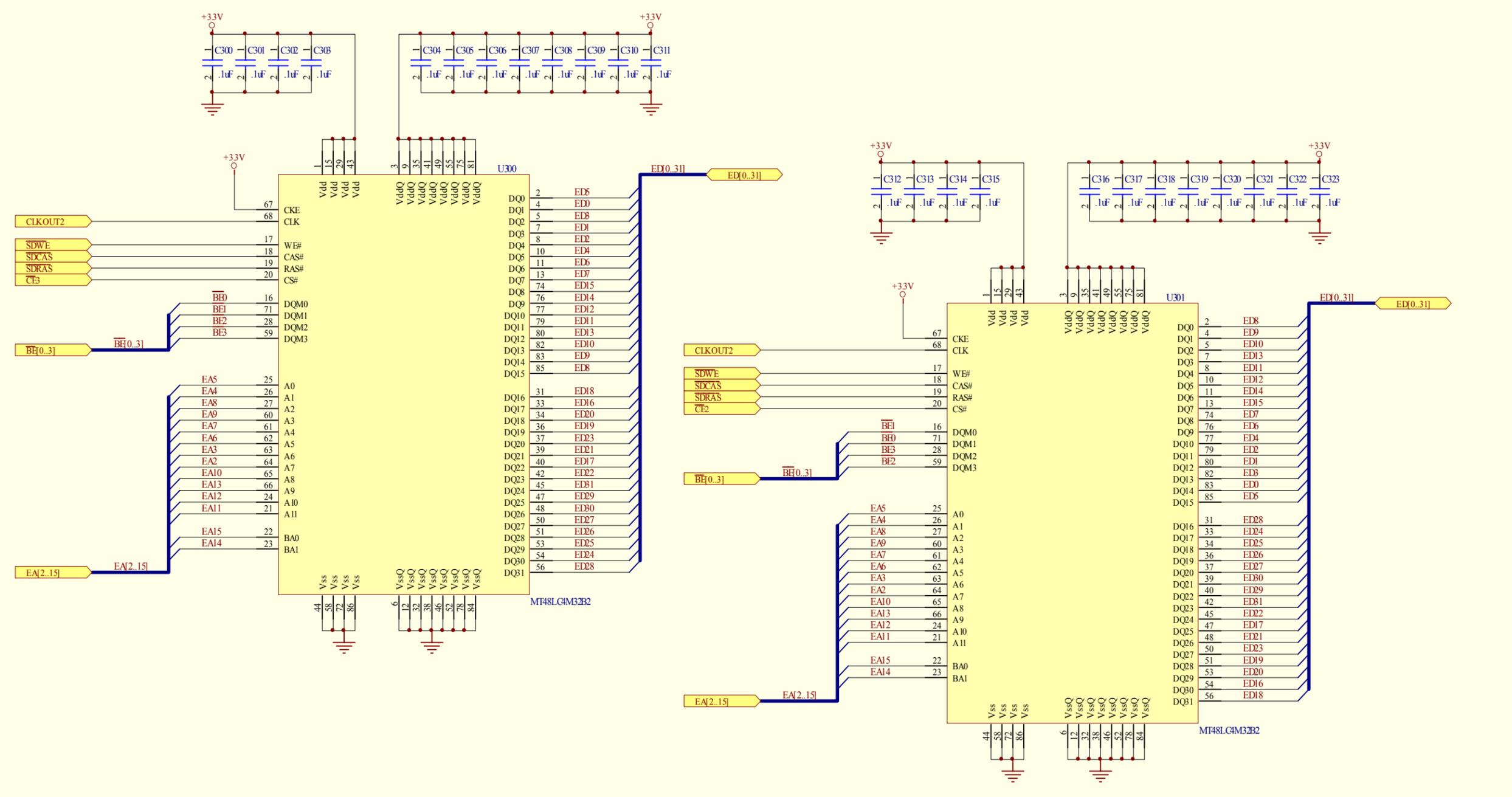
Section: DSP_2

Size: B	Number: VID-INT	Revision: B
Date: 6-May-2003	Time: 1004:08	Sheet 2 of 9
File: F:\Board Design\Engineering Platforms\Video Interface\Schematic\RevB\Video Interface.ddb - Documents\DSP_2		

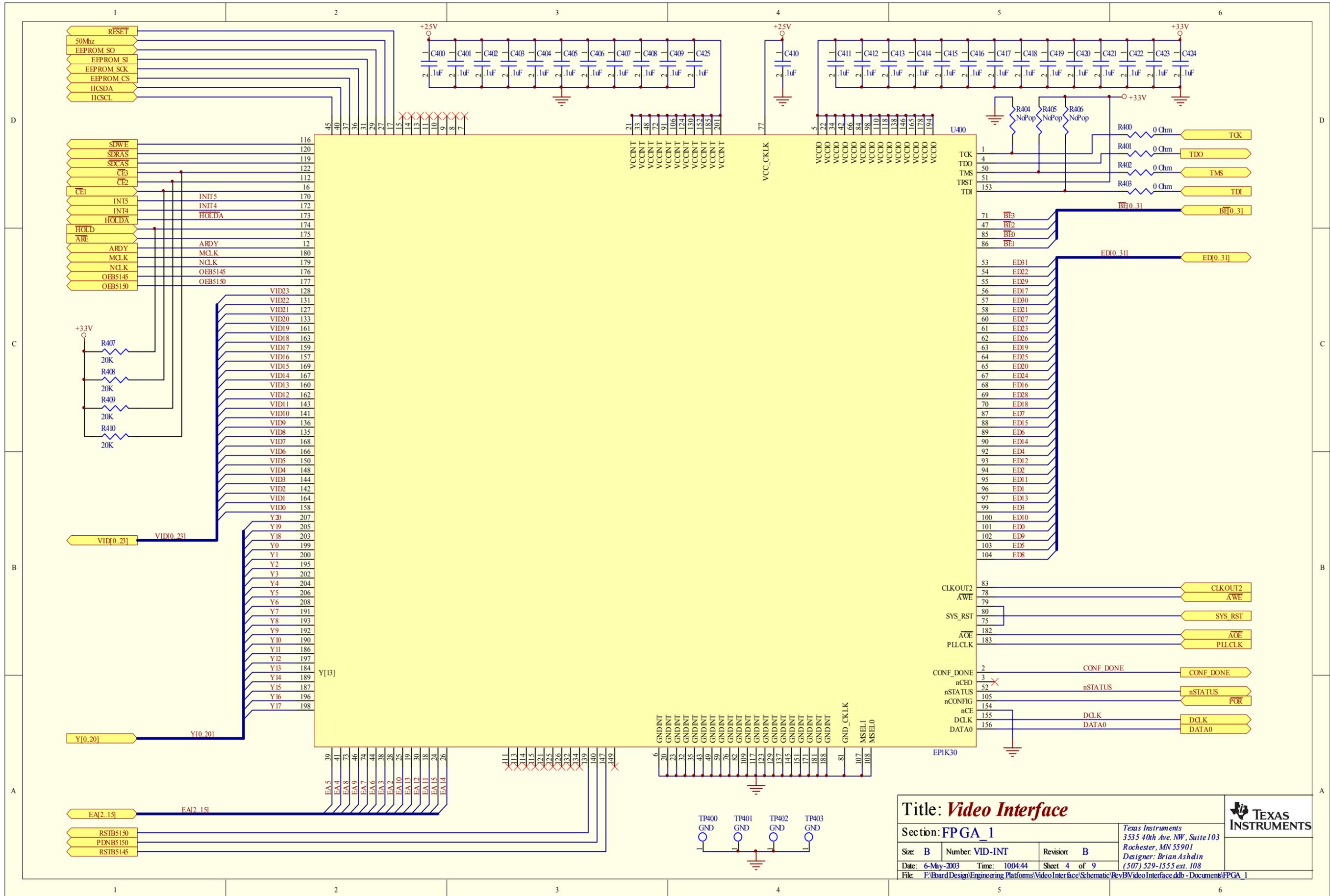
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D
C
B
A



Title: <i>Video Interface</i>		
Section: DRAM		
Size: B	Number: VID-INT	Revision: B
Date: 6-May-2003	Time: 1004:27	Sheet: 3 of 9
File: F:\Board Design\Engineering Platforms\Video Interface\Schematic\RevB\Video Interface.ddb - Documents\DRAM		Texas Instruments 3535 40th Ave. NW, Suite 103 Rochester, MN 55901 Designer: Brian Ashdin (507) 529-1555 ext. 108



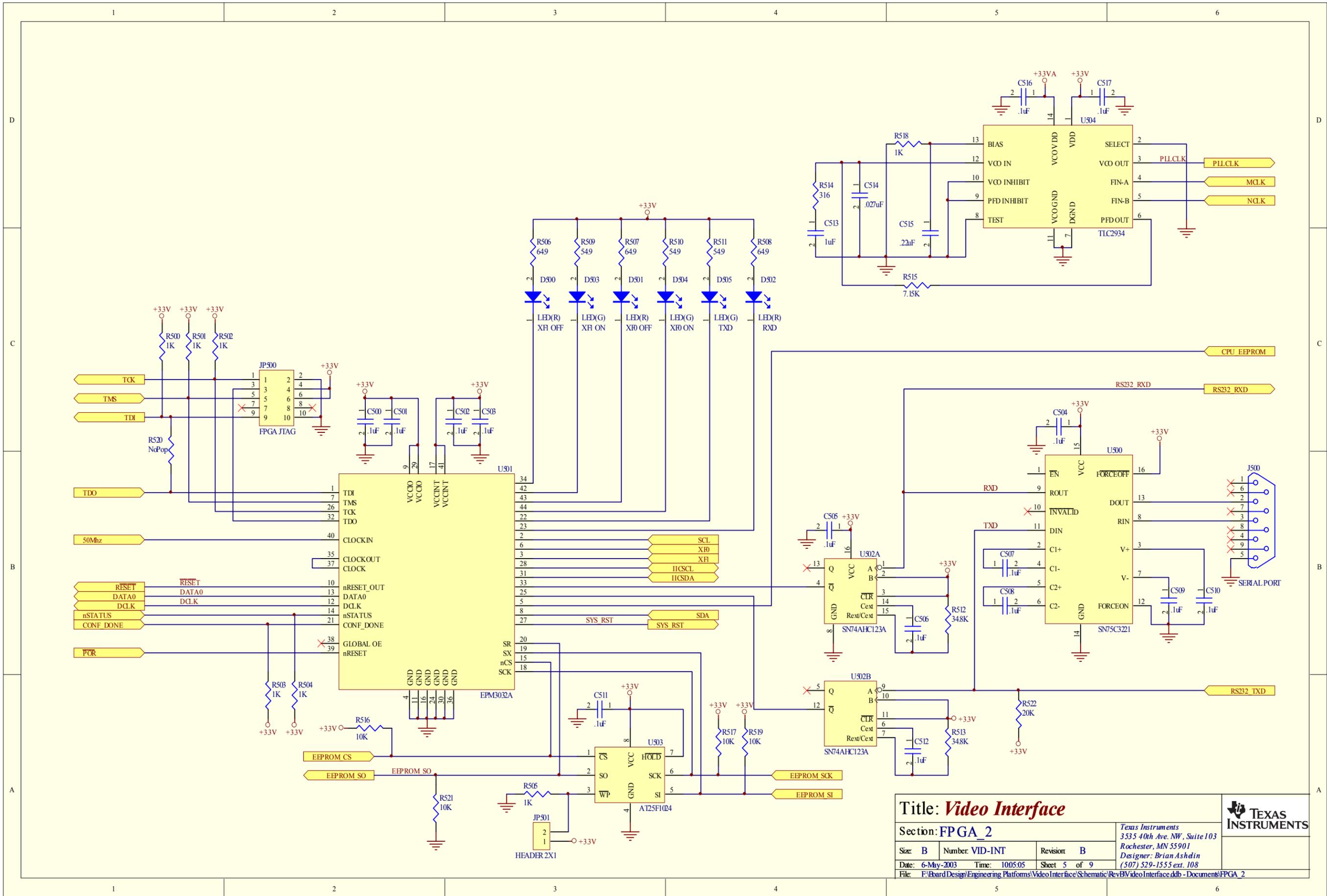
Title: Video Interface

Section: FPGA_1

Size: B Number: VID-INT Revision: B
 Date: 6-May-2003 Time: 1004:44 Sheet 4 of 9
 File: F:\Board Design\Engineering Platforms\Video Interface\Schematic\RevB\Video Interface.ddb - Documents\FPGA_1

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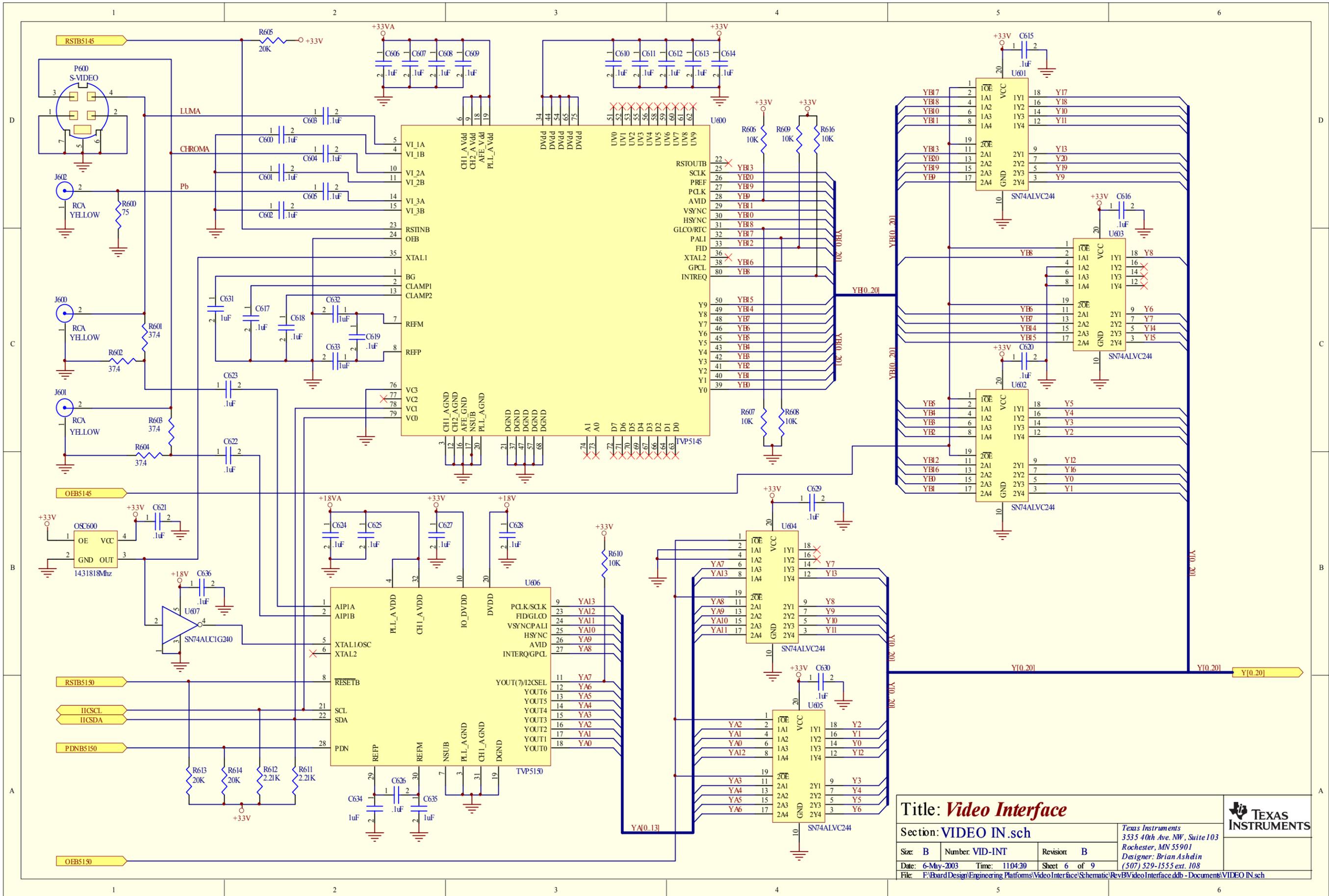
Title: Video Interface

Section: FPGA_2

Size: B	Number: VID-INT	Revision: B
Date: 6-May-2003	Time: 1005:05	Sheet 5 of 9
File: F:\Board Design\Engineering Platforms\Video Interface\Schematic\RevB\Video Interface.ddb - Documents\FPGA_2		

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 Designer: Brian Ashdin
 (507) 529-1555 ext. 108





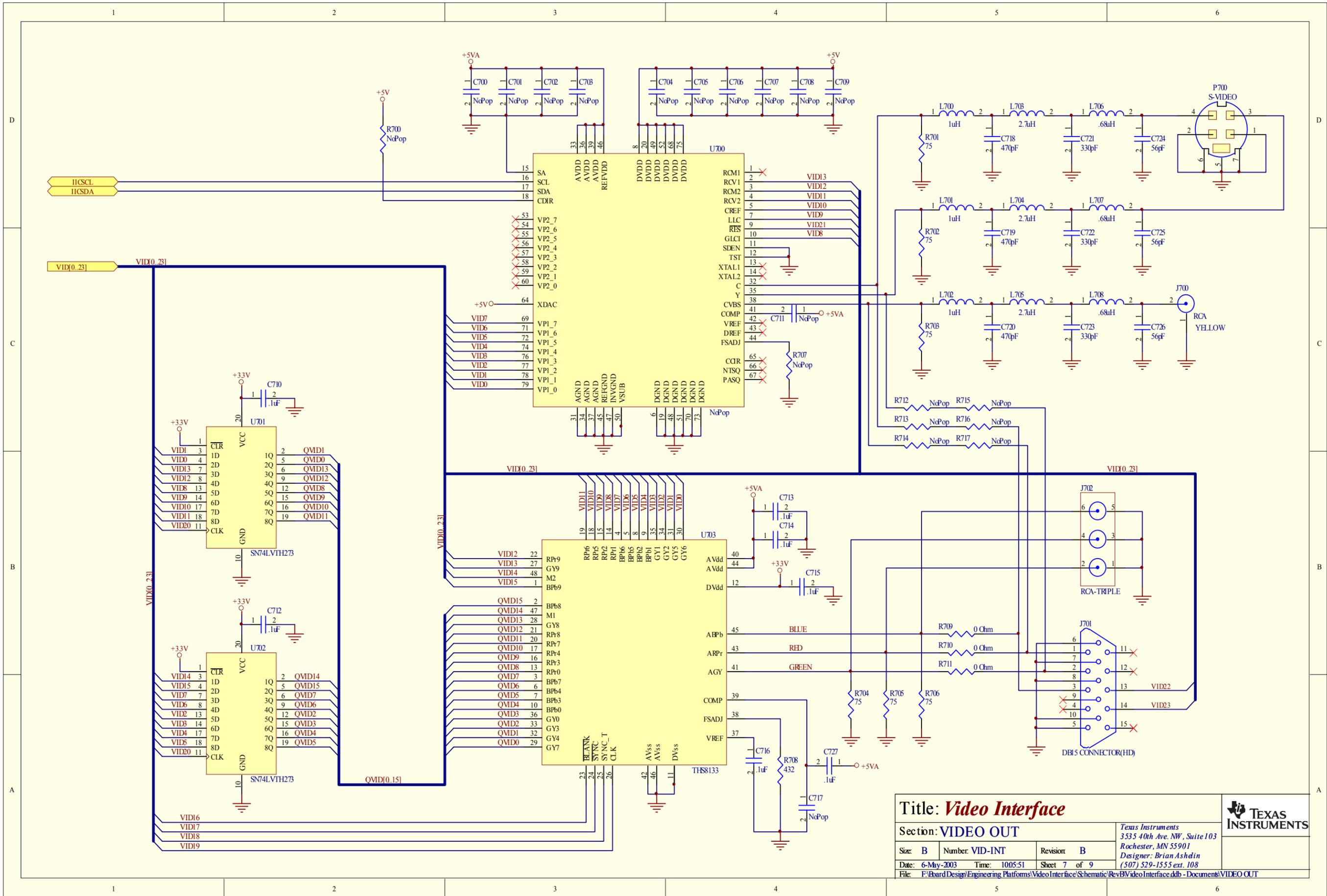
Title: Video Interface

Section: VIDEO IN.sch

Size: B Number: VID-INT Revision: B
 Date: 6-May-2003 Time: 11:04:30 Sheet 6 of 9

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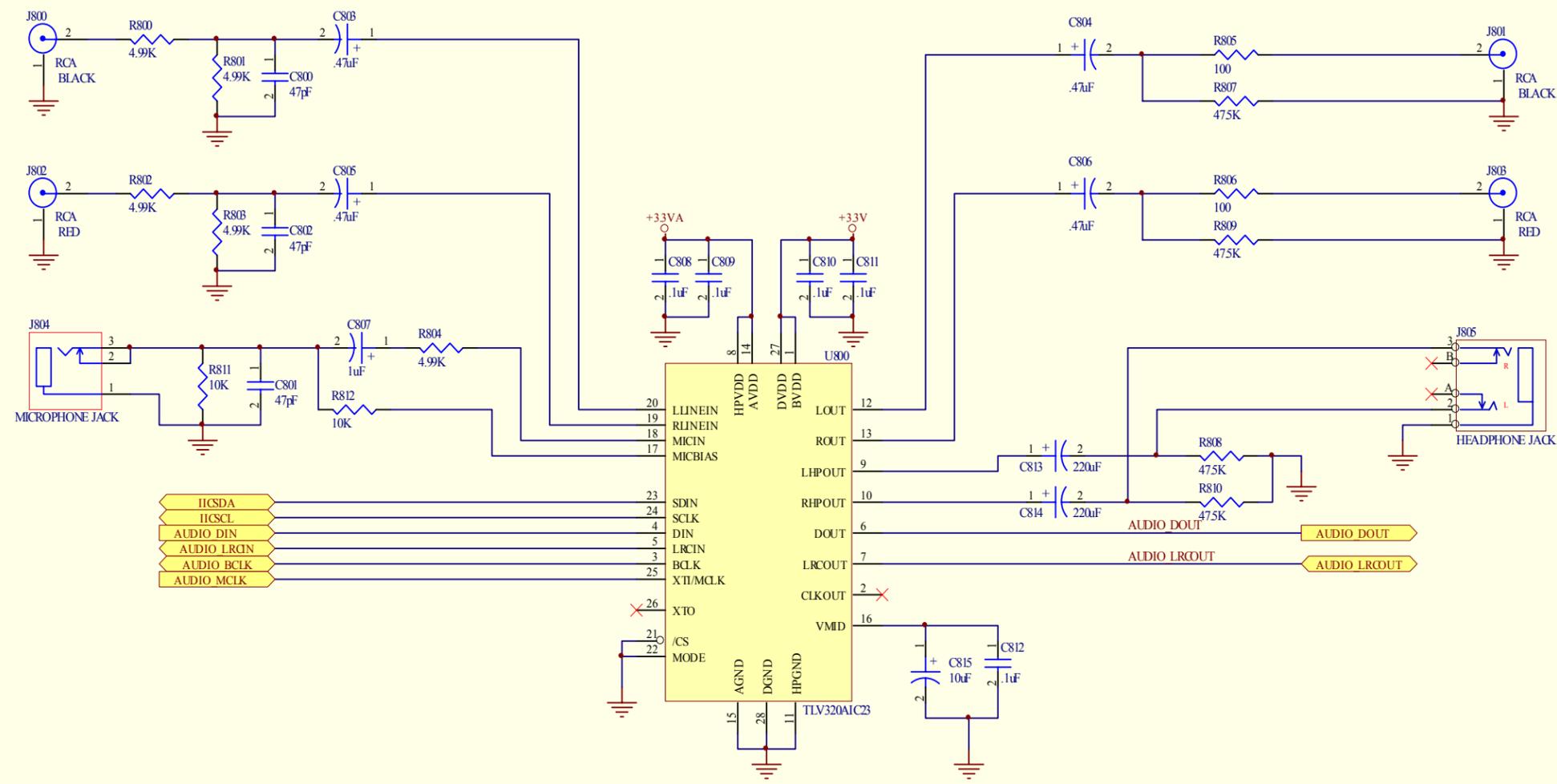
Title: Video Interface

Section: VIDEO OUT

Size: B Number: VID-INT Revision: B
 Date: 6-May-2003 Time: 1005:51 Sheet 7 of 9

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- IIICSDA
- IIICSL
- AUDIO DIN
- AUDIO LRCIN
- AUDIO BCLK
- AUDIO MCLK

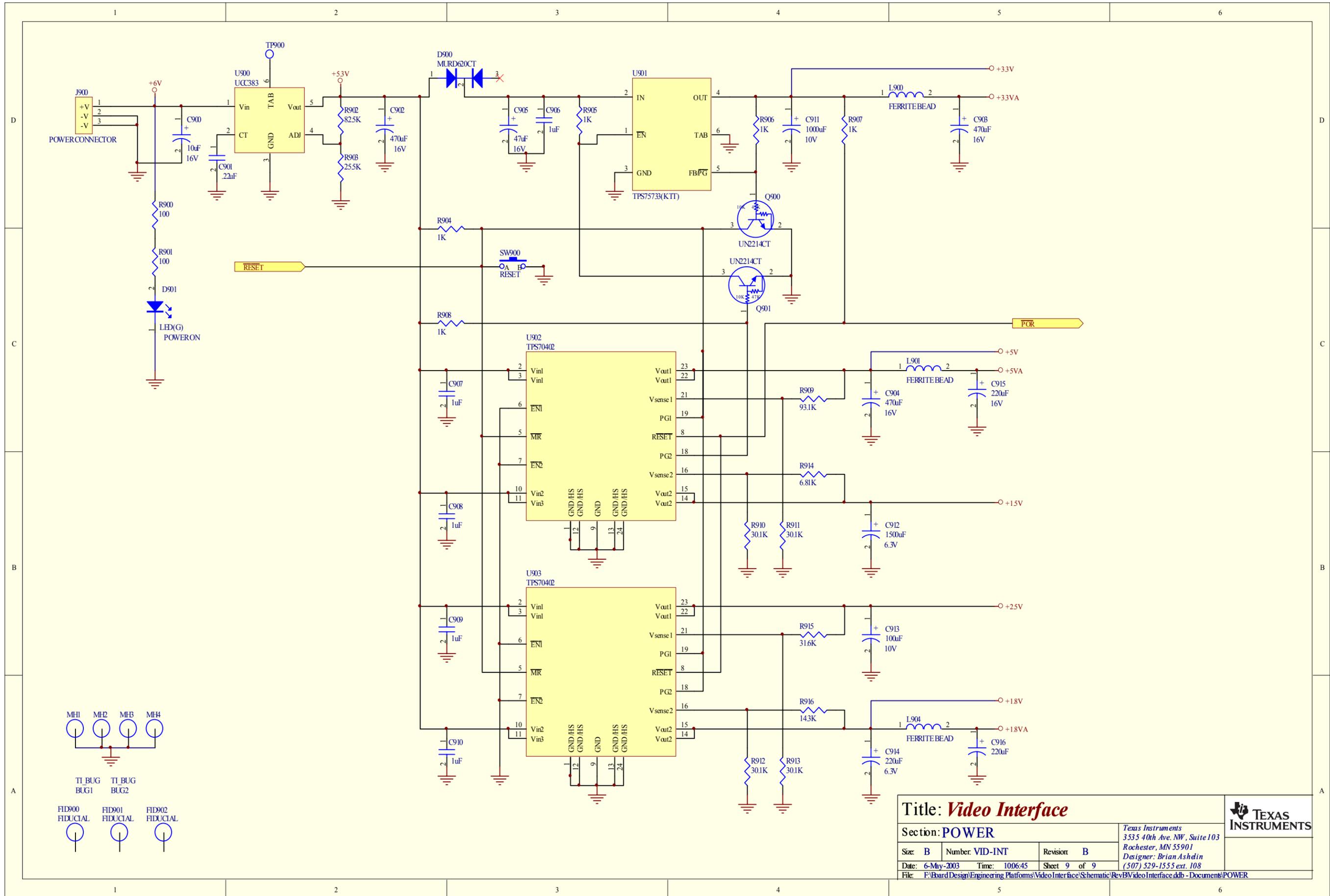
Title: Video Interface

Section: **AUDIO**

Size: B	Number: VID-INT	Revision: B
Date: 6-May-2003	Time: 1006:24	Sheet 8 of 9
File: F:\Board Design\Engineering Platforms\Video Interface\Schematic\RevB\Video Interface.ddb - Documents\AUDIO		

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Title: **Video Interface**

Section: **POWER**

Size: B	Number: VID-INT	Revision: B
Date: 6-May-2003	Time: 1006:45	Sheet 9 of 9
File: F:\Board Design\Engineering Platforms\Video Interface\Schematic\RevB\Video Interface.ddb - Documents\POWER		

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Mailing Address: Texas Instruments
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