

TLV1704-SEP Production Flow and Reliability Report

ABSTRACT

The TLV1704-SEP (Quad) device offers a wide supply range, rail-to-rail inputs, low quiescent current, and low propagation delay. All these features come in industry-standard, extremely-small packages, making these devices the best general-purpose comparators available.

The open collector output offers the advantage of allowing the output to be pulled to any voltage rail up to 36 V above the negative power supply, regardless of the TLV1704-SEP supply voltage.

The device is a microPower comparator. Low input offset voltage, low input bias currents, low supply current, and open-collector configuration make the TLV1704-SEP device flexible enough to handle almost any application, from simple voltage detection to driving a single relay.

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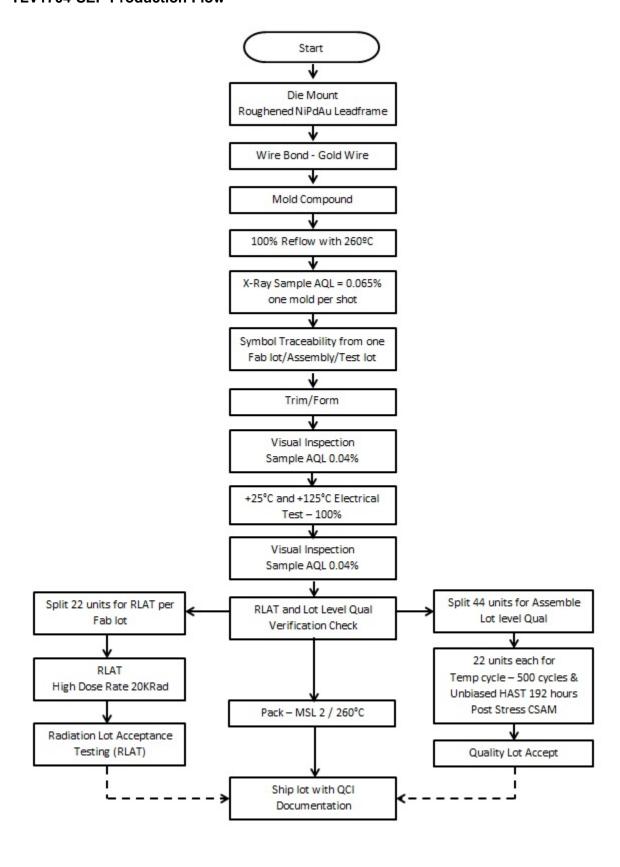
Device Introduction www.ti.com

Device Introduction

TLV1704-SEP is a Radiation Hardened device in plastic package which allow this device to be use in a space application. The device was verified immune to 43 MeV·cm²/mg at 125°C for single event latchup. Each Fab lot was tested according to MIL-STD-883 for Radiation Lot Acceptance Tested (RLAT) up to 20 krad(Si) and each Assembly and Test lot will go thru process flow as shown in Section 2. To ensure the quality of TLV1704-SEP it is qualified with Space EP requirement, which is explained in Section 3.



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Device Qualification www.ti.com

3 Device Qualification

The following is the device qualification summary:

Qualification by Similarity (Qualification Family)

A new device can be qualified either by performing full scale quality and reliability tests on the actual device or using previously qualified device(s) through "Qualification by Similarity" (QBS) rules. By establishing similarity between the new device and those qualified previously, repetitive tests will be eliminated, allowing for timely production release. When adopting QBS methodology, the emphasis is on qualifying the differences between a previously qualified product and the new product under consideration. The QBS rules for a technology, product, test parameters or package shall define which attributes are required to remain fixed in order for the QBS rules to apply. The attributes which are expected and allowed to vary will be reviewed and a QBS plan shall be developed, based on the reliability impact assessment above, specifying what subset of the full complement of environmental stresses is required to evaluate the reliability impact of those variations. Each new device shall be reviewed for conformance to the QBS rule sets applicable to that device. See JEDEC JESD47 for more information.

Device Baseline ⁽¹⁾							
TI Device:	TLV1704AMPWTPSEP/ TLV1704AMPWPSEP		Assembly Site:	TI-MLA (Malaysia)			
DLA VID:	V62/18613-01XE		Test Site:	TI-MLA (Malaysia)			
Wafer Fab:	FFAB		Pin/Package Type:	TSSOP (PW) 14			
Fab Process:	BICOM3XHV		Leadframe:	Cu			
Fab Technology:	BICOM		Termination Finish:	NiPdAu			
Die Revision:	E		Bond Wire:	24.3 μm Au			
Die Name:	RTLV1704PAH2		Moisture Sensitivity:	MSL 2 / 260°C			
ESD CDM:	±1000 V						
ESD HBM:	±1000 V						

⁽¹⁾ Baseline information in effect as of the date of this report.



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Space Enhanced Products New Device Qualification Matrix									
Note	Note that qualification by similarity ("qualification family") per JEDEC JESD47 is allowed.								
Description	Condition	Sample Size Used/Rejects	Lots Required	Test Method					
Electromigration	Maximum Recommended Operating Conditions	N/A	N/A	Per TI Design Rules					
Wire Bond Life	Maximum Recommended Operating Conditions	N/A	N/A	Per TI Design Rules					
Electrical Characterization	TI Data Sheet	15	3	N/A					
Electrostatic Discharge	НВМ	2 unitalyoltaga	NI/A	EIA/JESD22-A114					
Sensitivity	CDM	3 units/voltage	N/A	EIA/JESD22-C101					
Latch-up	Per Technology	5/0	3	EIA/JESD78					
Physical Dimensions	TI Data Sheet	5/0	1	EIA/JESD22- B100					
Thermal Impedance	Theta-JA on board	Per Pin-Package	N/A	EIA/JESD51					
Bias Life Test	125°C / 1000 hours or equivalent	45/0	3	JESD22-A108 ⁽¹⁾					
Biased Humidity	85°C / 85% / 1000 hours			JESD22-A101 ⁽¹⁾					
or	or	77/0	3						
Biased HAST	130°C / 85% / 96 hours			JESD22-A110 ⁽¹⁾					
Extended Biased Humidity	85°C / 85% / 2600 hours (for reference)			JESD22-A101 ⁽¹⁾					
or	or	77/0	1						
Extended Biased HAST	130°C / 85% / 250 hours (for reference)			JESD22-A110 ⁽¹⁾					
Unbiased HAST	130°C / 85% / 96 hours	77/0	3	JESD22-A.118 ⁽¹⁾					
Temperature Cycle	-65°C to +150°C non- biased for 500 cycles	77/0	3	JESD22-A104 ⁽¹⁾					
Solder Heat	260°C for 10 seconds	22/0	1	JESD22-B106					
Resistance to Solvents	Ink symbol only	12/0	1	JESD22-B107					
Solderability	Condition A (steam age for 8 hours)	22/0	1	ANSI/J-STD-002-92					
Flammability	Method A / Method B	5/0	1	UL-1964					
Bond Shear	Per wire size	5 units × 30/0 bonds	3	JESD22-B116					
Bond Pull Strength	Per wire size	5 units × 30/0 bonds	3	ASTM F-459					
Die Shear	Per die size	5/0	3	TM 2019					
High Temp Storage	150°C / 1000 hours	15/0	3	JESD22-A103-A ⁽¹⁾					
Moisture Sensitivity	Surface Mount Only	12	1	J-STD-020-A ⁽¹⁾					
Radiation Response Characterization	Total Ionization Dose, and Single-Event Latchup	N/A	N/A	MIL-STD-883/Method 1019					
Outgassing Characterization	TML (Total Mass Lost), CVCM (Collected Volatile Condensable Material), WVR (Water Vapor Recorded)	5	1	ASTM E595					

⁽¹⁾ Precondition performed per JEDEC Std. 22, Method A112/A113.



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Outgassing test was performed on 5 units. A total mass loss (TML) of 1.00% and collected volatile condensable material (CVCM) of 0.10% were used as screening levels for rejection of spacecraft materials. The outgas test was performed in a vacuum environment of less than 5×10^{-5} torr according to ASTM E 595, for a duration of 24 hours, at 125°C. The TML, CVCM, and the amount of Water Vapor Recovered (WVR) were measured after the test.

RESULTS

: The following tables list the results of the testing:

Table 1: Outgas test resu

TML	CVCM	WVR
(%)	(%)	(%)
0.01	0.04	0.00
< 0.01	0.04	0.03
		(%)



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Quality and Reliability Data Disclaimer

The attached quality and reliability information is specific to the TI Enhanced Plastic product family of plastic encapsulated commercial-off-the-shelf (COTS) semiconductor products and components. Due to possible differences in product assembly and test baselines, this information is NOT APPLICABLE to TI standard, industrial, or automotive catalog commercial products.

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