



Texas Instruments Incorporated
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PCF8574/A WAIVER

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Regions: US/Europe/Japan/Asia

THIS WAIVER IS REQUIRED FOR ALL PACKAGE TYPES

**PCF8574DGVR / PCF8574DGVRE4 / PCF8574DW / PCF8574DWE4 / PCF8574DWR /
PCF8574DWRE4 / PCF8574N / PCF8574PW / PCF8574PWE4 / PCF8574PWR /
PCF8574PWRE4 / PCF8574RGYR
PCF8574ADGVR / PCF8574ADGVRE4 / PCF8574ADW / PCF8574ADWE4 /
PCF8574ADWG4 PCF8574ADWRG4 / PCF8574ADWR / PCF8574ADWRE4 / PCF8574AN /
PCF8574APW / PCF8574APWE4 / PCF8574APWR / PCF8574APWRE4 / PCF8574ARGYR**

Texas Instruments Inc has discovered that the PCF8574/A does not meet the datasheet spec of 0ns (min limit) for Serial Data Hold time (Tsdh). Please see the table below for the original datasheet specification and waived limits. If a customer is currently using these parts in production, they will most likely not experience an issue in current applications. However, any new application that requires the PCF8574/A to meet a Tsdh value of 0ns may not work. In this case, the PCF8574/A may not acknowledge read or write commands from the I²C master and perform as expected.

Customers may use these devices in new applications now if designed to the waived limits. The waived limits can be met by increasing the hold time. This can be achieved by slowing down the SDA transition through using a larger resistor for the required pullup on the SDA line or increasing the SDA load capacitance, provided all other recommended operating conditions are met.

Texas Instruments Inc is in the process of making the appropriate changes to these devices. The redesigned device is estimated to be released for production in 3QTR of 2005.

PCF8574/A				
Parameter	Original Datasheet		Waived Limits	
	(-40°C to +85°C)		(-40°C to +85°C)	
	Min	Max	Min	Max
Tsdh (I ² C serial data hold time)	0	900ns	300ns	

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CUSTOMER
Signed and accepted
Printed Name of person signing this document: _____
Title of person signing this document: _____
Signature: _____
Customer (Company) Name : _____
Sold To Number: _____
Quantity _____
Date: _____

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