	1	2	3	4
			Γ	Revision History
	Design Notes * Built on "LM2830/31 SOT23-5 EVAL"		T T	Revision Notes
	* R101-X for test purpose only			A * Built & tested
A	* Passive components tuned for same per	ormance		A
	Input 4.5 5	51/	Outpu	ut
	4.55	U1-1 1 LM2830X-Q1	L1-1	
	VIN O			2-1 2 VOUT
	GND O		4.7uH 4.7uH 4.9.9 49.9 49.9 49.9	GND
		10µF 10.0k GND 2	<sup>−</sup> D1-1 MBRM120 <sup>−</sup> R1-1 <sup>−</sup> 11.8k	
в		fswitch = 1.6 MHz		В
			GND R2-1 GND GND	
			600mV	
	Input		Outpu	ıt
	4.5 5		1.3V (	@ 0.6A
	J1-	U1-2 2 LM2830Z-Q1	J	2-2
С	VIN O GND O			o VOUT c c
		$ \begin{array}{c c} - & - & - & - & - \\ \hline \\ - & - & - \\ \hline \\ - & - & - \\ \hline \end{array} $	2.2uH PD1-2 MBRM120 2.2uH R101-2 49.9 C4-2 10µF 10µF	
		fswitch = 3.0 MHz	MBRM120  R1-2 11.8k	
			GND R2-2 GND GND	
			GÑD R2-2 GÑD GÑD ₹10.0k	
			600mV	
			GNU	
D				D
	Texas Instruments and/or its licensors do not warrant the accura	cy or completeness of this specification or any information contained the	Designed for: Public F Project Title: 4.55.5 ein. Number: PMP8631 Rev: A Sheet Title:	Release Mod. Date: 6/13/2013 N -> 1.3V @ 0.6A TEXAS instruments
	Texas Instruments and/or its licensors do not warrant that this de	esign will meet the specifications, will be suitable for your application or fit	for any SVN Rev: Not in version control Assembly Variant: Va	
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	particular purpose, or will operate in an implementation. Lexas In You should completely validate and test your design implementa 1	tion to confirm the system functionality for your application.	Engineer: Matthias Ulmann Contact: http://www.ti	.com/support © Texas Instruments 2013 4

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