



Product Change Notification Form

| | | |
|--------------------------------|---------------|----------------------------------|
| PCN #: LMI-PCN-070002 | Rev: A | Issue Date: 4-May-07 |
| Type of Change: Level 1 | | Effective Date: 25-May-07 |

Reason for Change:

A new all metal layer revision of the LM3S102 (RevC2) was generated to correct RevC0 errata and include new design features

Detailed Description of changes:

The following errata have been corrected in RevC2. Please see Luminary Micro document *LM3S102 Errata for C0*.

- Errata 1.6 Brown-Out-Reset (BOR) state unknown after initial brown out condition.
The BOR state is now properly reflected in the Reset Cause (RESC) register.
- Errata 1.7 Brown-Out-Reset (BOR) does not work when the PLL is enabled to drive the system clock
- Errata 1.8 Software Reset does not work when the PLL is enabled to drive the system clock
- Errata 1.9 Device is non-functional when VDD drops below LDO voltage setting +500 mV
- Errata 2.1 GPIO internal pull-up resistor does not pull up to 3.3 V
- Errata 2.2 PB6 is not 5-V tolerant
- Errata 3.1 JTAG interface is held in reset if TRST is configured as a GPIO
- Errata 3.2 Serial Wire Debug (SWD) interface is held in reset if TRST is configured as a GPIO
- Errata 5.1 Watchdog Timer reset does not work when the PLL is enabled to drive the system clock

There are two new features included in the new RevC2 version of Stellaris:

- New feature 1.0 - Power on reset (POR) state is held asserted when in a brown-out condition. The internal reset circuit of the device holds the device in reset until the brown-out condition is eliminated.
- New feature 2.0 - Enhanced code protection. See the data sheet for information on code protection features for this Stellaris® family microcontroller.

Products Affected:

| Old Part Number | Description | Qualification P/N | New Part Number |
|------------------|-----------------|-------------------|------------------|
| LM3S102-IRN20-C0 | Microcontroller | N/A | LM3S102-IRN20-C2 |

Forecasted Key Milestones:

| Milestone | Date |
|--------------------------------|----------|
| Samples Available | 4-May-07 |
| Qualification Report Available | 4-May-07 |
| Production Release | 4-May-07 |

(*) The date of "First Availability of Post-Conversion Product" is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand; therefore, although customers should be prepared to receive the Post-Converted products on this date, we will continue to ship the pre-converted products until the inventory has been depleted.

Recommended Action: Accept PCN

Reference Documents / Attachments: N/A

Should you have any issues with the timeline or content of this change, please contact the representative listed below within 10 days. No response will be deemed as customer's acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this PCN.

For questions, concerns, or comments please direct all correspondence to: customer.service@luminarymicro.com

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2024, Texas Instruments Incorporated