

Global Positioning System (GPS) Receiver

Benefits

- Reduce board space with software signal correlator, eliminating the need for hardware correlator
- Increase design flexibility with software supporting multiple tasks on the receiver DSP
- Improve functionality with support for multiple Radio Frequency (RF) front-ends
- Enable portable applications with low-power DSP-based solution minimizing board space



Telephony algorithms provided by SPIRIT-DSP

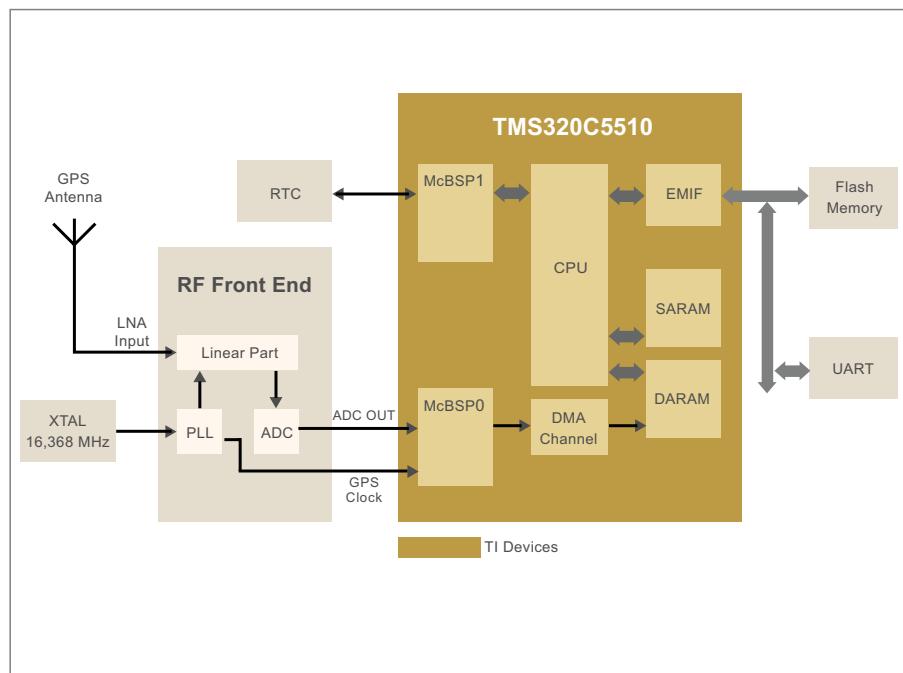
Target Applications

- Automatic vehicle location and fleet tracking devices
- Car security systems
- Mobile handheld devices, such as PDAs, where position location capability is a distinguishing, but not a primary importance feature
- Maritime navigation systems
- Applications with high-accuracy position location, velocity estimation and time synchronization requirements

Telecom solutions based on Texas Instruments DSPs offer developers the flexibility to design a variety of products. These solutions include low-power DSPs and comprehensive, field-proven software supporting industry standards and development tools, which significantly cut power consumption, reduce development time, minimize board space, and enable wired and wireless data, voice, telephony and connectivity functions.

For developers of 12-channel GPS receivers, SPIRIT has developed a software solution based on the TI OMAP™ and TMS320C55x™ DSP platforms. The software package includes a 12-channel correlator, signal search and detection algorithms, and a signal tracking and navigation task solution. The receiver requires signal input from a GPS antenna and calculates coordinates, velocity and time. An 8-channel version of the receiver is also available.

System Example: 12-Channel SW GPS Receiver Portable Device



Global Positioning System (GPS) Receiver

Functional Description

Hardware
<ul style="list-style-type: none">TI TRF5101 Front-end transceiver performs GPS signals receiving, amplification, pass band filtering and digitization
<ul style="list-style-type: none">TI C5510 DSP performs all necessary processing including signal correlation
<ul style="list-style-type: none">Real-Time Clock (RTC) is used for time storing between receivers on/off
<ul style="list-style-type: none">Flash memory is used for storing of the GPS almanac and programs
<ul style="list-style-type: none">UART is utilized for communication of the receiver with other devices
H/W Performance
<ul style="list-style-type: none">Up to 12 channels during signal search
<ul style="list-style-type: none">Frequency: L1, C/A-code
<ul style="list-style-type: none">Position accuracy: 8 m (rms)
<ul style="list-style-type: none">Velocity accuracy: 0.05 m/sec
<ul style="list-style-type: none">Timing accuracy: 100 ns or less
<ul style="list-style-type: none">Sensitivity: -140 dBm (tracking)
<ul style="list-style-type: none">Reacquisition time: 2 sec or less
<ul style="list-style-type: none">Hot start: 10 sec
<ul style="list-style-type: none">Warm start: 30 sec
<ul style="list-style-type: none">Cold start: 60 sec
<ul style="list-style-type: none">Power Consumption: <=250 mW
<ul style="list-style-type: none">Advanced signal processing algorithms: Phase Locked Loop, Delay Locked Loop and Kalman Filtering which forecast frequencies and delays of potential visible satellites
Performance
Implementation of the 12-channel software GPS requires about 150 MIPS. Main allocation of MIPS consumption is initial signal processing:
<ul style="list-style-type: none">Unpacking of signals from RF Front-end (17 MIPS)
<ul style="list-style-type: none">Signal correlation (about 5.5 MIPS per correlation channel)
<ul style="list-style-type: none">Preparing of channel reference signal (7 MIPS)
Data memory: 270 kBytes
Program Memory: 40 kBytes
Main part of memory is used by SW correlator:
<ul style="list-style-type: none">Input signal buffer (42 kBytes)
<ul style="list-style-type: none">Unpacked channel reference signals (11 kBytes per channel)
<ul style="list-style-type: none">Buffer for unpacked channel reference signal (22 kBytes)
<ul style="list-style-type: none">GPS PRN codes storing (22 kBytes)

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Component Selection

Hardware

- TI TMS320VC5510 or TI OMAP5910 DSP Processors
- TRF5101 RF Transceiver
- TCXO, 10.000 MHz crystal device
- Real-Time Clock (RTC): A lot of RTC chips are present in the market; good choice is MAXIM DS1339 in a µSOP package
- UART driver: MAX2338
- Flash memory: any kind of flash memory can be used including serial flash memory chips and parallel, recommended size is 64 Kbytes

Software

- SW GPS software from SPIRIT

Getting Started – Development Tools

Tools

- TMS320VC5510 DSP Starter Kit (DSK)

Documentation

- TMS320VC5510 Fixed-Point Digital Signal Processor Data Manual, SPRS076F
- OMAP5910 Dual-Core Processor Data Manual, SPRS197A
- SW GPS Datasheet
- Complete schematics of a GPS receiver based on Texas Instruments TMS320C55x™ DSP chip and TRF5101 RF Front-end (usage of any other appropriated RF Front-end is possible)
- Software in object code form for C55x™ DSPs
- Software in object code form for OMAP™ processors

Additional documentation and support is available from Spirit Corp

Contact Information for Questions/Support

To purchase this solution or for more information, please contact Spirit Corp.
at: biz@spiritdsp.com

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