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| | H1 | H2 | | | | | | | | |
| | H1 NY PMS 440 0025 PH | H2 NY PMS 440 0025 PH | | | | | | | | |
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| | | LOG |) nstruments | | | | | | | |
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| | | | | | | | Designed for: Public Release | Mod. Date: 2013-5-21 | _ | - |
| | | | | | | Number: XX##### Rev | Project Title: Change in menu I r: E1 Sheet Title: | Project Project Options Parameter | TEXAS INSTRUMENTS | |
| | | | I exas Instruments and/or its licensors warrant that this design will meet the | s do not warrant the accuracy or completeness of this specification or specifications, will be suitable for your application or fit for any particult on is production worthy. You should completely validate and test your of | ny information contained therein. Texas Instruments and/or its like r purpose, or will operate in an implementation. Texas Instrument action implementation to confirm the autom functionality for the sector will be a sector for the sector functionality of the sector of the sector sector sector for the sector functionality of the sector of the sector sector sector for the sector functionality of the sector of the sector sector sector for the sector functionality of the sector of the sector sector sector sector of the sector sector sector of the sector sector sector of the sector sector of the sector sector of the sector sector of the sector of th | ensors do not SVN Rev: Not in version co ts and/or its Drawn By: | ntrol Assembly Variant: [No Variation File: Hardware_ANSI-B.SchDo | nsj Sheet: 1 of 1 c Size: B | nttp://www.ti.com | |
| | 1 | | 2 | 3 | 4 | 5 | joor ready contract. http://www.tl.com/sup | 6 | ?Texas Instruments 2012 | L |
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| | | | | | | The Stackup Legend below this is static. If you change the stackup, update the Legend. |
| | | | | | | Leven Charle He Beteil (and Bet Beter |
| | | | | | | Layer Stack Up Detail for: Ref_Design.PcbDoc |
| | | | | | | Top Solder Mask ("GTS) Solder Resist |
| | | | | | | Botton Layer GGBL) 1.4mil |
| | | | | | | Bottom Solder Hask (+GBS) Solder Resist |
| Α | | | | | | DESIGN INFORMATION A |
| | | | | | | BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION) |
| | | | | | | |
| | | | | | | Number of Layers : 2 |
| | | | | | | MIN. TRACK WDTH: 8 ML |
| | | | | | | MIN. CLEARANCE: 8 MIL |
| | | | | | | MIN. VIA PAD SIZE: 24 MIL |
| - | | | | | | MINIMUM ANNULAR RING 0.05mm (2ML) EXTERNAL |
| | | | | | | PER IPC-D-275 CLASS 2 LEVEL C REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES+/- 3 MIL |
| | | | | | | |
| | | | | | | |
| | | | | | | FR-4 X FR-4 High Tg OTHER |
| | | | | | | THICKNESS: X 62 MIL (1.6mm) +/-10% OTHER |
| | | | | | | TOLERANCE: X ANSI PC-6012 TYPE 3 CLASS 2 |
| | | | | | | |
| В | | | | | | BOW & TWIST: X ANSI IPC-6012 TYPE 3 CLASS 2 B |
| | | | | | | COPPER THICKNESS (FINISHED): |
| | | | | | | OUTER: X 1.4ML (1oz) 2ML (1.4oz) 2.8ML (2oz) |
| | | | | | | INNER SIGNAL: X 1.4ML (10z) 2.8MIL (20z) |
| | | | | | | DRUNG |
| | | | | | | REFERENCE: X AS SHOWN X NC_DRILL FILES |
| | | | | | | PTH MIN COPPER THICKNESS: X 1MIL OTHER |
| | | | | | | BOARD FINISH: |
| | | | | | | SLKSCREEN: X TOP X BOTTOM |
| | | | | | | SILKSCREEN COLOR: X WHITE OTHER |
| | | | | | | SOLDER RESIST COLOR: |
| | | | | | | |
| | | | | | | SURFACE FINISH: X MMERSION GOLD (ENIG) Pb-FREE HASL |
| | | | | | | OTHER |
| 0 | Output | | | | | ARRAY/PANEL: CUT AND TRIM PER MECH LAYER 1 |
| | | | | | | N.C. ROUTE X V. SCORE |
| | | | | | | CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF: |
| | | | | | | X ANSI IPC-A-600F CLASS -> 1 X 2 3 |
| | | | | | | X ANSI IPC-A-600F CLASS -> 1 X 2 3 X UL 94V-0 X RoHS OTHER <u>PER ORDER</u> |
| | | | | | | ADDITIONAL REQUIREMENTS: |
| | | ND2 | | | | MICROSECTION: YES |
| | | | | | | BARE BOARD ELEC. TEST: NONE X REQUIRED PER ORDER |
| | | | | | | |
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| | | <u> </u> | | | | |
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| | | | | | | PROJECT TILE: |
| | PWM Filter (Rev | A) | | | | Change in menu Project Project Options Paramet(|
| | 2700.00mi1- | | | | | DESIGNED FOR: Public Release |
| D | | < <u>−</u> 1(| 000 (mil)> | | | PLE NAME: |
| | | | | | | PWM Filter.PcbDoc |
| | | | Texas Instr | ments (TD) and/or its licensors do not warrant the accuracy | or completeness of this specification | ENGINEER: LAYOUT BY: |
| | | | or any info | mation contained therein. TI and/or its licensors do not war | rrant that this design will meet | Enter name of project Wha ddid the Layout? |
| | M9 Title Sheet | | the specific an implement | ations, will be suitable for your application or fit for any ation. TI and/or its licensors do not warrant that the desig | particular purpose, or will operate in In is production worthy. You should | ALTIUM DESIGNER VERSION: |
| | | | | alidate and test your design implementation to confirm the s | | SCALE: 1.00 10.0.0.27009 |
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| Comment | Designator | Footprint | LibRef | Quantity |
|--------------------|--|----------------------|--------------------|----------|
| 5000 | +15V, -15V, S1_IN, S1_O S2_IN, S2_OUT | Keystone5000 | 5000 | 6 |
| 470n | C1 | 1210 | Capacitor | 1 |
| 47n | C2 | 1210 | Capacitor | 1 |
| 4.7n | СЗ | 1210 | Capacitor | 1 |
| 470p | C4 | 1210 | Capacitor | 1 |
| NU | C5, C6 | 1210 | Capacitor | 2 |
| 0.1u | C7, C9 | 0603 | Capacitor | 2 |
| 10u | C8, C10 | 1210 | Capacitor | 2 |
| 100u | C11, C13 | 1210 | Capacitor | 2 |
| 1р | C12 | 0603 | Capacitor | 1 |
| 10p | C14 | 0603 | Capacitor | 1 |
| 5001 | GND0, GND3, GND4, GN GND6, GND7 | Ю5, Keystone5001 | 5001 | 6 |
| 108-0740-001 | GND1, GND2, V+, V- | Johnson_108-0740-001 | 108-0740-001 | 4 |
| NY PMS 440 0025 PH | H1, H2, H3, H4 | NY PMS 440 0025 PH | NY PMS 440 0025 PH | 4 |
| 901-143 | Input, Output | Amphenol_901-143 | 901-143 | 2 |
| TSW-106-07-G-D | J1 | TSW-106-07-G-D | TSW-106-07-G-D | 1 |
| 50 | R1 | 0603 | Resistor | 1 |
| 866 | R2 | 0603 | Resistor | 1 |
| 8.66k | R3 | 0603 | Resistor | 1 |
| 86.6k | R4 | 0603 | Resistor | 1 |
| 866k | R5 | 0603 | Resistor | 1 |
| NU | R6, R7 | 0603 | Resistor | 2 |
| 310k | R8 | 0603 | Resistor | 1 |
| 10k | R9, R10 | 0603 | Resistor | 2 |
| 1k | R11 | 0603 | Resistor | 1 |
| 31k | R12 | 0603 | Resistor | 1 |
| OPA2209 | U1 | D0008A_M | OPA2188 | 1 |

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