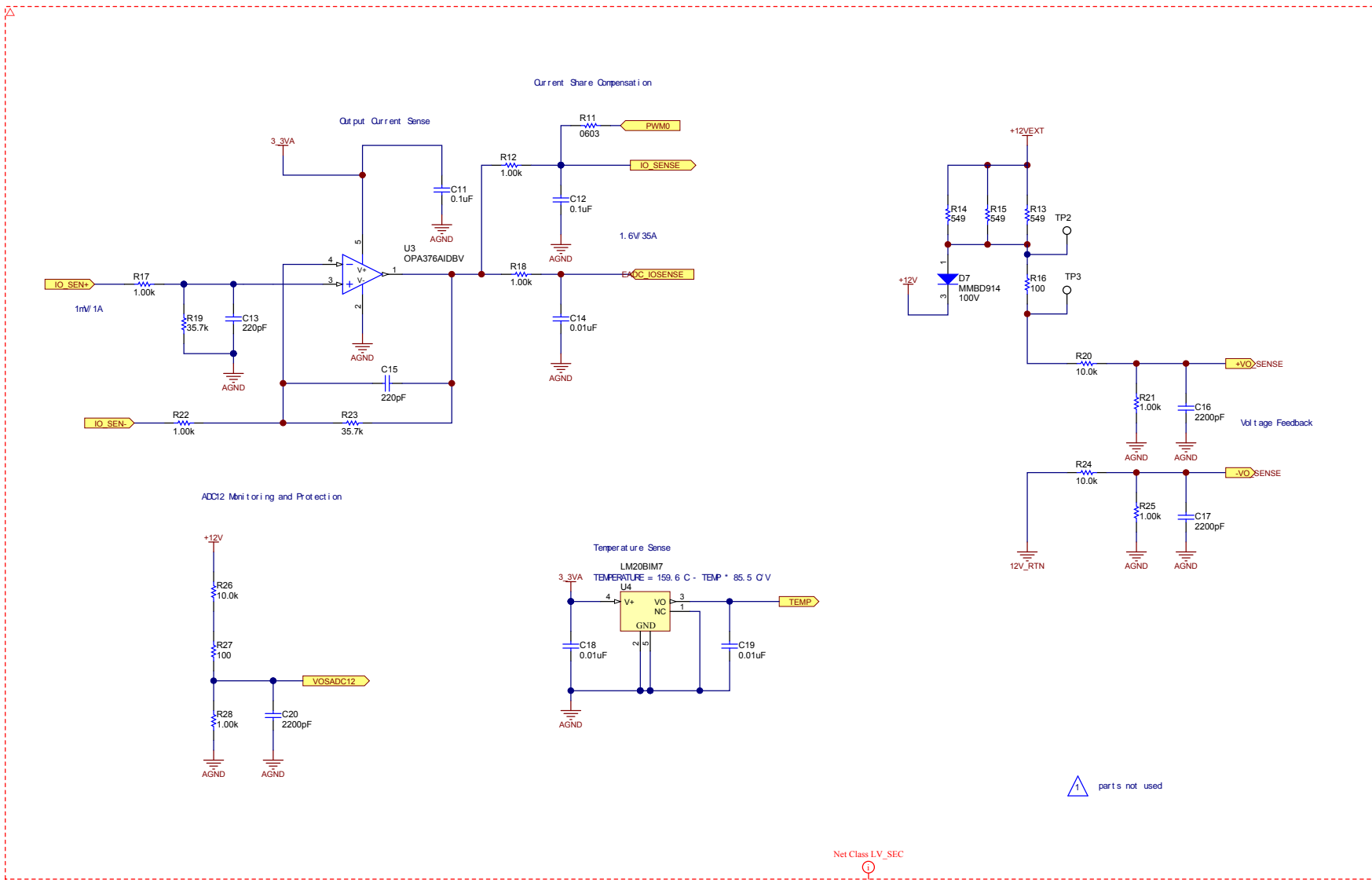


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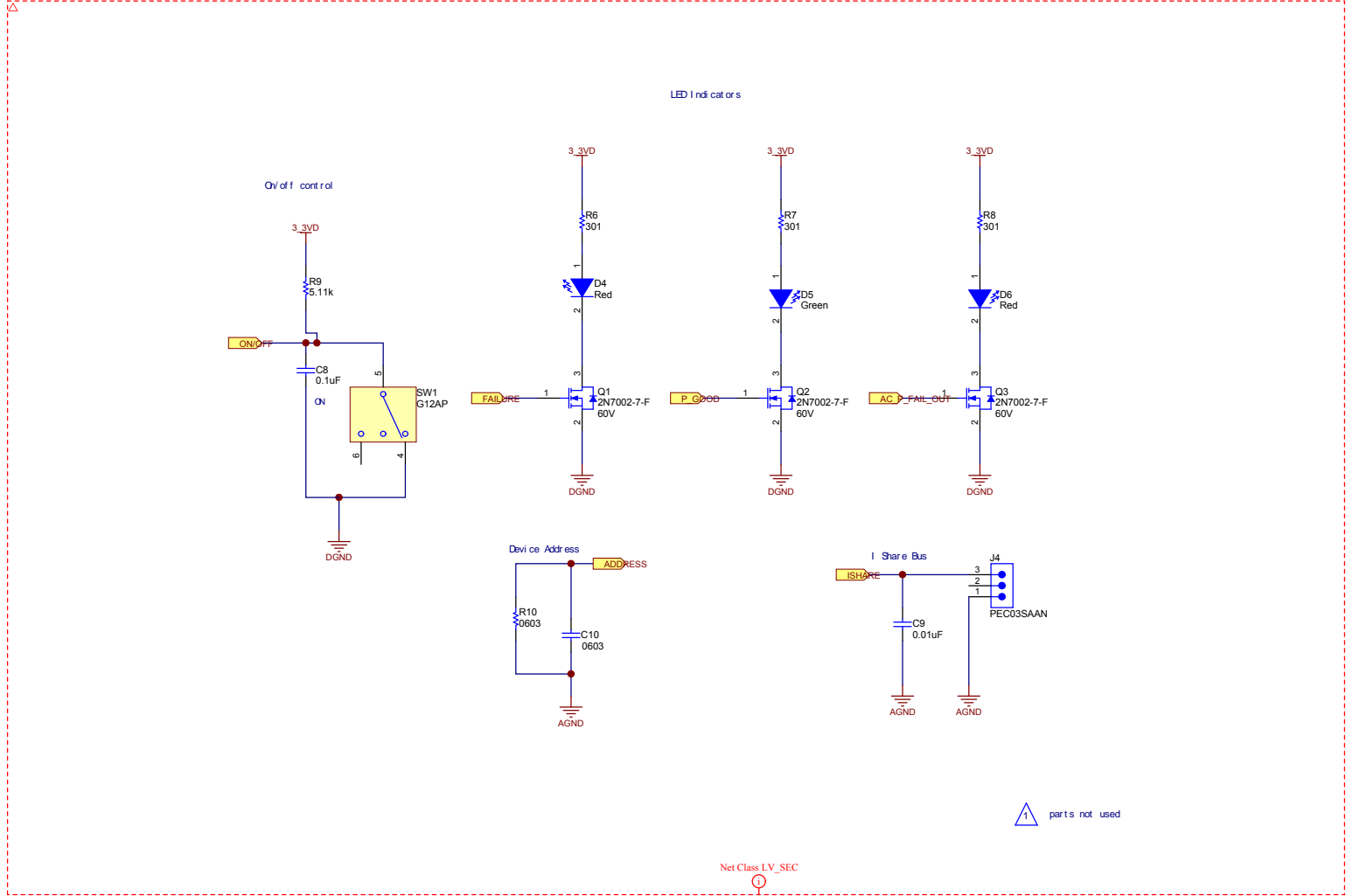


Feedback

1 parts not used

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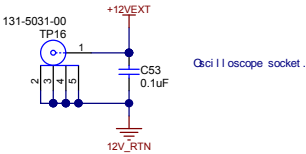
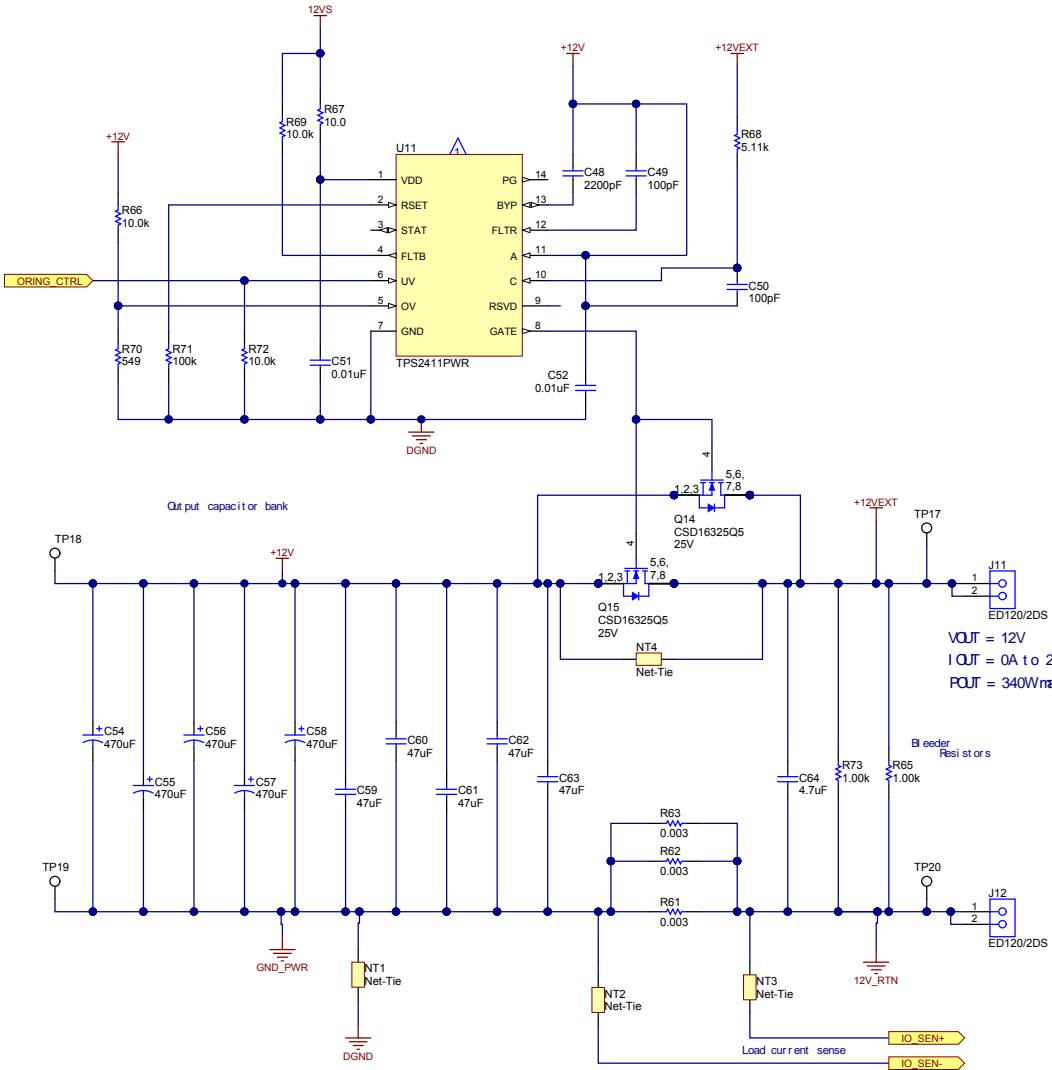
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SVN Rev: Not in version control	Sheet Title:	Project Title: TIDA-00381	
Drawn By:	Assembly Variant: Variant name not interpreted	File: GPIO_SchDoc	Sheet: 2 of 8
Engineer: B.M./H.H./Y.Q.	Contact: http://www.ti.com/support	Size: B	http://www.ti.com

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Mode Oring Control

OUTPUT



V_{OUT} = 12V
 I_{OUT} = 0A to 29A
 P_{OUT} = 340W_{max}

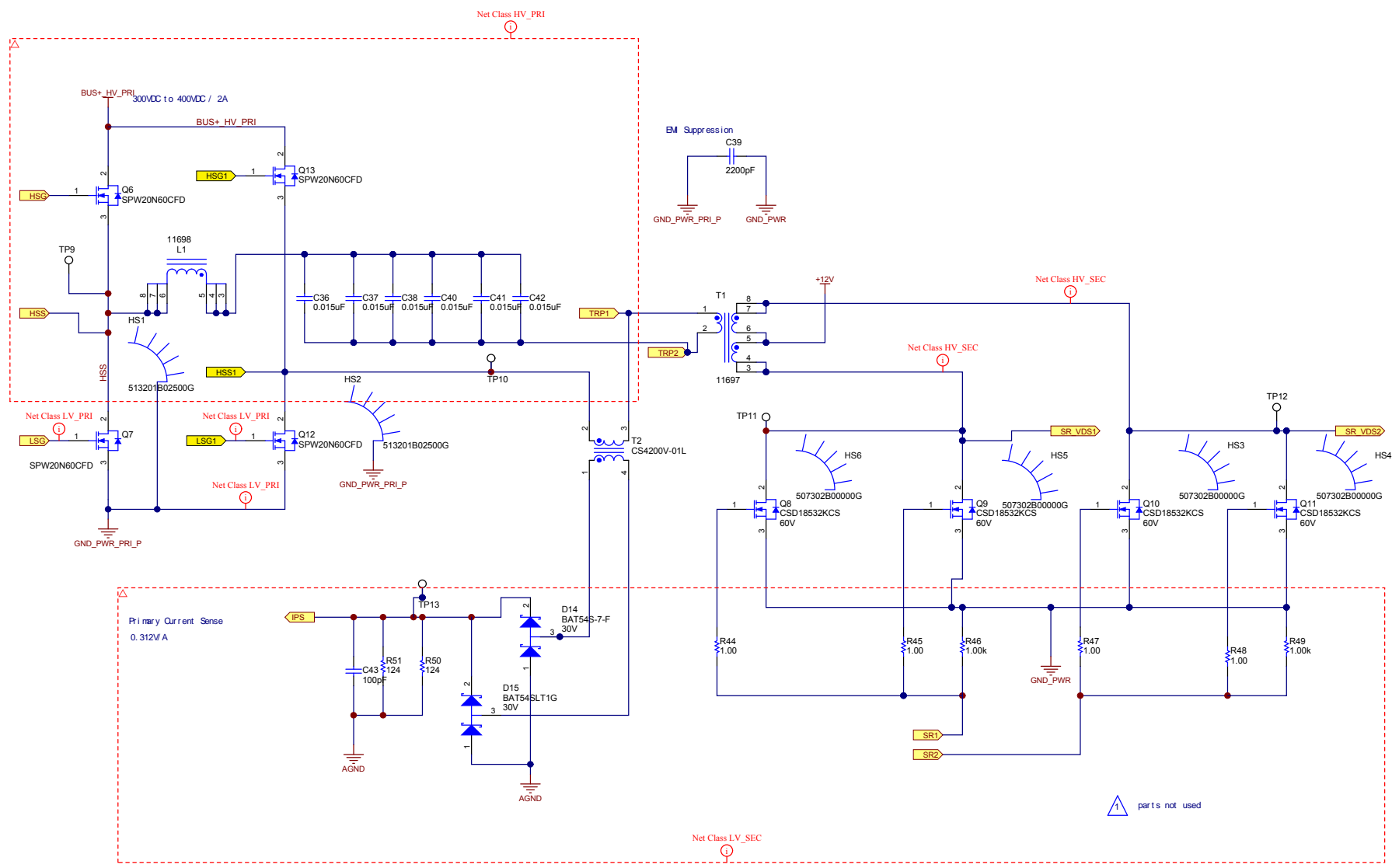
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SVN Rev: Not in version control	Assembly Variant: Variant name not interpreted	Project Title: TIDA-00381	Sheet Title:
Drawn By:	File: OUTPUT_SchDoc	Sheet: 8 of 8	Size: B
Engineer: B.M./H.H./Y.Q.	Contact: http://www.ti.com/support	http://www.ti.com	©Texas Instruments Copyright Year



Power Stage



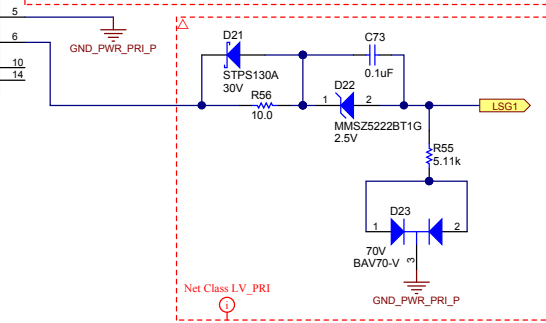
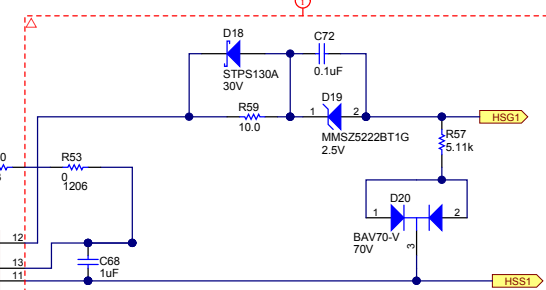
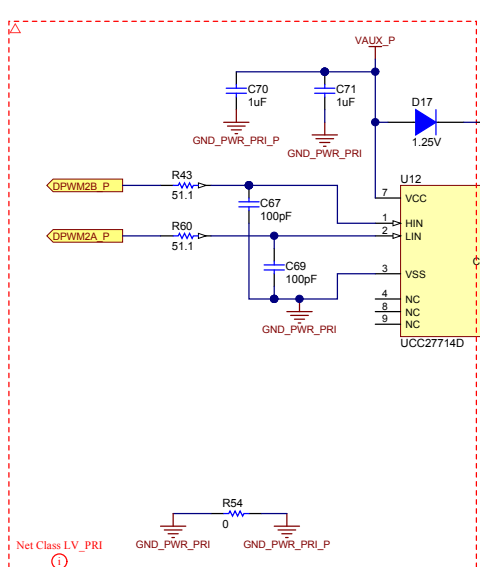
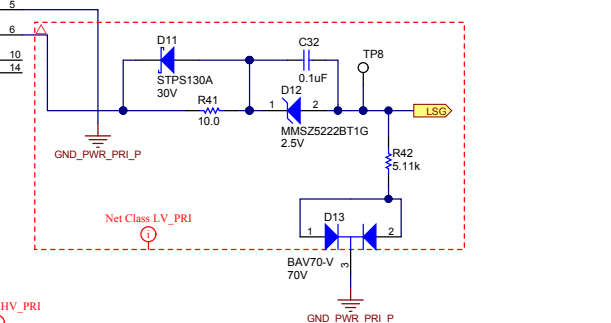
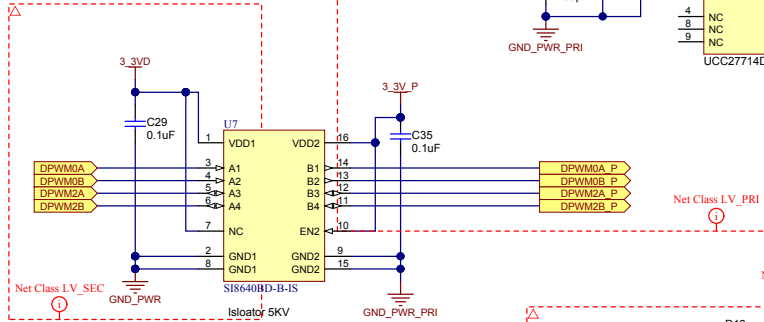
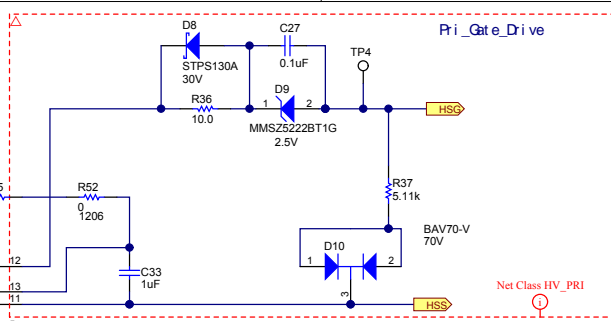
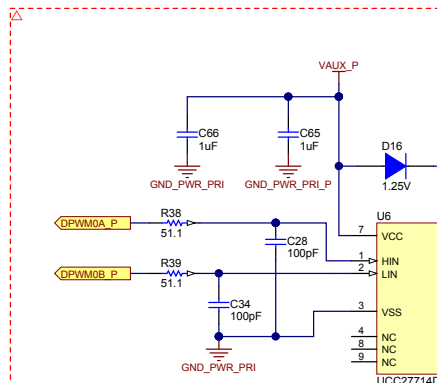
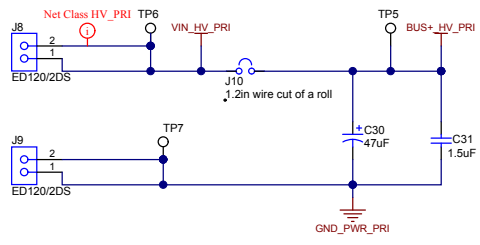
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Drawn By: Engineer: B.M./H.H./Y.Q.		Sheet Title:	
		Assembly Variant: Variant name not interpreted	Sheet: 6 of 8
		File: POWER_STAGE_SchDoc	Size: B
		Contact: http://www.ti.com/support	



V_N = 350 to 400VDC; I_{lin max} = 1.5A



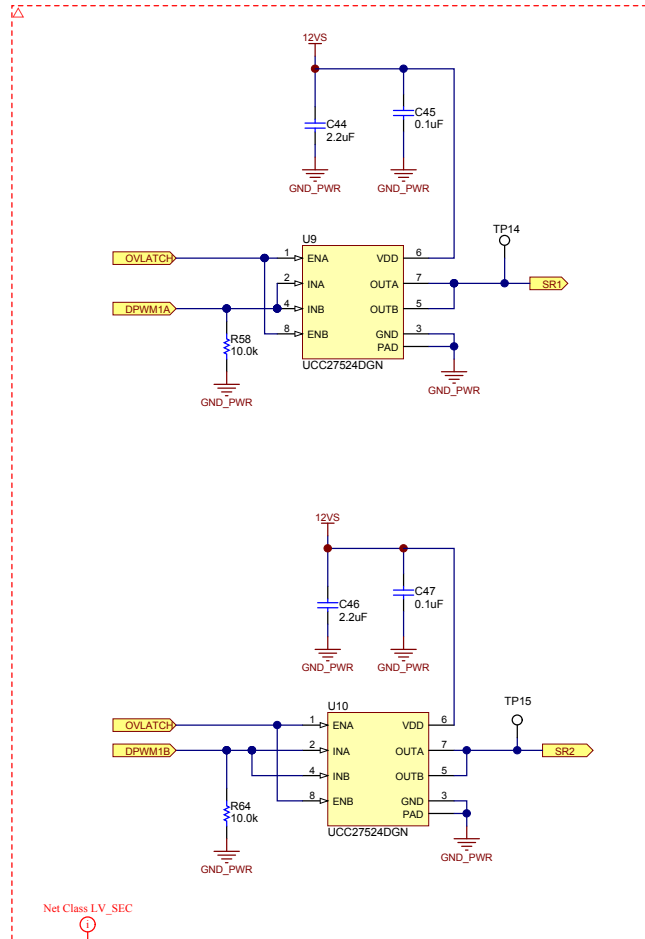
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SVN Rev: Net in version control		Project Title: TIDA-00381	
Drawn By: Engineer: B.M./H.H./Y.Q.		Sheet Title: Assembly Variant: Variant name not interpreted	Sheet: 5 of 8
		File: PRI_GATE_DRIVE_SchDoc	Size: B
		Contact: http://www.ti.com/support	http://www.ti.com

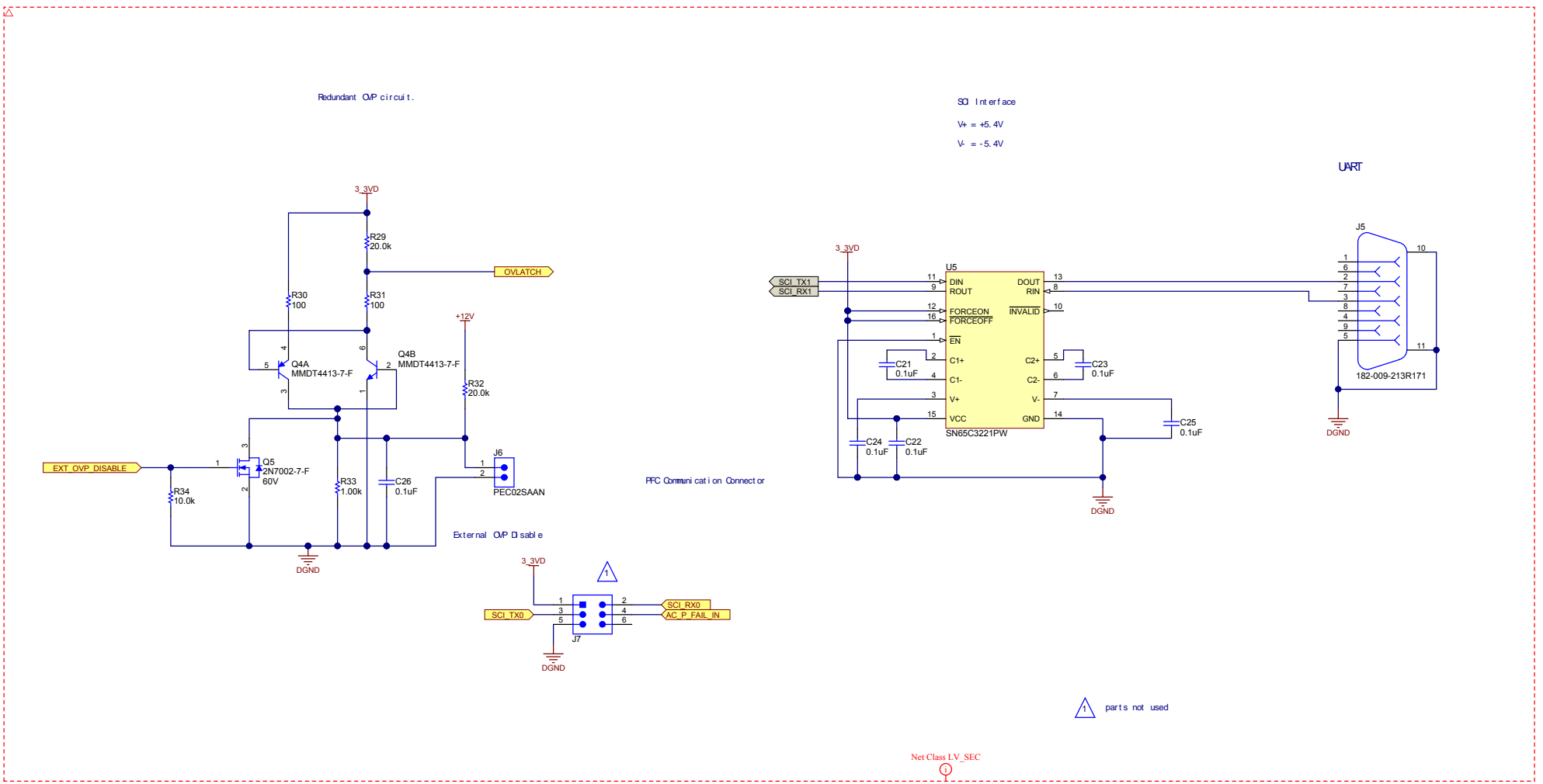


SR gate drivers



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Number: TIDA-00381	Rev: E1	Designed for: UCC27714	Mod. Date: 2015/2/13
SVN Rev: Not in version control		Project Title: TIDA-00381	
Drawn By: B.M./H.H./Y.Q.		Sheet Title:	
		Assembly Variant: Variant name not interpreted	Sheet: 7 of 8
		File: SR_CONTROL_SchDoc	Size: B
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FFC Communication Connector

External OVP Disable

SCI Interface
 V+ = +5.4V
 V- = -5.4V

UART

parts not used

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Drawn By:	Assembly Variant: Variant name not interpreted	Sheet: 4 of 8	
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