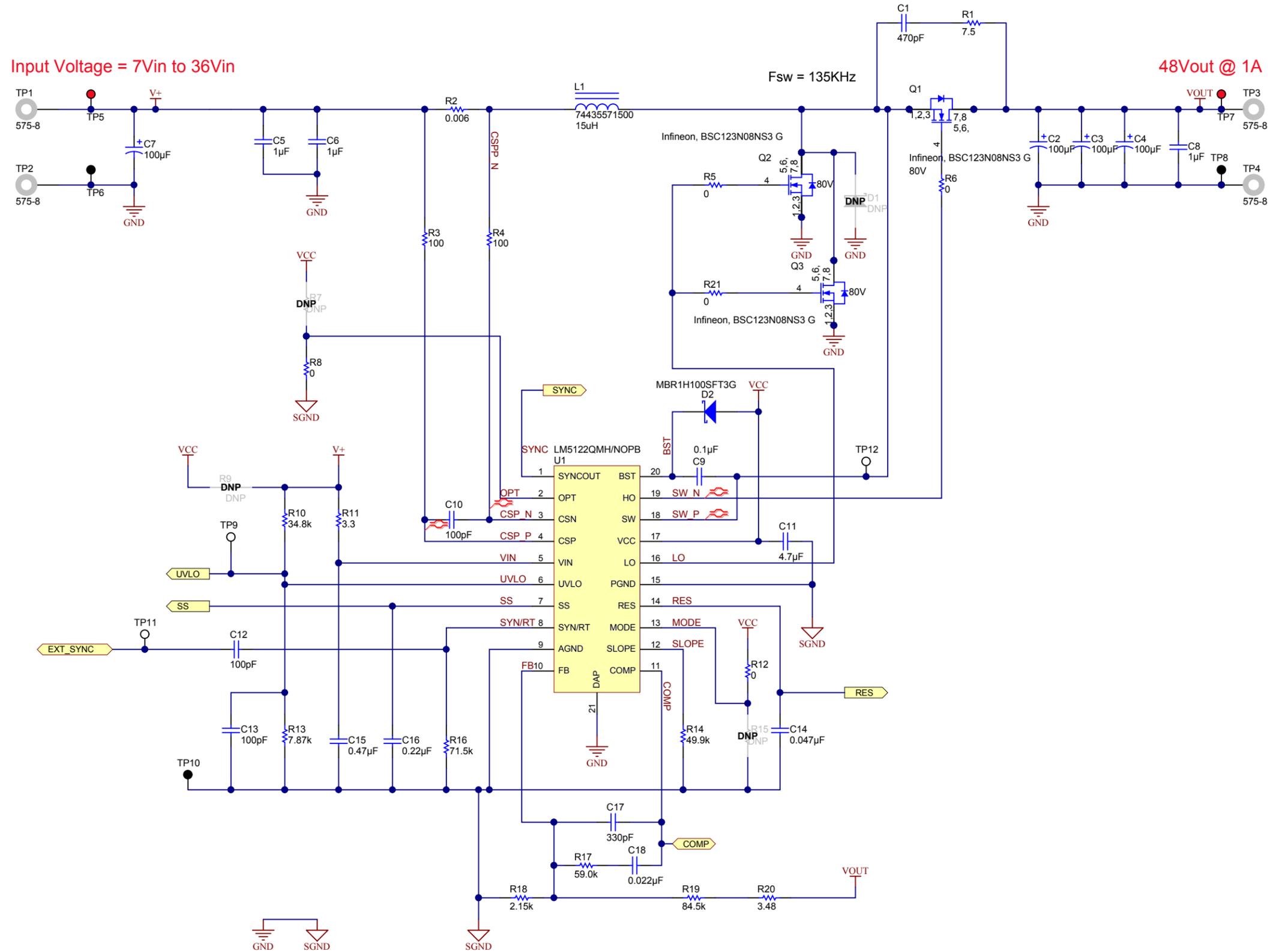


Note:
Built on PMP7896 RevB printed circuit board



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Designed for: Public Release		Mod. Date: 4/15/2015	
Project Title: LM5122 Sync Boost			
Number: PMP10503	Rev: 1	Sheet Title:	
SVN Rev: Version control disabled		Assembly Variant: 001	
Drawn By:	File: PMP10503 Schematic.SchDoc	Sheet 1 of 2	
Engineer: Hrag Kasparian	Contact: http://www.ti.com/support	Size: B	



H1 NY PMS 440 0025 PH H2 NY PMS 440 0025 PH H3 NY PMS 440 0025 PH H4 NY PMS 440 0025 PH

H5 1902C H6 1902C H7 1902C H8 1902C

DNP FID1 DNP FID2 DNP FID3

PCB LOGO
Texas Instruments

PCB: PMP7896 RevB
PMP7896 RevB

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Project Title: LM5122 Sync Boost			
Number: PMP10503		Rev: 1	Sheet Title:
SVN Rev: Version control disabled		Assembly Variant: 001	Sheet 2 of 2
Drawn By:		File: PMP10503_Hardware_ANSI-B_SchDoc	Size: B
Engineer: Hrag Kasparian		Contact: http://www.ti.com/support	



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