	A	I	~			*		–
	1	2	3			4		5
4								LayerNameMateri1Top Overlay22Top SolderSolder3Top LayerCopper4Dielectric1FR-45Signal Layer 1Copper6Dielectric 2FR-47Signal Layer 2Copper8Dielectric 3FR-49Bottom LayerCopper10Bottom SolderSolder11Bottom Overlay1
						140 OHM	DIFF, TOP SIDE, USING	12 MIL TRACES WITH 25 MIL SPACING 8.5MIL TRACES WITH 25 MIL SPACING BMIL TRACE
E	3							
		What What was not		Symbol Quantity ▼ 6 O 41 X 233	Finished Hole Size Plate 43.31mil (1.100mm) NPTH 7.87mil (0.200mm) PTH 8.00mil (0.203mm) PTH			
					10.00mil (0.254mm) PTH 33.46mil (0.850mm) PTH 40.00mil (1.016mm) PTH 43.31mil (1.100mm) PTH 59.06mil (1.500mm) PTH 62.99mil (1.600mm) PTH 63.00mil (1.600mm) PTH	Round Round Round Round Round Round		
C	☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆☆		Ľ	4 612 Total	125.98mil (3.200mm) PTH	Round		
		0000000 0000000 ⁰ 000000 ⁰ 000000 00000000						
C								
	ALL ARTWORK VIEWED FROM TOP SIDE BOARD LAYER NAME = DROMBED FROM TOP SIDE TID #: PLOT NAME = Fabrication Drawing GENERA	TIDA-00486		or any informat the specificati an implementati	ion contained therei ons, will be suitabl on. TI and/or its li	in. TI and/or its e for your applic censors do not wa	licensors do not warr ation or fit for any p arrant that the design	or completeness of this specification ant that this design will meet aarticular purpose, or will operate in is production worthy. You should stem functionality for your application.
	PLUT NHITE = Fabrication Drawing GENERH	2	3	compretery vall	uuve and test your (4	ton to contine the sy	stem functionality for your application.
					<u> </u>			

				<u> </u>				
				6				
rial	Th	ickness	Constant	Board Layer Stack				
lar Deci		40mi 1	3.5					
ler Resi Der		40m11 40mil	3.5					
High 1		.00mil	4.2	<u> ////////////////////////////////////</u>				
ber		4Omil						
High 1		.00mil	4.2					
ber High 1		fOmil .00mil	4.2					
per		40mil	7.2					
ler Resi			3.5					
;			DESIGN INFO	RMATION				
	MIN.	. TRACK W	IDTH: <u>8</u>	MIL				
	MIN.	. CLEARAN		 6MIL				
	MIN.	. VIA PAD	SIZE: 18	<u>3</u> MIL				
MIN				2MIL) EXTERNAL				
		C-D-275	CLASS 2 LEV	/EL C +/- <u>5</u> MIL, HOLES+/- <u>3</u> MIL				
RE		JN IULERA	NUES; METAL	·/ <u> </u>				
MAT	ERIAL:							
	FR-408 X FR-4 High Tg OTHER							
ТН	THICKNESS: 62 MIL (1.6mm) +/-10% X OTHER 53ML +/-10%							
	TOLERANCE: X ANSI IPC-6012 TYPE 3 CLASS 2							
			OTHER +/-	-				
во	W & TW	IST: X		5012 TYPE 3 CLASS 2				
			OTHER +/-					
DRILL	LING:							
		: X AS	S SHOWN	X NC_DRILL FILES				
			_					
	RD FINISH							
	SILKSCREEN: X TOP BOTTOM							
9	SILKSCREEN COLOR: X WHITE OTHER							
		RESIST COL						
SURF	SURFACE FINISH: X IMMERSION GOLD (ENIG) ENEPIG							
	IMM.	TIN/SILVER	OR EQUIV	OTHER				
ARR	AY/PANE	Ŀ 🗌	CUT AND TH	RIM PER MECH LAYER 1	c			
			N.C. ROUTE	X V. SCORE				
CER	TIFICATION			RKMANSHIP FOR ALL PCBS				
			et or excee -600F class	D THE REQUIREMENTS OF: $5 -> \square 1 \square 2 \square 3$				
		L 94V-0		OTHER <u>PER ORDER</u>				
	ADDITIONAL REQUIREMENTS: MICROSECTION: YES							
	BARE BOARD ELEC. TEST: NONE X REQUIRED PER ORDER							
		JRER'S UL:			\vdash			
		I	ia					
			TEXAS	S SUMENTS				
			- 1131R	UMEN 15				
	PROJECT TITLE:							
Is	olated	1 RS-48	5 To Wi-	Fi Bridge with 24 VAC	Pou			
DESIG	INED FOR:							
Pu	blic F	Release						
FILE N	NAME:				D			
	FLE NAME: ISE4033_ISO485_to_WiFi.PcbDoc							
ENGIN	EER:		I	AYOUT BY:				
M.	Кпар	П	Krypton Solutions/RS					
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