	1	2	3		4	5		6
A		LayerNameMateri1Top Overlay22Top LayerSolder3Top LayerCopper4Dielectric1FR-45Bottom SolderSolder6Bottom SolderSolder7Bottom Overlay1						A
							MIN. TRACK WIDTH:	NFORMATION 8 MIL 0.2 mm
в		ux and all contaminants. Use of no clean flux is not acceptable.					MIN. VIA PAD SIZE: MINIMUM ANNULAR RING 0.05mm PER IPC-D-275 CLASS 2 REGISTRATION TOLERANCES: ME HOLE SIZE TOLERANCE (UNLESS MATERIAL: FR-408 X FR-4 Hig THICKNESS: X 62 ML (1.6mm) TOLERANCE: X ANSI P OTHER BOW & TWIST: X ANSI P OTHER DRILLING:	24 ML n (2ML) EXTERNAL LEVEL C TAL +/- 5 ML, HOLES +/- 3 ML OTHERWISE SPECIFIED: +/- 3 ML n Tg OTHER
	225 = These assembles must comply with workmanning to be on the clean that is not acceptable. 224 = These assembles must comply with workmanning to be on the clean that is not acceptable.						REFERENCE: X AS SHOWN X NC_DRILL FILES PTH MIN COPPER THICKNESS: X 1MIL OTHER	
c							BOARD FINSH: X TOP X BOTTOM SUKSCREEN: X TOP X BOTTOM SUKSCREEN COLOR: X WHITE OTHER SOLDER RESIST COLOR: X GREEN OTHER X MATTE SEMI-GLOSS SURFACE FINISH: X MMERSION GOLD (ENG) ENEPIG MM. TIN/SLVER OR EQUIV OTHER	
	Not visue hold visue Data with the design will be visue with the visue withe visue withe visue with the visue with the visue withe visue wit							g Analog Hall Sensor DC LAYOUT BY: Wilson Zuo ALTUM DESORE VERSON: 14, 3, 19, 51423
	1	2	3		4	5		6

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