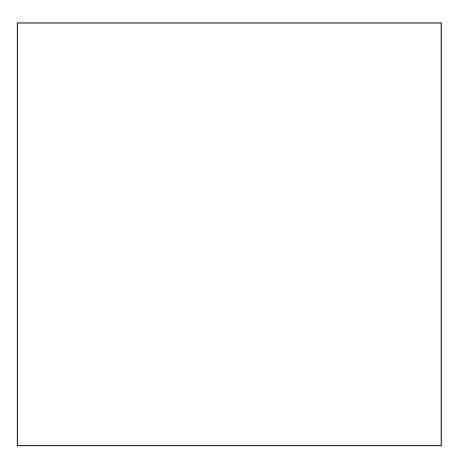


PCB VIEWED FROM TOP SIDE	BOARD #: PMP9227	REV: A	SVN REV:	Not In VersionControl
IGNORE ->				
PLOT NAME = Top Assembly Drawing	GENERATED : 12/3/2	2013 8:44:39	AM	TEXAS INSTRUMENTS



PCB VIEWED FROM BOTTOM SIDE	BOARD #: PMP9227	REV: A	SVN REV:	: Not In VersionControl
	Bottom Overlay <- 390NDI			
PLOT NAME = Bottom Assembly Drawing	GENERATED : 12/3/2	2013 8:44:40	AM	TEXAS INSTRUMENTS

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