1 2		3	4	5 6
A				NOTES: 1) Preliminary design only. 2) Matches PMP10412 REVA PWB. 3) Ref Des' 100 or larger are add-on parts. 4) REVB changes R4 and adds C16, C100 and R100.
		14		Output voltage: 28 V for lout < 130mA
B B B B B B B B B B B B B B B B B B B	μF	L1 33µH D1 DFLS140L U1 340QRTERQ1 FB 8 FB 8 FB 8 FB 8 FB 7 FB 12 FB 14 FB 12 FB 14 FB 14 F	10µF U2 INA139NA VIN+ -3 OUT VIN4 C10 0.033µF 20.0k R3 249 GND V+ 5 C11 0.1µF	
	1MHz	GND R6	D2 V* A MMDL914-TP T GND V* A T V* A T V V* A T V* A T V* A T V V* A T V V* A T V V* A T V V* A T V V V V V V V V V V V V V	
		R9 0	U3B 0.1µF 0.1	C16 0.1μF R100 3.01k
D			GND GND GND	C100 0.1µF
				Orderable: N/A Designed for: Public Release Mod. Date: 7/20/2015 TID #: Project Title: Project Title:
Texa	is Instruments and/or its licensors do not warrant ant that this design will meet the specifications, w	the accuracy or completeness of this specification or an ill be suitable for your application or fit for any particular	y information contained therein. Texas Instruments and/or its licensors do not purpose, or will operate in an implementation. Texas Instruments and/or its sign implementation to confirm the system functionality for your application.	Number: PMP11228 Rev: B Sheet Title:
1 2		worthy. You should completely validate and test your de 3	sign implementation to confirm the system functionality for your application. 4	Engineer: J Betten Contact: http://www.ti.com/support © Texas Instruments 2015 5 6



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