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5.2 Physical Layouts

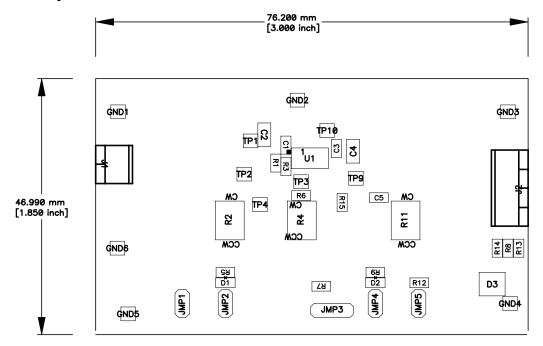


Figure 3. Assembly Layer

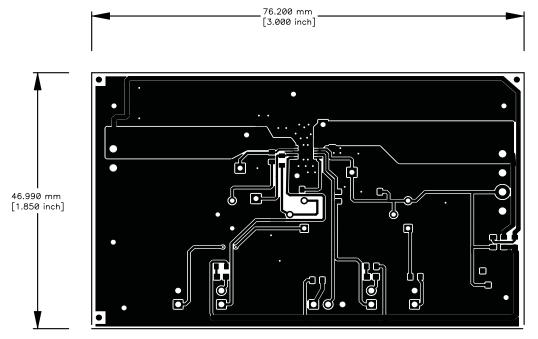


Figure 4. Top Layer

6





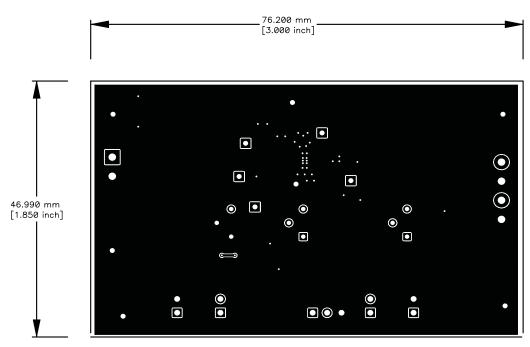


Figure 5. Bottom Layer

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