

6 Physical Layouts

This section contains the board layout and assembly drawings for the EVM.

6.1 Board Layout

Figure 5 shows the top assembly view of the EVM. Figure 6 shows the top etch layer of the EVM and Figure 7 shows the bottom etch layer.

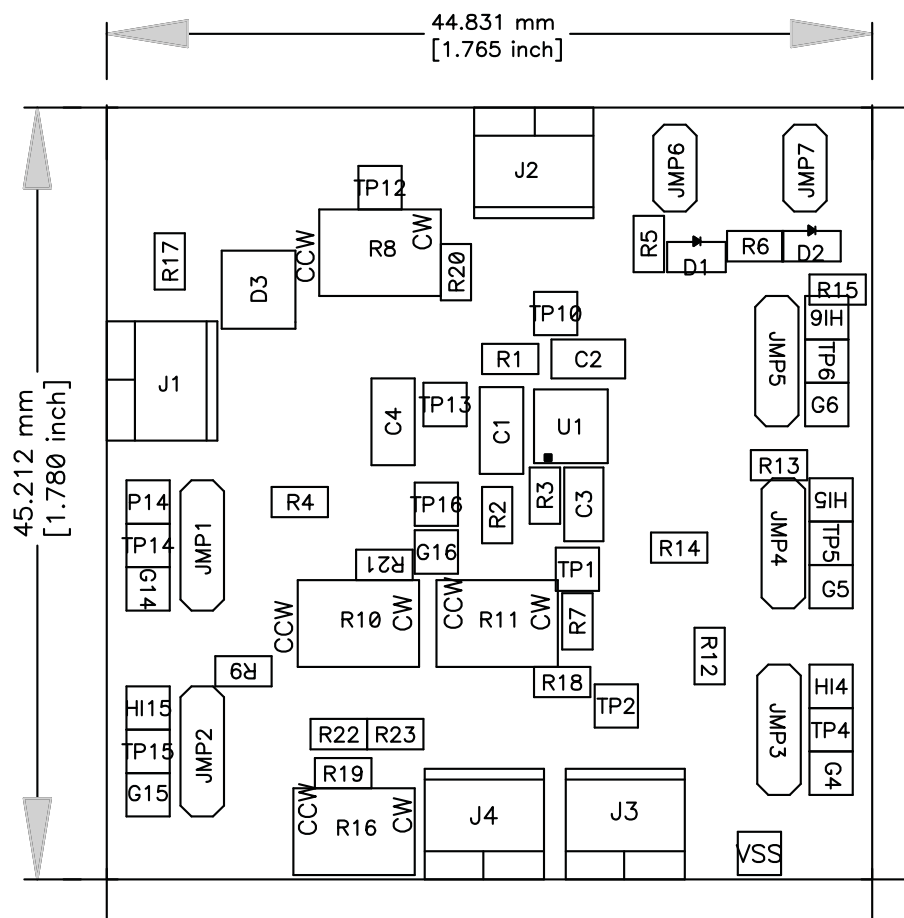


Figure 5. Top Assembly View

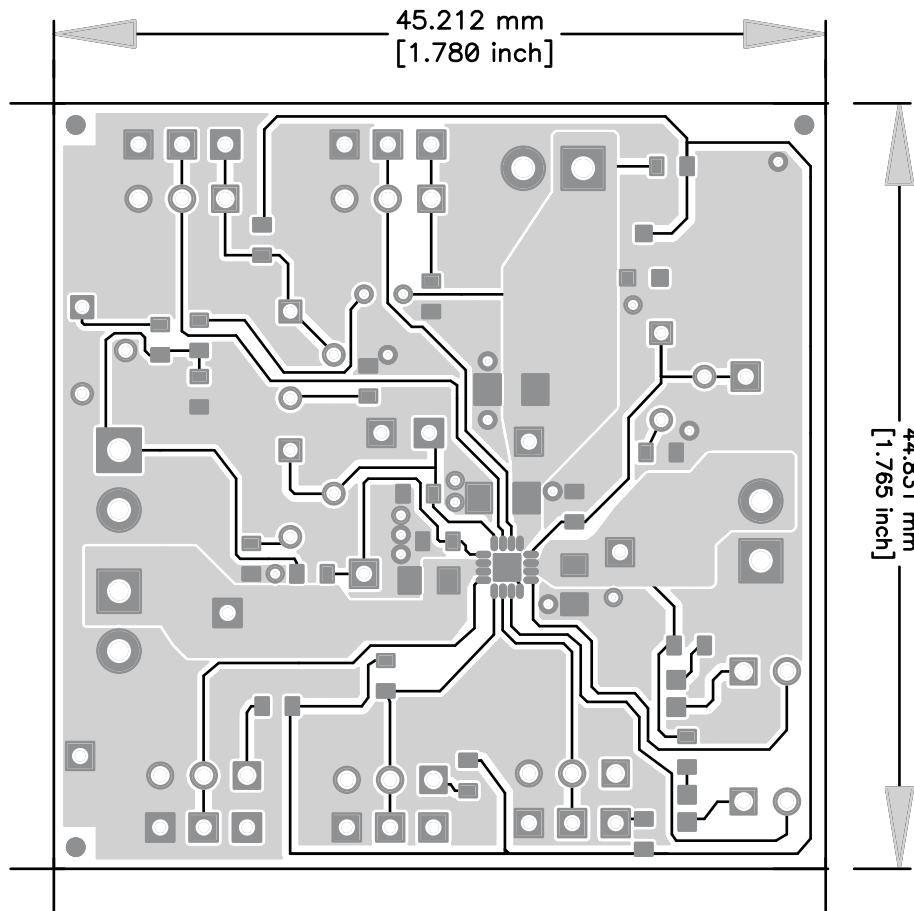


Figure 6. Board Layout – Top Etch Layer

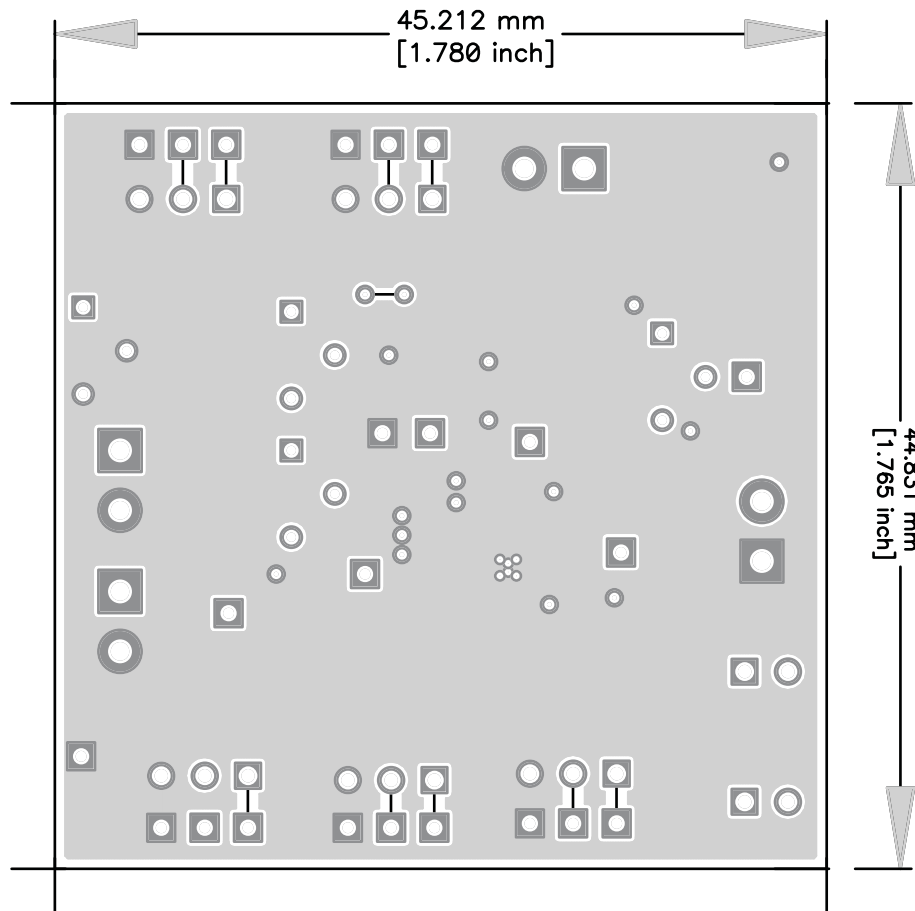


Figure 7. Board Layout – Bottom Etch Layer

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