

4.2 Board Layout

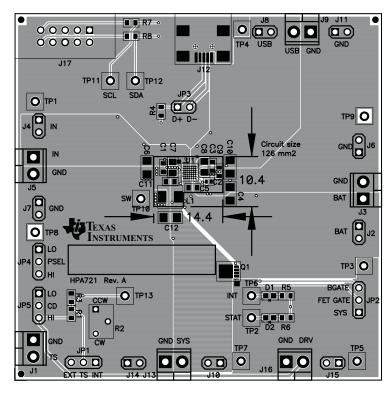


Figure 6. Top Assembly Layer

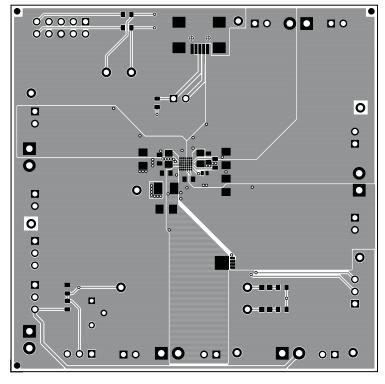


Figure 7. Top Layer



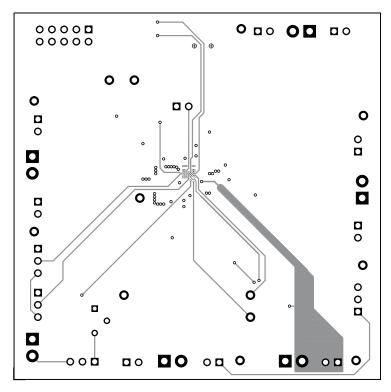


Figure 8. First Internal Layer

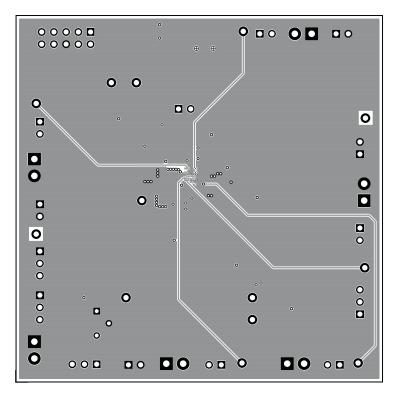


Figure 9. Second Internal Layer

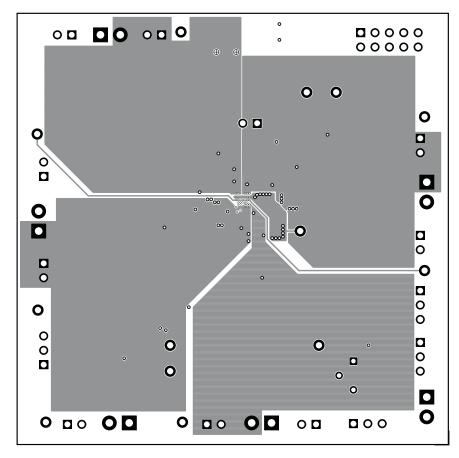


Figure 10. Bottom Layer

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