TI TECH DAYS

From start to finish: A product development roadmap for Sitara™ processors

Schuyler Patton

Sitara Processors



Overview

- Example Phases for a Product Development
- Evaluation Phase
- Board Development Phase
- Software Development Phase
- Production Phase / SW Lifecycle

2

Phases of Product Development



One Example of a Product Development Timeline





One Example of a Product Development Timeline

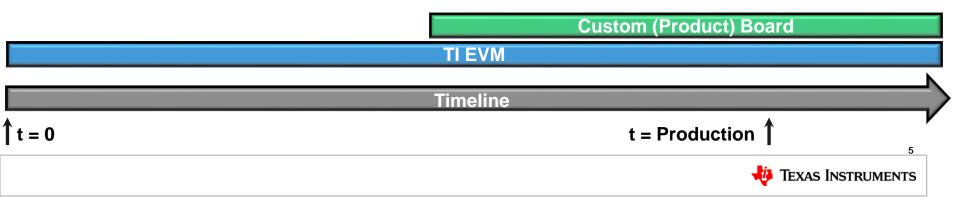
Sitara Processor Evaluation

Board Development

Software Development

Product Lifecyle

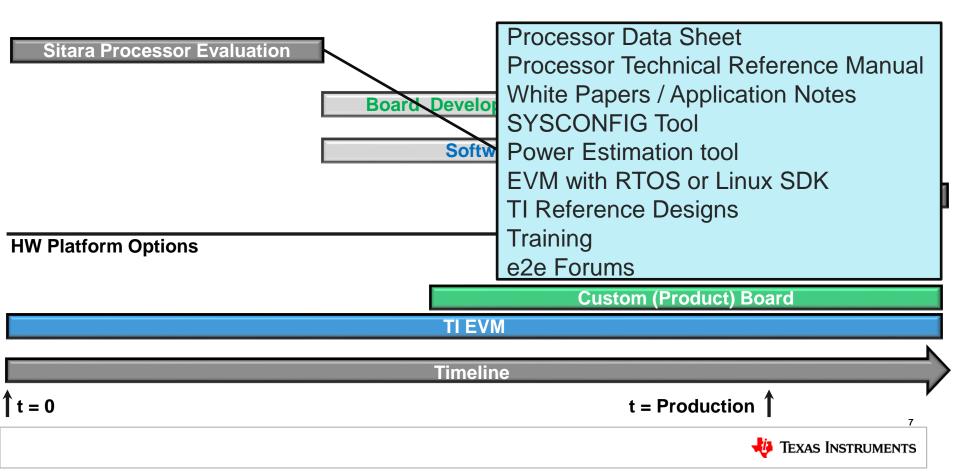
HW Platform Options



Processor Evaluation

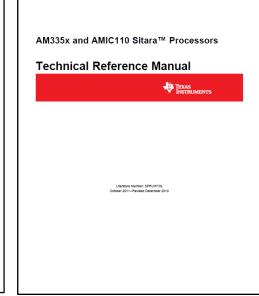


Product Development Timeline - Evaluation



Processor Evaluation – Datasheet & TRM

INSTRUMENTS	M3358, AM3357, AM3356, AM3354, AM3352, AM3351 SPRS117L-OCTOBER 2011-REVISED MARCH 2020					
AM335x Sitara™ Processors						
1 Device Overview 1.1 Features • Up to 1-GHz Sitara™ ARM [®] Cortex [®] -A8 32-Bit	PROFIBUS, PROFINET, EtherNet/IP ^{Tw} , and	A				
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- Datasheet
 - ARM processor frequencies supported
 - Available Peripherals
 - DDR Memory types supported
 - Power, Clocking Capabilities
- Technical Reference Manual (TRM)
 - Companion guide to Datasheet
 - details the integration, environment, functional description, programming models for each peripheral and subsystem in the device



Processor Evaluation – Technical Documentation

🌵 AM3358 da	ata she	et, product info 🗙 👆 AM	1335x Sitara™ Processors data: 🗙 🛛 👆 AM335x and AMIC110 Sitara™ Pr. 🗙 🛛 🏘 How to affordably add EtherNet/ 🗙 🛛 🕂	
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4		🕨 AM3358 🥑	ACTIVE	ta sheet Order now
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	Teo	chnical docum	nentation	
	*=	Top documentation for thi		
		Туре	Title	Date ↓↑
		All	Filter title by keyword	
	*	Datasheet	AM335x Sitara [™] Processors datasheet (Rev. L)	Nov. 15, 2019
	*	Errata	AM335x Sitara Processors Silicon Errata (Revs 2.1, 2.0, 1.0) (Rev. I)	Jan. 03, 2017
	*	User guide	AM335x and AMIC110 Sitara* Processors Technical Reference Manual (Rev. Q)	Dec. 13, 2019
		White paper	Evolving Semiconductor Technologies for Modern Telehealth Applications	Oct. 26, 2020
		Technical articles	How to affordably add EtherNet/IP, EtherCAT and PROFINET to an autonomous factory	Aug. 24, 2020
		White paper	EtherNet/IP on Ti's Sitara AM335x Processors (Rev. D)	Jul. 28, 2020
		E-book	Ein Techniker-Leitfaden für Industrieroboter-Designs	Mar. 25, 2020
		Application note	PRU-ICSS Feature Comparison (Rev. D)	Mar. 09, 2020
		User guide	Powering the AM335x, AM437x, and AM438x with TPS65218D0 (Rev. B)	Feb. 27, 2020
		E-book	E-book: An engineer's guide to industrial robot designs	Feb. 12, 2020
		Application note	AM335x Schematic Checklist (Rev. A)	Dec. 19, 2019
		Technical articles	Designing smarter remote terminal units for microgrids	Oct. 02, 2019
		Application note	AM335x EMIF Tools	Sep. 20, 2019
		Application note	AM335x PMIC Selection Guide (Rev. A)	Sep. 19, 2019
		Application note	Programmable Logic Controllers – Security Threats and Solutions	Sep. 13, 2019
		More literature	Sitara™ processors + WiLink™ 8 Wi-Fi® + Bluetooth® combo connectivity (Rev. A)	Jul. 30, 2019
		White paper	Power optimization techniques for energy-efficient systems (Rev. A)	Jun. 28, 2019
-		Technical articles	Security versus functional safety: a view from the Processor Software Development Kit	May 31, 2019

- White papers
 - Power Optimization Techniques
 - Sitara Processor Security
- Application Notes
 - Hardware Design Guide
 - Schematic Checklist
 - EMIF Tool
- E-Books
- Technical Articles



Processor Evaluation - SYSCONFIG Tool

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	UART_PRUSS1	Ð						2 00000000000000000				
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- Software tool that provides a Graphical User Interface for configuring pin multiplexing settings, resolving conflicts
- Perform "what-if" on possible pin mux configurations for a particular application



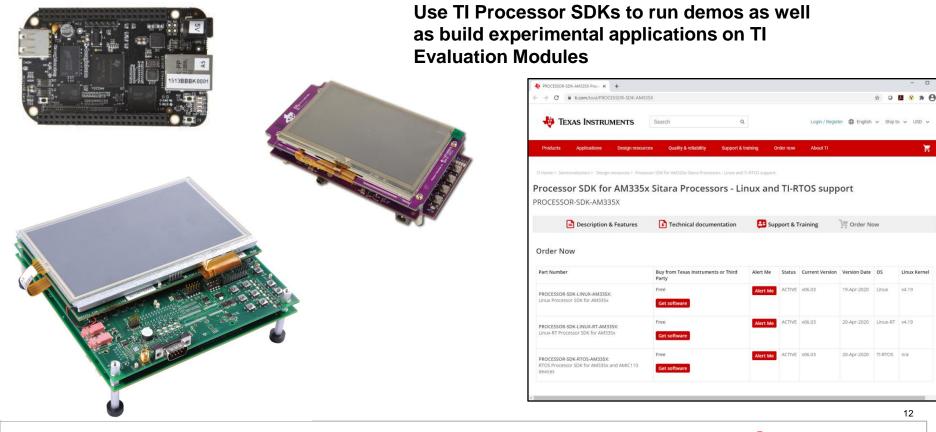
Processor Evaluation - TI Reference Designs

😓 AM3358 data sheet, p 🗙 👍 TI Refe	erence Desig	ns 🛙 🗙 🚸 AM335x Sitara [™] Proce 🗙 🛛 🚸 AM335x and AMIC110 🗙 📔 G texas instruments tech	🗙 🏘 TMS320x2833x, TM	s: × +
→ C 🔒 ti.com/reference-de	esigns/inde	ex.html#search?keyword=AM3358		☆ ♀ ⊿ ♥ ★ 8
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		Show quick search		
Hide filters Reset		27 matching designs out of 4174 total designs		Email
Market Automotive	^	Design title Filter by keyword(s) Q	Market	Product
Communications equipment Enterprise systems		TIDEP-01013 - Gesture controlled HMI with mmWave sensors and Sitara [™] processors reference design	Industrial	Sensors Processors
Industrial Personal electronics		TIDA-010032 - Universal data concentrator reference design supporting Ethernet, 6LoWPAN RF mesh and more	Industrial	Wireless connectivity Switches & multiplexers Processors Power management
Product Find power reference designs by para	∼ ameter			Microcontrollers (MCU) Logic Isolation Interface Data converters Amplifiers
Vin (V) (Min)	~	TIDEP-01005 - Human Machine Interface (HMI) for Smart Thermostat Reference Design	Industrial	Processors
Vin (V) (Max) Isolated/Non-Isolated Input Type Vout (V) (Nom)	* * *	TIDA-01568 - 12mm x 12mm, 5-Rail Power Sequencing for Application Processors Reference Design	Industrial Communications equipment	Wireless connectivity Processors Power management Logic Interface
lout (A) (Max) Output Power (W)	* *	TIDA-01555 - Flexible Interface (PRU-ICSS) Reference Design for Simultaneous, Coherent DAQ Using Multiple ADCs	Industrial	Processors Bower management

- Leverage TI technology to solve your system-level challenges
- Some designs use TI Evaluation Modules
- Schematics and other documentation provided such as PCB layouts, Bill of Materials (BOM) and User Guide

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Product Timeline - Evaluation





Product Evaluation Summary

- Use the Datasheet and The Technical Reference Manual to determine processor compatibility
- Review the available Application Notes, Whitepapers and other technical documentation available on ti.com
- Use the SYSCONFIG tool to evaluate possible pin mux outputs to determine "what if" processor configurations
- Review TI Reference Designs for design elements to be used in a new product
- Experiment with TI Evaluation Modules and the RTOS and Linux Software Development Kits to evaluate processor capabilities



Board Development



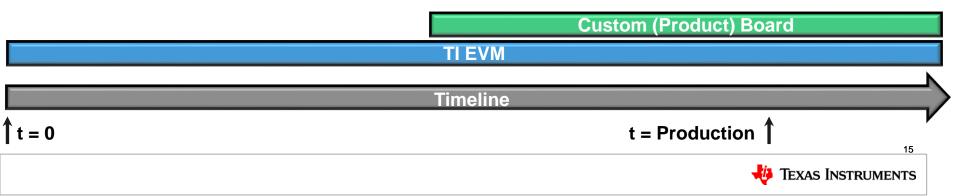
Sitara Processor Evaluation

Board Development

Software Development

Product Lifecyle

HW Platform Options



- The purpose of this application report is to walk hardware designers through the various stages of designing a board on this platform.
- Block diagram of suggested of hardware design flow for a board design
- Use Reference material provided by TI in the Technical Documentation tab of the Processor Product folder

-	EXAS NSTRUMENTS	Application Repo SPRABU5-May 201
		5x Hardware Design Guid
		Catalog Processor
	ABSTRA	ACT
The	purpose of this application report is to walk ha gning a board on this platform.	rdware designers through the various stages
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3		
4	Confirming Pin Multiplexing Compatibility	
5		
6		
8		
9	Creating the Schematics	
10		
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SPRABU5-N	lay 2019 mentation Feedback	AM335x Hardware Design Guide



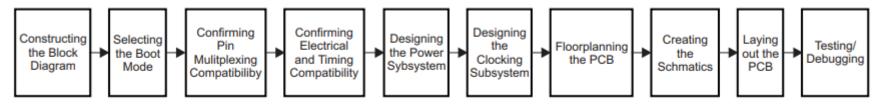
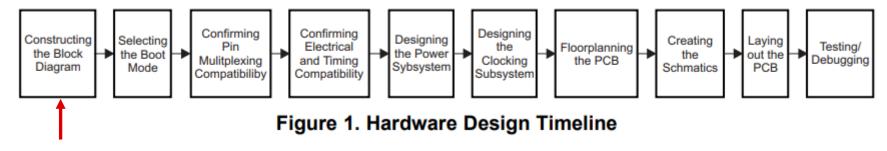


Figure 1. Hardware Design Timeline



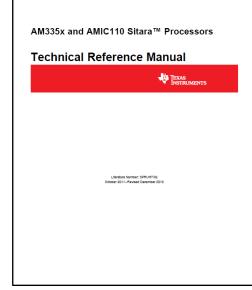


Processor Datasheet Technical Reference manual



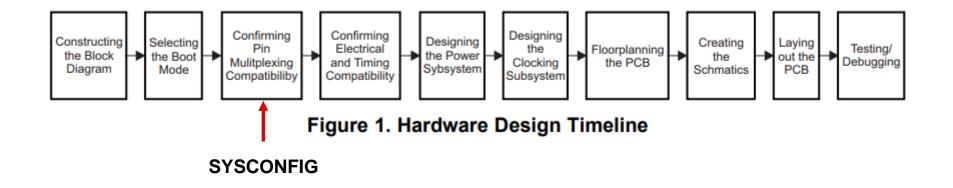
Board Development – Datasheet & TRM

INSTRUMENTS	SPRS717L -OCTOBER 2011-REVISED MARCH 2020							
AM335x Sitara™ Processors								
AM335x Sitrar ¹¹ 1 Device Overview 1.1 Features 1.1 Features 1.1 Features 1.2 Fea	Processors							
Locate Addresses of Data Errors from Syndrome Polynomials Generated Using a BCH Algorithm – Supports 4-, 8-, and 16-Bit per 512-Byte Block Error Location Based on BCH Algorithms	 Integrated 15- to 35-MH2 High-Frequency Oscillator Used to Generate a Reference Clock for Various System and Peripheral Clocks Supports Individual Clock Enable and Disable Control for Subsystems and Peripherata to Facilitate Reduced Power Consumption 							
 Programmable Real-Time Unit Subsystem and Industrial Communication Subsystem (PRU-ICSS) Supports Protocols such as EtherCAT[®]. 	 Five ADPLLs to Generate System Clocks (MPU Subsystem, DDR Interface, USB and 							



- Datasheet
 - Designing
 - Device interconnections
 - Electrical & Timing requirements
 - Pin Muxing
 - Power
 - DDR Memory Interfacing
- Technical Reference Manual (TRM)
 - Boot Modes
 - Peripheral Clocking and operations
 - Control Module, register descriptions



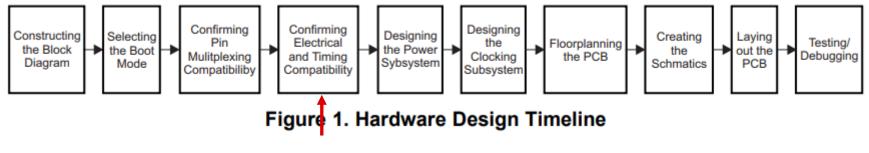




Board Development - SYSCONFIG Tool

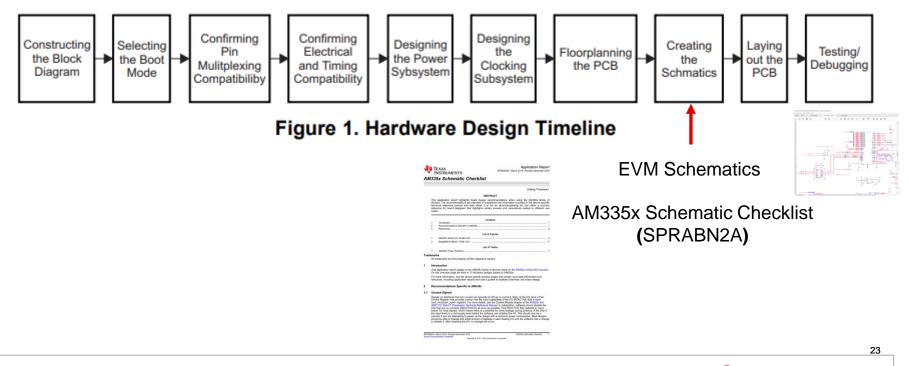
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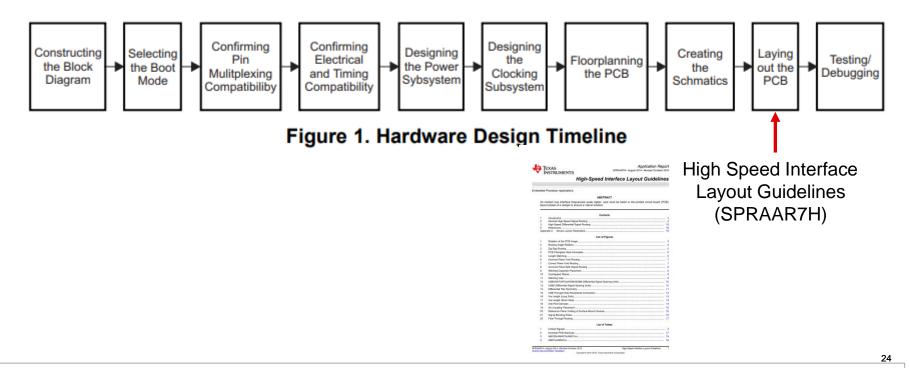


IBIS Models for Timing Analysis











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*	Top documentation for ti	his product selected by TI	
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*	Datasheet	AM335x Sitara [™] Processors datasheet (Rev. L)	Nov. 15, 2019
*	Errata	AM335x Sitara Processors Silicon Errata (Revs 2.1, 2.0, 1.0) (Rev. I)	Jan. 03, 2017
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	More literature	Sitara™ processors + WiLink™ 8 Wi-Fi® + Bluetooth® combo connectivity (Rev. A)	Jul. 30, 2019
	White paper	Power optimization techniques for energy-efficient systems (Rev. A)	Jun. 28, 2019

 The Technical documentation Tab of the product folder contains the list of available documentation for a Processor.



Board Development Summary

- Follow the steps shown in the Hardware Design guide
- Leverage the documentation provided in the processor product folder
- Use the Datasheet and TRM to create system block diagram
- The tools for SYSCONFIG, EMIF tool assists with determining pin mux configuration



Software Development



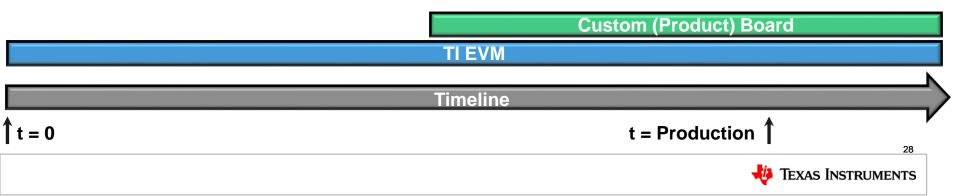
Sitara Processor Evaluation

Board Development

Software Development

Product Lifecyle

HW Platform Options

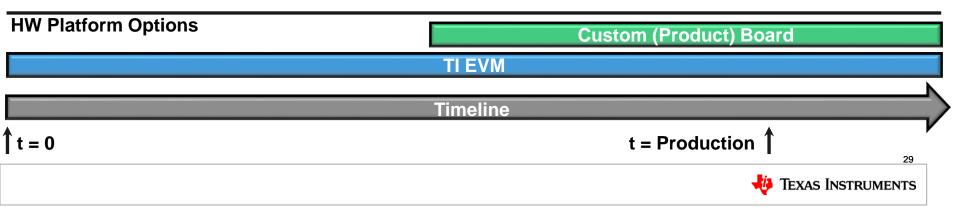


Software Development

SW Development/Testing/Release

Board Port Development – U-Boot/Linux

Board Port Development – RTOS

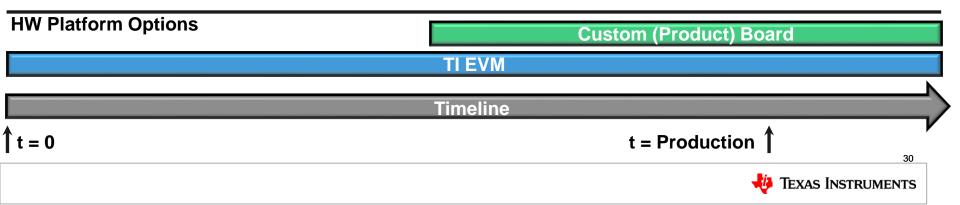


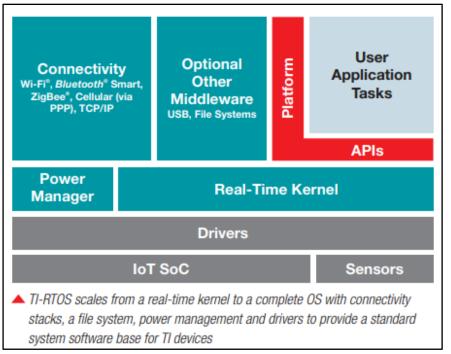
Software Development

SW Development/Testing/Release

Processors SDK RTOS

Processors SDK Linux





sprt646a.pdf

- Robust real-time TI-RTOS kernel including TCP/IP networking stack
- Posix thread-compatible API layer available
- Driver libraries that can be used with TI-RTOS or without a kernel
- Free and available as open source



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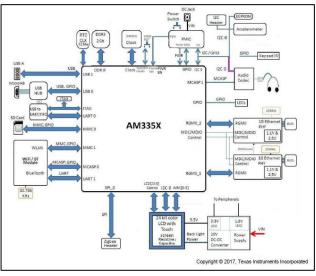
Foundational	Components (more in	formation on each piece of	the distribution)
U-Boot	Boot Monitor	Kernel	Filesystem
Tools	OpenCL	OpenCV	Graphics & Display
Multimedia	Examples, Demos	PRU-ICSS / PRU_ICSSG	Virtualization
IPC	OpenVX	CMEM	Machine Learning
ATF	OPTEE		

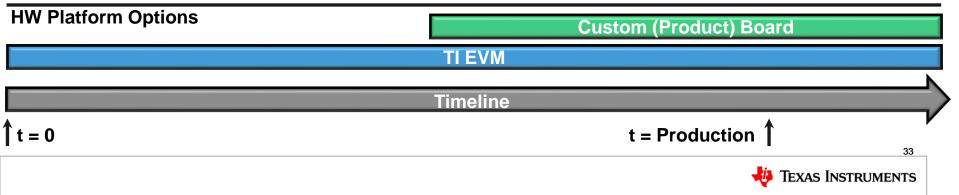
- Updated to the latest Long Term support (LTS) Linux kernel, boot loader and Yocto file system on an annual basis
- U-Boot community boot loader
- Robust, commercial-grade Linaro® GNU compiler collection (GCC) toolchain
- Yocto Project[™] OE Core compatible file systems support enables tailored Linux application support
- RT-Linux releases include a fully pre-emptible kernel for real-time applications

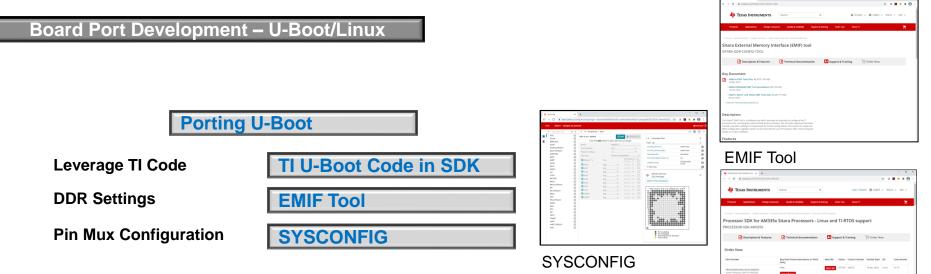


Board Port Development – U-Boot/Linux

- Porting Bring up U-Boot/Linux on Custom Hardware
 - Processor SDK Linux has the processor initialization, driver support for Linux and U-Boot
 - Leveraging TI EVM code makes porting strait forward
 - Use the EMIF and SYSCONFIG tools to assist with the port effort



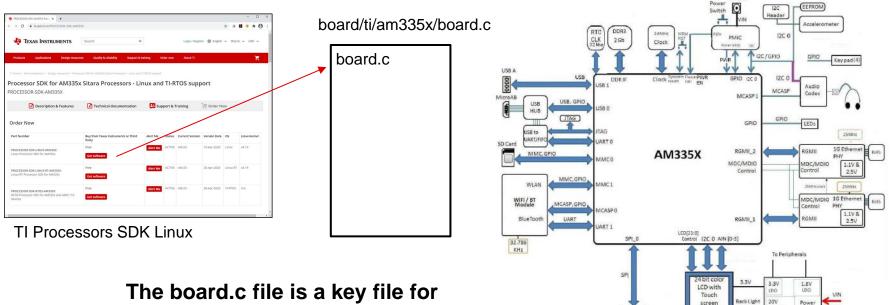






TI Processors SDK Linux

Product Timeline – U-Boot Board Port



porting U-Boot to a custom board

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Supply

DC-DC

Converter

Power

DCJack

screen

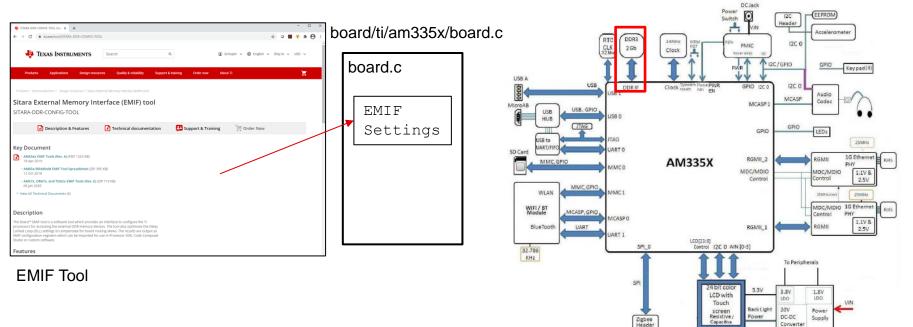
Resistive /

Capacitive

Zigbee Header



Product Timeline – U-Boot Board Port

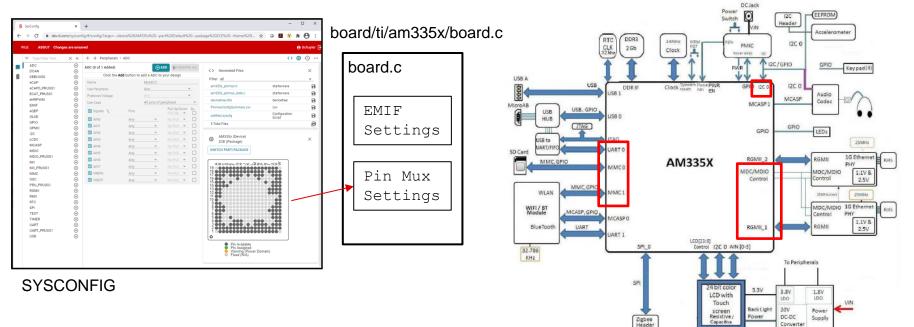


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Converter



Product Timeline – U-Boot Board Port



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Board Port Development – U-Boot/Linux

Porting Linux

Leverage TI Code

Pin Mux Configuration

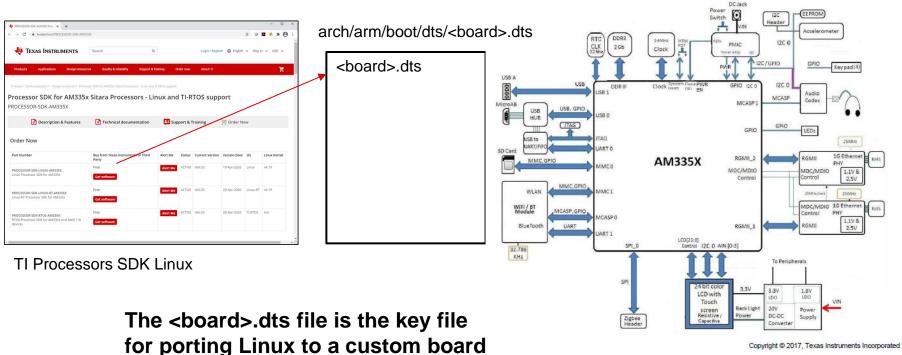
TI DTS Code

SYSCONFIG

AM335X	
	2 24/0 2/2 2 24/0 2/2 2 25/0

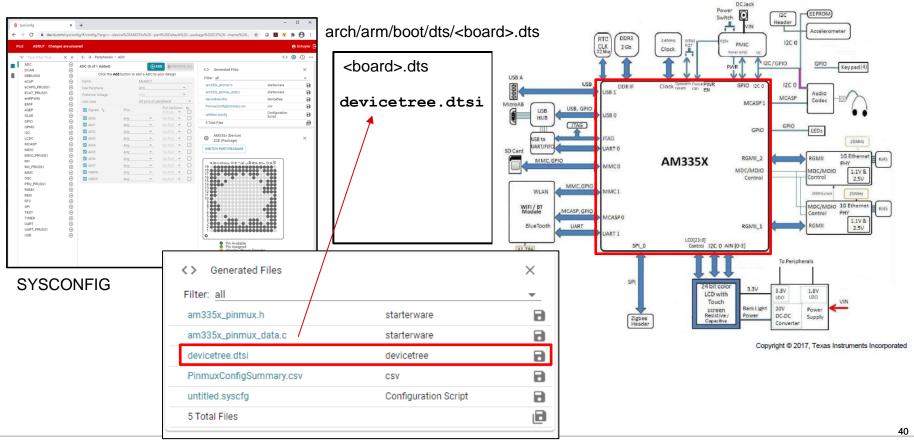


Product Timeline – Linux Board Port





Product Timeline – Linux Board Port





Software Development Summary

- TI Provides an RTOS and Linux SDK operating system for each processor
- The SDKs provide the starting point for application development
- The SYSCONFIG and EMIF Tools should be used to accelerate porting operating systems to a new board



Production and Lifecycle



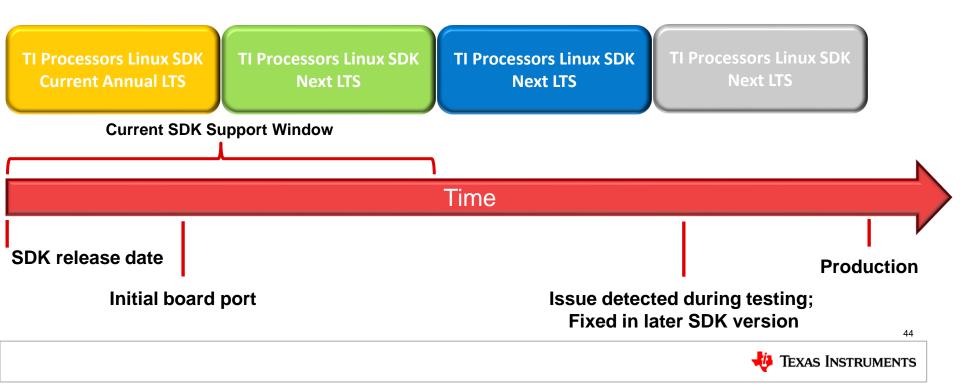
Product Timeline – Production UniFlash

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🌵 3.1. Board Support — Processor S	× +					_		
\leftrightarrow \rightarrow C $$ software-dl.ti	i.com/processor-sdk-rtos/esd/docs/latest/rtos/index_board.html#uniflash 🔍 🖈 🔍 🖪 🔇 🖈 🕘	:	Products Applications Design resources Qua	lity & reliability Support	& training Order now	About TI		
# Processor SDK RTOS	Other failures blink blink blink blink	*	TI Home > Semiconductors > Design resources > UniFlash stand-alone flas	sh tool for microcontrollers, Sitar	a** processors and SimpleLink**			
06_03_00_106	3.5. Board Utils		UniFlash stand-alone flash tool for SimpleLink™	r microcontrol	lers, Sitara™; pr	ocess	ors and	
1. Overview	3.5.1. Uniflash		UNIFLASH					
2. Release Specific	3.5.1.1. Introduction		Description & Features	nical documentation	🚨 Support & Trainir	ng	े्स Order Nov	,
B 3.1. Board Support	Uniflash is an Unified Flashing tool which provides utilities for flashing the application software images to non-removable flash devices on TI hardware platforms.							
⊞ 3.2. Diagnostics			Order Now					
3.3. Diagnostics Execution	Uniflash for TI processors platform includes two components						-	
3.4. Power-On Self Test	Flash Programmer		Part Number		its or third Party Alert Me		Current version	version Date
😑 3.5. Board Utils	Host utility		UNIFLASH-CLOUD: UniFlash cloud development on TI Resource Explorer	Start development		ACTIVE		
🛙 3.5.1. Uniflash				Free	Alert Me	ACTIVE	v6.1.0	20-IUL-2020
3.5.2. UART Apploader	Flash porgrammer runs on target platform which takes care of receiving the images from Uniflash host utility and programming them onto flash devices. Flash programmer communicates with Uniflash host utility over the UART Interface.		UNIFLASH: UniFlash for most TI microcontrollers (MCUs) and mmWave sensors			•		
4. Foundational Components	communicates white ominant max during over the orient interfaces			Get software				
5. DSP Software	Flash programmer which is part of the Uniflash release can be found at - " <uniflash root="">/processors/FlashWriter/<board name="">"</board></uniflash>		UNIFLASH_PREVIOUS:	Free			v3.4.1	16 Feb 2016
6. Device Drivers			UniFlash previous versions with support for CC3200 and CC3100	Get software				
7. PRU-ICSS Firmware	Host utility runs on host machine which provides Command-line Interface (CLI) to communicate with flash programmer. Windows and Linux are the supported OS platforms for running Uniflash host utility. Host utility uses UART or ITAG interface to download the flash programmer to the target platform. All data transfers between Uniflash host utility and							
8. Compilers	Flash programmer happens over UART Interface.							
9. Examples and Demonstrations					Support & Statuting Odder now About 11 websitestic, Staturing Training Training Instation Support & Training Training Statut Current Version Date Senset Active Active Salau Active Active Salau Support Active Active Salau Support Active Active Salau Support			
10. How to Guides	Refer to Uniflash Documentation for more details on Uniflash tool.							
11. Frequently Asked Questions	3.5.1.2. Supported Platforms						Sessors and Jess Current Version Version Date VE Version 20.jul.2020	
12. Documentation Tarball	5.5.1.2. Supported Flattorns							
	Below table shows the platforms supported by Uniflash and flash devices supported on each platform. Download mode indicates the mode of communication for downloading flash programmer to target platform.							

			FLASH DEVICE					DOWNLOAD MODE					
								JTAG					
soc	SOC Core	PLATFORM	SPI	QSPI	OSPI	EMMC	UART	Uniflash CLI	Manual				
		AM335x GP EVM	х				х	х					
AM335x	Cortex-A8	AM335x ICEv2	х					х					
		AMIC110 ICE	х				х	х					
AM437x	Cortex-A9	AM437x IDK		х				х					
AM571x	Cortex-A15	AM571x IDK		х				х					
AM572x	Cortex-A15	AM572x IDK		х				х					
AM574x	Cortex-A15	AM574x IDK		х				х					



Product Timeline – Lifecycle





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