

AMC3301, AMC3302, and AMC3330 Evaluation Module



ABSTRACT

This user's guide describes the characteristics, operation, and use of the AMC3301EVM, AMC3302EVM, and the AMC3330EVM. The evaluation module (EVM) is an evaluation and development kit for evaluating the [AMC3301](#), [AMC3302](#), or the [AMC3330](#). These devices are precision isolation amplifiers with an integrated DC/DC converter that powers the high side of the amplifier. A complete circuit description as well as schematic diagram and bill of materials are included in this document.

Throughout this document, the abbreviation *EVM* and the term *evaluation module* are synonymous with the AMC3301EVM, AMC3302EVM, and AMC3330EVM.

The following related documentation is available through the Texas Instruments website at www.ti.com.

Related Documentation

Device	Literature Number
AMC3301	SBAS917
AMC3302	SBASA11
AMC3330	SBASA35

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Trademarks

All other trademarks are the property of their respective owners.

1 EVM Overview

1.1 Features

This EVM supports the following features:

- Full-featured evaluation board for the AMC3301, AMC3302, or AMC3330 single-channel precision isolation amplifier with integrated DC/DC converter
- Screw terminals for easy access to analog inputs and outputs

1.2 Introduction

The AMC3301, AMC3302, and AMC3330 devices are precision isolation amplifiers with an output separated from the input circuitry by a silicon dioxide (SiO₂) barrier that is highly resistant to magnetic interference. This barrier is certified to provide basic galvanic isolation of up to 7000 V_{PEAK} according to UL1577 and IEC60747-5-2 specifications.

For use in high-resolution measurement applications, the inputs of the AMC3301, AMC3302, and AMC3330 are optimized for direct connection to shunt resistors or other low-level signal sources.

2 Analog Interface

The analog inputs to the AMC3301, AMC3302, and AMC3330 are routed from the two-wire screw terminal at J2. These screw terminals provide access to the inverting and non-inverting inputs of the AMC3301, AMC3302, or the AMCC3330 device installed at U1.

2.1 Analog Inputs

The analog inputs to the printed-circuit board (PCB) consists of an RC filter circuit and coupled inductor. By default, R2 and R4 on the analog input are populated as 10-Ω resistors. Capacitor C8 is populated with a 8.2-nF capacitor. [Figure 2-1](#) shows an example input circuit for the AMC3301, AMC3302, and AMC3330 devices.

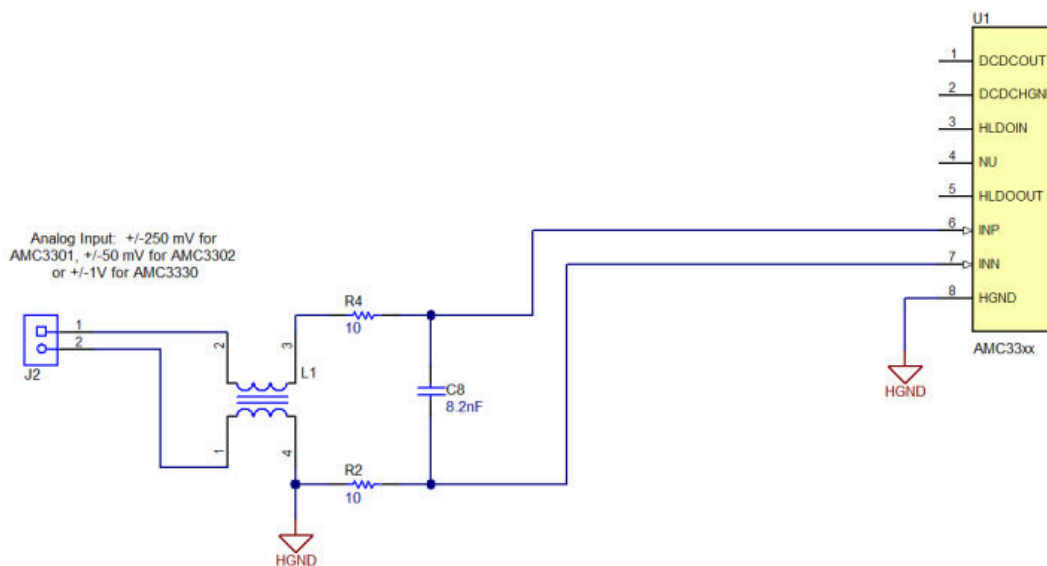


Figure 2-1. AMC3301, AMC3302, and AMC3330EVM Schematic: Analog Input Section

2.2 Analog Outputs

The analog outputs from the evaluation module are fully-differential signals centered at a common-mode output voltage of 1.44 V. As Figure 2-2 shows, the outputs are available on the three screw terminals of J1.

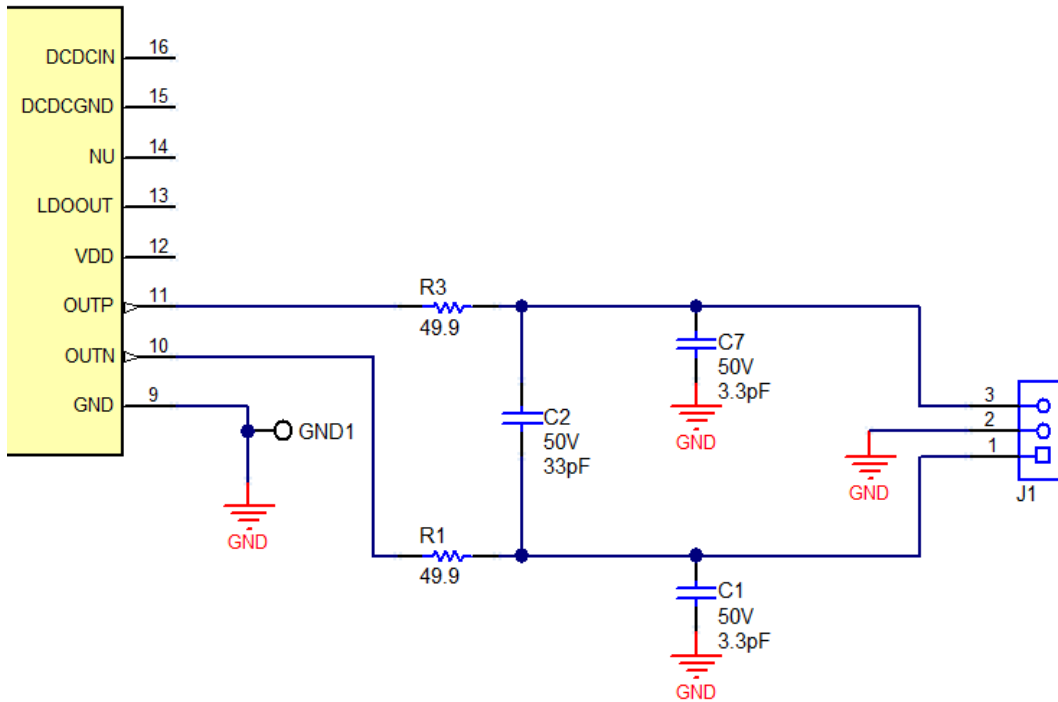


Figure 2-2. AMC3301, AMC3302, and AMC3330EVM Schematic: Analog Output Section

3 Power Supplies

The evaluation module requires a single power rail, VDD. VDD is on the low voltage side of the amplifier. Power for the high side of the amplifier is generated internally via the DC/DC converter circuit employed in the AMC3301, AMC3302, and AMC3330 device.

3.1 VDD Input

J3 provides access to the VDD supply. Use a voltage between 3.0 VDC and 5.5 VDC for the user-applied VDD supply. Figure 3-1 shows the input power.

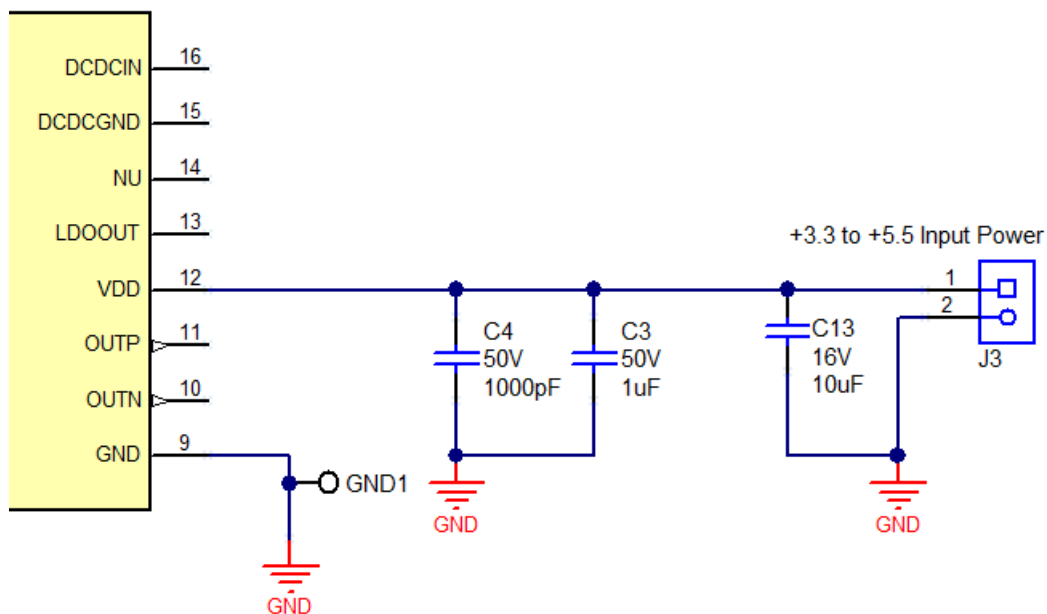


Figure 3-1. AMC3301EVM, AMC3302EVM, and AMC3330EVM VDD Input

4 EVM Operation

This section describes the general operation of the AMC3301EVM, AMC3302EVM, and the AMC3330EVM.

4.1 Analog Inputs: J2

The analog input to the evaluation module can be applied directly to J2 pins 1 and 2.

CAUTION

For the limitations of the analog input range, and to ensure that the appropriate analog and digital voltages are applied before connecting any analog input to the EVM, see the [AMC3301 Fully-Differential Isolation Amplifier](#), [AMC3302 Fully-Differential Isolation Amplifier](#) or [AMC3330 Fully-Differential Isolation Amplifier](#) data sheets.

Table 4-1 summarizes the details of J2.

Table 4-1. J2: Analog Inputs

Pin Number	Signal	Description
J2.1	IN+	Non-inverting input to U1 (pin 6)
J2.2	IN-	Inverting analog input to U1 (pin 7)

4.2 User Power and Analog Output: J1 and J3

The filtered analog output from the AMC3301EVM and AMC3330EVM board is applied directly to J1, pins 1 and 3. Table 4-2 summarizes the details of J1.

Table 4-2. J1: Analog Output

Pin Number	Signal	Description
J1.1	OUTN	Inverting analog output from U1 (pin 10)
J1.2	GND	Ground reference
J1.3	OUTP	Non-inverting output from U1 (pin 11)

The VDD power input to the AMC3301EVM, AMC3302EVM, and AMC3330EVM PCB can be applied directly to J3, pins 1 and 2. Table 4-3 lists the details of J3.

Table 4-3. J3: VDD Power

Pin Number	Signal	Description
J3.1	VDD	Connection to the U1 VDD terminal (pin 12)
J3.2	GND	Connection to the U1 GND terminal (pin 9)

4.3 Device Operation

After the VDD power is applied to the AMC3301EVM, AMC3302EVM, or AMC3330EVM, the analog outputs are available with a fixed gain and a DC common-mode offset equal to 1.44 V.

An analog input signal can be applied directly at screw terminal J2. See [Figure 2-1](#) and [Table 4-1](#) for details. The differential analog input range for the AMC3301, (VIN+) – (VIN–), is specified at ± 250 mV with a maximum of ± 320 mV before clipping occurs. The differential analog input range for the AMC3302, (VIN+) – (VIN–), is specified at ± 50 mV with a maximum of ± 64 mV before clipping occurs. The differential analog input range for the AMC3330, (VIN+) – (VIN–), is specified at ± 1 V with a maximum of ± 1.25 V before clipping occurs.

The analog output of the AMC3301 isolation amplifier has a nominal gain of 8.2, while the AMC3302 isolation amplifier has a nominal gain of 41 and the AMC3330 isolation amplifier has a nominal gain of 2. For the AMC3301, with a full-scale input voltage of ± 250 mV, the nominal output is ± 2.05 V. For the AMC3302, with a full-scale input voltage of ± 50 mV, the nominal output is ± 2.05 V. For the AMC3330, with a full-scale input voltage of ± 1 V the nominal output is ± 2 V.

The output voltage of the AMC3301EVM, AMC3302EVM, and AMC3330EVM is centered on 1.44 V and provides a convenient analog input range to the embedded analog-to-digital converters (ADCs) of the [MSP430](#) and [TMS320C2000](#) series of digital processors.

5 Layout, BOM, and Schematic

This sections contains the PCB layout, [bill of materials](#), and [schematic](#) of the AMC3301EVM, AMC3302EVM, and AMC3330EVM.

5.1 Layout

[Figure 5-1](#) shows the AMC3301EVM, AMC3302EVM, and AMC3330EVM PCB layout.

Note

Board layout is not to scale. This figure is intended to show how the board is laid out and is not intended to be used for manufacturing AMC3301EVM, AMC3302EVM, and AMC3330EVM PCBs.

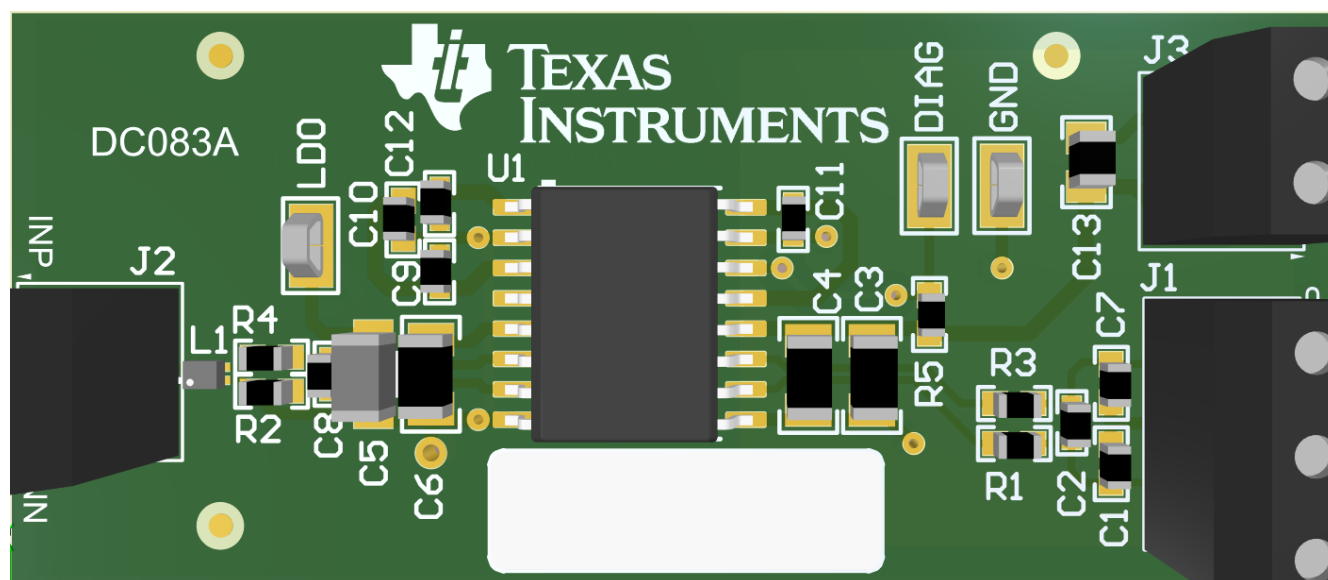


Figure 5-1. AMC3301EVM, AMC3302EVM, and AMC3330EVM Top Layer Silkscreen

5.2 Bill of Materials

Table 5-1 lists the bill of materials.

Note

All components must be RoHS compliant. Some part numbers can be leaded or RoHS. Verify that purchased components are RoHS compliant.

Table 5-1. AMC3301EVM Bill of Materials

Item	Qty	Ref Des	Description	Manufacturer	Part Number
1	2	C1, C7	CAP, CERM, 3.3 pF, 50 V, +/- 8%, C0G/NP0, 0603	AVX	06035A3R3CAT2A
2	1	C2	CAP, CERM, 33 pF, 50 V, +/- 5%, C0G/NP0, 0603	Kemet	C0603C330J5GACTU
3	1	C3	CAP, CERM, 1 uF, 50 V, +/- 10%, X7R, 1206	AVX	12063C105KAT2A
4	2	C4, C6	CAP, CERM, 1000 pF, 50 V, +/- 5%, C0G/NP0, 1206	AVX	12065C102KAT2A
5	1	C5	CAP, CERM, 0.1 uF, 50 V, +/- 5%, C0G/NP0, 1206	TDK	C3216NP01H104J160AA
6	1	C8	CAP, CERM, 8200 pF, 100 V, +/- 10%, X7R, 0603	MuRata	GRM188R72A822KA01D
7	2	C9, C11	CAP, CERM, 0.1 uF, 50 V, +/- 10%, X7R, 0603	Kemet	C0603C104K5RACAUTO
8	1	C10	CAP, CERM, 1000 pF, 25 V, +/- 5%, C0G/NP0, 0603	Kemet	C0603C102K5RACTU
9	1	C12	CAP, CERM, 1 uF, 25 V, +/- 10%, X7R, 0603	TDK	CGA3E1X7R1E105K080A C
10	1	C13	CAP, CERM, 10 uF, 16 V, +/- 10%, X5R, 0805	Taiyo Yuden	EMK212BJ106KG-T
11	3	DIAG1, GND1, LDO1	Test Point, Miniature, SMT	Keystone	5015
12	2	J1, J2	Terminal Block, 3.5mm Pitch, 3x1, TH	On-Shore Technology	ED555/3DS
13	1	J3	Terminal Block, 3.5mm Pitch, 2x1, TH	On-Shore Technology	ED555/2DS
14	2	R1, R3	RES, 49.9, 1%, 0.1 W, 0603	Panasonic	ERJ-3EKF49R9V
15	2	R2, R4	RES, 10, 5%, 0.25 W, 0603	Vishay-Dale	CRCW060310R0JNEAHP
16	1	R5	RES, 47 k, 5%, 0.1 W, 0603	Yageo	RC0603JR-0747KL
17	1	L1	Coupled inductor, 0.1 A, 4.5 ohm, SMD	Taiyo Yuden	MCF12102G900-T
18	1	U1	AMC3301 Precision, ±250-mV Input, Reinforced Isolated Amplifier with Integrated DC/DC Converter	Texas Instruments	AMC3301DWER or AMC3302DWER or AMC3330DWER

5.3 Schematic

Figure 5-2 shows the AMC3301, AMC3302 and AMC3330EVM schematic.

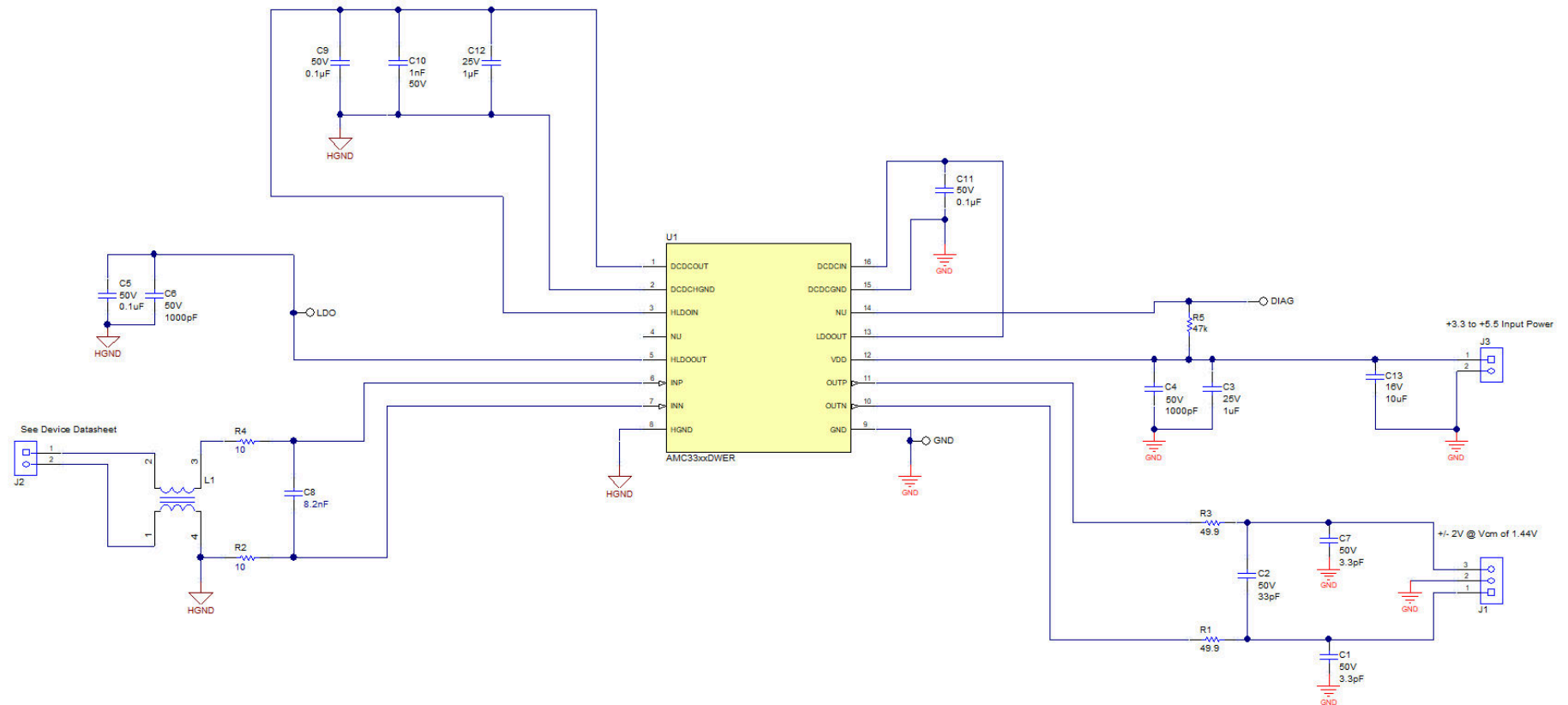


Figure 5-2. AMC3301, AMC3302 and AMC3330EVM Schematic

6 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision A (March 2020) to Revision B (September 2020)	Page
• Updated the numbering format for tables, figures, and cross-references throughout the document.....	2
• Added AMC3302 throughout document.....	2
• Changed tables to just have the pin number and removed the part number references in the <i>EVM Operation</i> section.....	5
• Changed BOM.....	7
• Changed schematic.....	8

Changes from Revision * (June 2019) to Revision A (March 2020)	Page
• Added AMC3300 throughout document.....	2

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User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI's recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.

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3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

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This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

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Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

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-
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