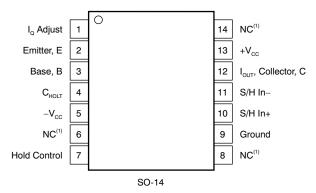


# **DEM-OPA-SO-1C Demonstration Fixture**

## 1 Description

The DEM-OPA-SO-1C demonstration fixture is a generic, unpopulated printed circuit board (PCB) for a single, wide bandwidth, DC restoration circuit in an SO-14 package. Figure 1 shows the package pinout for this PCB. For more information on these types of op amps, as well as good PCB layout techniques, see the individual amplifier data sheets.



NOTE: (1) No Connection.

Figure 1. SO Package Pinout, Top View



## 2 Circuit

The circuit schematic in Figure 2 shows the connections for all possible components. Each configuration uses only some of the components.

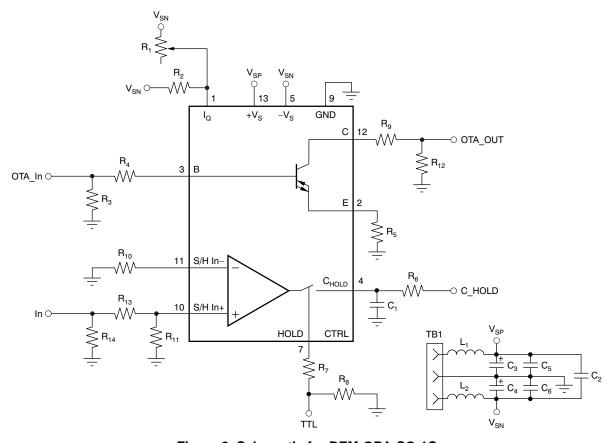


Figure 2. Schematic for DEM-OPA-SO-1C

## 3 Components

Components that have RF performance similar to the ones in Table 1 may be substituted.

PART	DESCRIPTION	
C <sub>5</sub> , C <sub>6</sub>	Tantalum Chip Capacitor, SMD EIA Size 3516, 20V	
C <sub>1</sub> , C <sub>2</sub> , C <sub>3</sub> , C <sub>4</sub>	Multilayer Ceramic Chip Capacitor, SMD 1206, 50V	
OTA_IN, OTA_OUT, IN, TTL, C_HOLD	SMA or SMB Board Jack (Amphenol 901-144-8)	
L <sub>1</sub> , L <sub>2</sub>	EMI-Suppression Ferrite Chip, SMD 1206 (Steward LI 1206 B 900 R)	
TB <sub>1</sub>	Terminal Block, 3.5mm Centers (On-Shore Technology ED555/3DS)	
R <sub>XX</sub>	Metal Film Chip Resistor, SMD 1206, 1/8W	

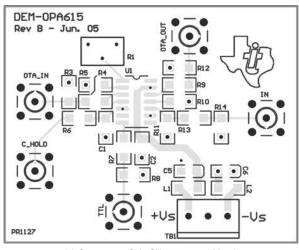
**Table 1. Component Descriptions** 

Please refer to Figure 3 for the location of the following components:

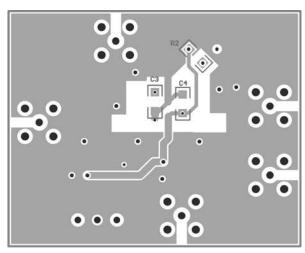
- R<sub>3</sub>, R<sub>4</sub>, R<sub>9</sub> and R<sub>12</sub> set the desired input/output impedances of the Operational Transconductance Amplifer (OTA) section.
- R<sub>5</sub> is used to set the gain of the OTA section.
- R<sub>10</sub>, R<sub>11</sub>, R<sub>13</sub>, and R<sub>14</sub> set the desired input/output impedances of the sampling OTA section.
- C<sub>1</sub> is the hold capacitor.
- R<sub>7</sub> and R<sub>8</sub> sets the input impedance for the TTL control.



- C<sub>2</sub>, C<sub>3</sub>, C<sub>4</sub>, C<sub>5</sub>, and C<sub>6</sub> are supply bypass capacitors. C<sub>2</sub> is optional; it adds a bypass between the supplies, which may improve distortion performance.
- L<sub>1</sub> and L<sub>2</sub> are ferrite chips that can reduce interactions with the power supply at high frequencies. If not desired, they can be replaced with 0Ω resistors.



(a) Component Side Silkscreen and Metal



(a) Ground Plane Side Silkscreen and Metal (bottom view)

(1) The board name appearing in the top silkscreen is DEM-OPA615 with the Revision B design finalized in June 2005.

Figure 3. DEM-OPA-SO-1C Demonstration Fixture Layout



### 4 Board Layout

This demonstration fixture is a two-layer PCB. It uses both a ground plane and power traces on the top and bottom. The ground plane has been opened up around op amp pins that are sensitive to capacitive loading. Power-supply traces are laid out to keep current loop areas to a minimum. The SMA (or SMB) connectors may be mounted either vertically or horizontally onto the board edge. The location and type of capacitors used for power-supply bypassing are crucial for high-frequency amplifiers. The tantalum capacitors,  $C_5$  and  $C_6$ , do not need to be close to pins 5 and 13 on the PCB and may be shared with other amplifiers. See the individual op amp data sheet for more information on proper board layout techniques and component selection.

## 5 Measurement Tips

This demonstration fixture, with the component values shown, is designed to operate in a  $50\Omega$  environment; most data sheet plots are obtained under these conditions. It is easy to change the component values for different input and output impedance levels. However, do not use high-impedance probes; they represent a heavy capacitive load to the op amp, and will alter the amplifier response. Instead, use low-impedance ( $\leq 500\Omega$ ) probes with adequate bandwidth. The probe input capacitance and resistance set an upper limit on the measurement bandwidth. If a high-impedance probe must be used, place a  $100\Omega$  resistor on the probe tip to isolate its capacitance from the circuit.

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