

# TLV767EVM-014 Evaluation Module

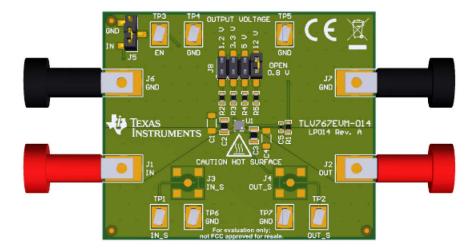


Figure 1. TLV767EVM-014 Evaluation Module

This user's guide describes the operational use of the TLV767EVM-014 evaluation module (EVM) as a reference design for engineering demonstration and evaluation of the TLV76701DRV, low-dropout linear regulator (LDO). Included in this user's guide are setup and operating instructions, thermal and layout guidelines, a printed circuit board (PCB) layout, a schematic diagram, and a bill of materials (BOM).

Throughout this document, the terms *demonstration kit*, *evaluation board*, and *evaluation module* are synonymous with the TLV767EVM-014.

Table 1 lists the related documentation available through the Texas Instruments web site at www.ti.com.

Device	Literature Number
TLV767	SLVSE84

## Table 1. Related Documentation



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# 1 Introduction

Texas Instruments' TLV767EVM-014 EVM helps design engineers evaluate the operation and performance of the TLV767 family of linear regulators for possible use in their own circuit application. This particular EVM configuration contains a single linear regulator. The regulator is capable of delivering up to 1 A to the load. For stability, use a  $1-\mu$ F (or larger) output capacitor for the TLV767.

# 1.1 Before You Begin

The following warnings and cautions are noted for the safety of anyone using or working close to the TLV767EVM-014. Observe all safety precautions.



**Warning** Warning hot surface. Contact may cause burns. Do not touch.

# CAUTION

The circuit module may be damaged by over temperature. To avoid damage, monitor the temperature during evaluation and provide cooling, as needed, for your system environment.

# CAUTION

Some power supplies can be damaged by application of external voltages. If using more than one power supply, check your equipment requirements and use blocking diodes or other isolation techniques, as needed, to prevent damage to your equipment.



EVM Setup

# 2 EVM Setup

This section describes how to properly connect and setup the TLV767EVM-014, including the jumpers and connectors on the EVM board.

## 2.1 Input/Output Connector and Jumper Descriptions

### 2.1.1 J1 – IN

Input power-supply voltage connector. Twist together the positive input lead and ground return lead from the input power supply, and keep them as short as possible to minimize input inductance.

### 2.1.2 J2 – OUT

Regulated output voltage connector.

### 2.1.3 J3 – IN\_S

Input sense.

### 2.1.4 J4 – OUT\_S

Output sense.

### 2.1.5 J5 – ENABLE

Output enable. To enable the output, connect a jumper to short IN to EN. To disable the output, connect a jumper between GND and EN.

### 2.1.6 J6 – GND

Input ground return connector.

### 2.1.7 J7 – GND

Output ground return connector.

# 2.1.8 J8 – OUTPUT VOLTAGE Set

For convenience, the EVM is prepopulated with four resistor divider options. Place a shunt on J9 next to the silkscreen label of your desired output voltage.

### 2.1.9 TP1 – IN\_S

Input sense test point.

### 2.1.10 TP2 – OUT\_S

Output sense test point.

### 2.1.11 TP3 – EN

Enable test point.

### 2.1.12 TP4 – GND

Ground test point.

# 2.1.13 TP5 – GND

Ground test point.

# 2.1.14 TP6 – GND

Ground test point.

# 2.1.15 TP7 – GND

Ground test point.

# 2.2 Soldering Guidelines

To avoid damaging the LDO, use a hot-air system for any solder rework to modify the EVM for the purpose of repair or other application reasons.

# 2.3 Equipment Connection

Connect the equipment as shown in the following steps:

- 1. Set the input power supply up to 16 V (max), and turn the power supplies off.
- 2. Connect the positive voltage lead from the input power supply to IN at the J1 connector of the EVM.
- 3. Connect the ground lead from the input power supply to GND at the J6 connector of the EVM.
- 4. Connect a 0-A to 1-A load between OUT and GND.
- 5. Disable the output by shorting EN to GND through J5.

# 3 Operation

Operate the equipment using the following steps:

- 1. Turn on the power supplies.
- 2. Enable the output by jumping J5 (the EN pin) to VIN.
- 3. Vary the respective load and input voltage, as necessary, for test purposes.



PCB Layout

www.ti.com

# 4 PCB Layout

Figure 2 to Figure 4 illustrate the PCB layout for this EVM.

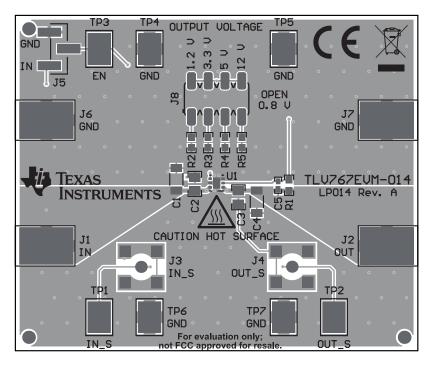


Figure 2. Assembly Layer

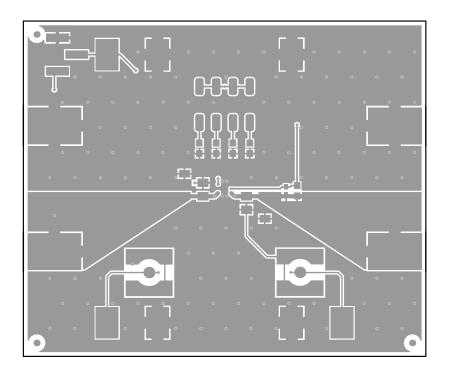


Figure 3. Top Layer Routing



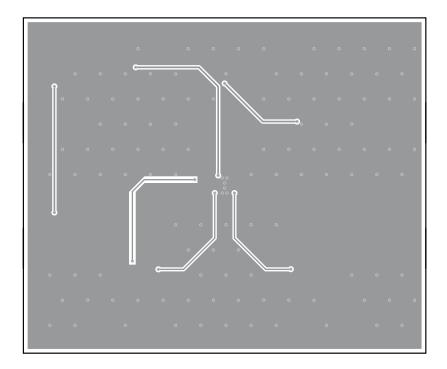


Figure 4. Bottom Layer Routing



#### Schematic

# 5 Schematic

Figure 5 is the schematic for this EVM.

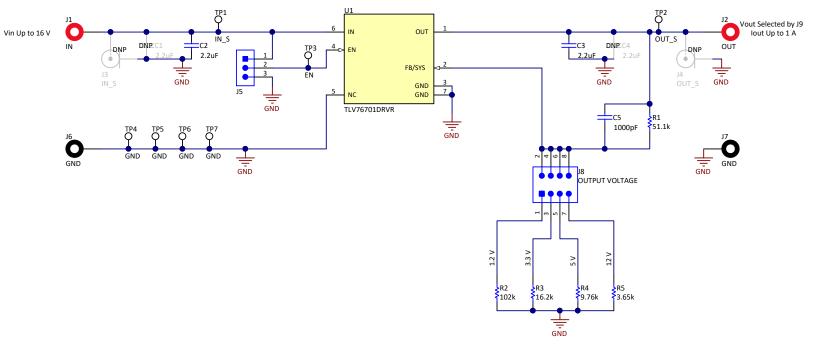


Figure 5. TLV767EVM-014 Schematic



# 6 Bill of Materials

The BOM for this EVM is shown in Table 2.

Designator	QTY	Value	Description	Package Reference	Part Number	Manufacturer	Alternate Part Number	Alternate Manufacturer
!PCB1	1		Printed Circuit Board		LP014	Any		
C2, C3	2	2.2uF	CAP, CERM, 2.2 uF, 50 V, +/- 20%, X7R, 0805	0805	C2012X7R1H225M125AC	TDK		
C5	1	1000pF	CAP, CERM, 1000 pF, 50 V, +/- 10%, X7R, 0402	0402	GRM155R71H102KA01D	MuRata		
J1, J2	2		Standard Banana Jack, Insulated, Red	6091	6091	Keystone		
J5	1		Header, 100mil, 3x1, Gold, SMT	Samtec_TSM-103-01-X-SV	TSM-103-01-L-SV	Samtec		
J6, J7	2		Standard Banana Jack, Insulated, Black	6092	6092	Keystone		
J8	1		Header, 2.54mm, 4x2, Gold, SMT	Header, 2.54mm, 4x2, SMT	95278-801A08LF	FCI		
R1	1	51.1k	RES, 51.1 k, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	CRCW060351K1FKEA	Vishay-Dale		
R2	1	102k	RES, 102 k, 1%, 0.1 W, 0603	0603	RC0603FR-07102KL	Yageo		
R3	1	16.2k	RES, 16.2 k, 1%, 0.1 W, 0603	0603	RC0603FR-0716K2L	Yageo		
R4	1	9.76k	RES, 9.76 k, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	CRCW06039K76FKEA	Vishay-Dale		
R5	1	3.65k	RES, 3.65 k, 1%, 0.1 W, 0603	0603	RC0603FR-073K65L	Yageo		
SH-J1, SH-J2	2	1x2	Shunt, 100mil, Gold plated, Black	Shunt	969102-0000-DA	3M	SNT-100-BK-G	Samtec
TP1, TP2, TP3, TP4, TP5, TP6, TP7	7		Test Point, Compact, SMT	Testpoint_Keystone_Compact	5016	Keystone		
U1	1		1-A, Positive Voltage Regulator, DRV0006A (WSON-6)	DRV0006A	TLV76701DRVR	Texas Instruments		
C1, C4	0	2.2uF	CAP, CERM, 2.2 uF, 50 V, +/- 10%, X7R, 1206	1206	C3216X7R1H225K160AB	TDK		
FID1, FID2, FID3	0		Fiducial mark. There is nothing to buy or mount.	N/A	N/A	N/A		
J3, J4	0		Connector, SMA Jack, Vertical, Gold, SMD	SMA	142-0711-201	Cinch Connectivity		

# Table 2. TLV767EVM-014 BOM<sup>(1)(2)(3)(4)</sup>

<sup>(1)</sup> These assemblies are ESD sensitive, observe ESD precautions.

<sup>(2)</sup> These assemblies must be clean and free from flux and all contaminants. Use of no-clean flux is not acceptable.

<sup>(3)</sup> These assemblies must comply with workmanship standards IPC-A-610 Class 2.

<sup>(4)</sup> Unless otherwise noted in the *Alternate Part Number* or *Alternate Manufacturer* columns, all parts may be substituted with equivalents.

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3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

#### CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

#### FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

#### 3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

#### **Concerning EVMs Including Radio Transmitters:**

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

#### Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

#### **Concerning EVMs Including Detachable Antennas:**

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

#### Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

#### 3.3 Japan

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