User’s Guide
14-24-Logic-EVM User's Guide

ABSTRACT
This user’s guide contains support documentation for the 14-24-Logic Evaluation Module (EVM). Included is a description of how to set up and configure the EVM, the printed circuit board (PCB) layout, the schematic, and the bill of materials (BOM) of the 14-24-Logic-EVM.

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Trademarks
All trademarks are the property of their respective owners.
1 Introduction

The 14-24-Logic-EVM is a generic EVM developed to support leaded Logic devices in the PW, DB, D, DW, NS, DYY, and DGV packages. This EVM can be used to evaluate any device in the package family and pin counts described in Table 1-1. The PCB can be broken down into six sections with each section supporting certain packages indicated on the board. This EVM allows the user to have a great amount of flexibility when evaluating leaded Logic devices.

<table>
<thead>
<tr>
<th>TI Package Name</th>
<th>Package Family</th>
<th># of Pins</th>
</tr>
</thead>
<tbody>
<tr>
<td>PW</td>
<td>TSSOP</td>
<td>14, 16, 20, 24</td>
</tr>
<tr>
<td>DB</td>
<td>SSOP</td>
<td>14, 16, 20, 24</td>
</tr>
<tr>
<td>NS</td>
<td>SOP</td>
<td>14, 16, 20, 24</td>
</tr>
<tr>
<td>DGV</td>
<td>TVSOP</td>
<td>14, 16, 20, 24</td>
</tr>
<tr>
<td>D</td>
<td>SOIC</td>
<td>14, 16, 20, 24</td>
</tr>
<tr>
<td>DW</td>
<td>SOIC</td>
<td>14, 16, 20, 24</td>
</tr>
<tr>
<td>DYY</td>
<td>SOT-23-THN</td>
<td>14, 16</td>
</tr>
</tbody>
</table>

1.1 Kit Contents

Table 1-2. 14-24-Logic-EVM Kit Contents

<table>
<thead>
<tr>
<th>Item</th>
<th>Description</th>
<th>Quantity</th>
</tr>
</thead>
<tbody>
<tr>
<td>14-24-Logic-EVM</td>
<td>PCB</td>
<td>1</td>
</tr>
<tr>
<td>Headers</td>
<td>12 position, 100-mil (2.54 mm), thru-hole</td>
<td>6</td>
</tr>
</tbody>
</table>

1.2 Features

The 14-24-Logic-EVM has the following features:
- Multiple package support
- Breadboard compatible
- Easy-to-use and flexible evaluation
2 Hardware

2.1 PCB Overview

The 14-24-Logic-EVM PCB is designed to be straightforward for new users to begin evaluating leaded Logic devices. This section will highlight a few aspects of the PCB that are helpful to the user.

- Board breakable into six sections with inclusion of v-scored grooves
- Each section has headers connected to device pins, $V_{CC}$, and GND
- Dashed lines included to assist with the placement of devices with less than 24 pins
- Designated supply inputs which can support thru-hole test points
- Bypass capacitor footprint included for device $V_{CC}$

2.2 Hardware Setup

This section will cover the six steps to take when evaluating a leaded Logic device using this EVM.

1. Identify the package you will be using for the device being evaluated. As stated previously, this EVM has six sections each of which contains a footprint in which a logic device can be placed. Break off the selected section (optional).
2. Solder down the device. Some sections support multiple packages so carefully solder down the device to make sure it is aligned properly. If a device with less than 24 pins is being evaluated, it should be placed towards the top of the footprint.
3. Ensure device $V_{CC}$ pin is connected to the bypass capacitor. If pin 1 of the device is connected to pin 1 of the footprint then it will be connected correctly. Figure 2-2 shows an example of a 14-pin device in the D package placed correctly on the EVM.
4. Interface with device pins. The kit includes six 12-pin headers which will allow the user to fully populate a single section. An example of this, with the addition of test points and a bypass capacitor for the supply, can be seen in Figure 2-3.

5. The device \( V_{CC} \) pin will need to be connected to the \( V_{CC} \) of the EVM. If using the headers, this can be accomplished using a simple shunt. If the headers are not being used then a simple solder bridge can be formed from the I/O header pad to the \( V_{CC} \) header pad.

6. Repeat step 5 for the GND pin and any unused input pins of the device. Figure 2-4 shows an example of how to use shunts to both power the device and tie unused inputs to a defined logic state to prevent them from floating. For more information on why it is important to avoid floating inputs, see the *Implications of Slow or Floating CMOS Inputs* application report.
Figure 2-5 is included to highlight that the GND shunt will shift up as the device pin count decreases even though the VCC will always be shunted in the top right assuming the device is placed correctly.

### 3 Board Layout

![Board Layout Diagram](image-url)
4 Bill of Materials

This section will provide information on the components that can be used with the 14-24-Logic-EVM. Other components can be used as long as they are able to fit the provided plated holes and pads.

<table>
<thead>
<tr>
<th>Item</th>
<th>Value</th>
<th>Description</th>
<th>Package Reference</th>
<th>Part Number</th>
<th>Manufacturer</th>
</tr>
</thead>
<tbody>
<tr>
<td>Bypass Capacitor</td>
<td>0.1 µF</td>
<td>CAP, CERM, 0.1 µF, 50 V, ± 20%, X7R, 0805</td>
<td>0805</td>
<td>0805S104MAT2A</td>
<td>AVX</td>
</tr>
<tr>
<td>Header</td>
<td></td>
<td>Header, 2.54 mm, 12×1, Gold, TH</td>
<td>Header, 2.54 mm, 12×1, TH</td>
<td>PBC12SABN</td>
<td>Sullins Connector Solutions</td>
</tr>
<tr>
<td>Red Test Point</td>
<td></td>
<td>Test Point, Multipurpose, Red, TH</td>
<td>Red Multipurpose Test point</td>
<td>5010</td>
<td>Keystone</td>
</tr>
<tr>
<td>Black Test Point</td>
<td></td>
<td>Test Point, Multipurpose, Black, TH</td>
<td>Black Multipurpose Test point</td>
<td>5011</td>
<td>Keystone</td>
</tr>
</tbody>
</table>

5 References

1. Texas Instruments, *Implications of Slow or Floating CMOS Inputs* application report

6 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision * (October 2018) to Revision A (December 2021)

- Updated the numbering format for tables, figures, and cross-references throughout the document ....................2
- Updated the packages from N, P to DYY .............................................................................................................2
- Updated the Package and Pin Support Table to include DYY package information ..............................................2
- Updated the 14-24-Logic-EVM PCB figure ........................................................................................................3
- Updated the 14-24-Logic-EVM Layout figure .....................................................................................................5
- Added References section ...................................................................................................................................6
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3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

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NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

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Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.
Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l’impédance requise pour chaque type d'antenne. Les types d’antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l’exploitation de l’émetteur.

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2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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