User's Guide **BQ79616-Q1 and BQ75614-Q1 GUI User's Guide**



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ABSTRACT

This document provides a guide to install, set up, and use the BQ79616-Q1 graphical user interface (GUI). In this document, the basic functionalities of the GUI are documented with the intent of reducing the time needed to become familiar with the GUI.

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1 Getting Started

1.1 Installing the GUI

In the following sections, we will step through the instructions to properly install each item in order. Start by downloading the below files 1) for GUI executable and 2) for the required USB2ANY firmware.

- •
- Download Software GUI via link: https://www.ti.com/product/BQ79616-Q1#design-development##softwaredevelopment

USB2ANY Explorer installer folder (this can be downloaded from http://www.ti.com/tool/USB2ANY and downloading USB2ANY Explorer Software under Key Document)

• USB2ANY SDK installer (2_USB2ANY_2.8.2.0_SDK_Setup.exe)

1.1.1 Uninstalling and Updating the BQ79616 GUI

Very Important The current latest version of the GUI is bqAutoEval 1.0.4. If you have used a previous older version then you must uninstall the previous version before you update to this newer version. The steps are below:

- 1. Close out of all instances of the BQ79616 GUI. Ensure that there are no remaining Node.js processes running in the background by ending them from the task manager.
- 2. The uninstaller can be found in C:\Program Files (x86)\Texas Instruments\BQ79616. Run the uninstaller.
- 3. The uninstaller may ask you if you want to uninstall GUI Composer Runtime if there are no additional dependencies on GUI Composer Runtime on the user's computer. **Do NOT uninstall GUI Composer Runtime or you will have to repeat step 1.1.2 below.**
- 4. Extract the components of the new installer and run them from any location in the user's PC.

1.1.2 Installing GUI Composer Runtime

- 1. If you already have the GUI Composer Runtime Installer downloaded from previously using an older GUI version and did not uninstall it, then you may skip step 2.
- 2. When you run the GUI executable software link from the bq79616 product folder, you will be prompted to download the GUI Composer Runtime. Ensure to follow instructions and accept downloading the GUI Composer Runtime 7.4.0.

1.1.3 Updating USB2ANY Firmware

This step is crucial to having the GUI work correctly without any errors, and may be the first issue if you run into any problems when first connecting to the EVM. If you do not do this correctly, the GUI will notify you that either your USB2ANY Hardware is not connected properly or that you have an outdated firmware version. If the GUI recognizes this, it will prompt you with the latest firmware and further instructions to update the USB2ANY.

- 1. To properly use the latest bqAutoEval GUI v1.0.4, you are required to update to firmware version 3_1_1_14. This file is named USB2ANY_3_1_1_14 BQAutoEval.txt. Skip to step 5 if USB2ANY is brand new. Upon opening the GUI, it will prompt to update automatically.
- Download and extract the contents of the USB2ANY Explorer installer folder (this can also downloaded from http://www.ti.com/tool/USB2ANY and downloading USB2ANY Explorer Software under Key Document) and run the executable from within it. Keep all default installation folder locations.
- 3. To update the USB2ANY firmware,ensure you have downloaded the USB2ANY SDK and ran the executable (2_USB2ANY_2.8.2.0_SDK_Setup.exe). If the default location from the installation wizard was not changed, the folder can be found in C:\Program Files (x86)\TI USB2ANY SDK.
- 4. Ensure the USB2ANY firmware file "USB2ANY_3_1_1_14 BQAutoEval.txt" is saved somewhere to the hard drive. Copy the "USB2ANY_3_1_1_14 BQAutoEval.txt" file and paste it into the **Firmware** folder as in the below image.



This PC > Windows (C:) > Program Files (x86) > TI USB2ANY SDK

	Name ^	Date modified	Туре	Size
	🔒 bin	6/11/2019 3:50 PM	File folder	
7	>	6/26/2019 3:59 PM	File folder	
×.	Sample Code	6/11/2019 3:50 PM	File folder	
*	API Reference for USB2ANY SDK 2.8	5/12/2017 2:44 PM	Adobe Acrobat D	2,659 KB
ł	Instructions for Updating USB2ANY Fir	5/8/2017 1:29 PM	Adobe Acrobat D	203 KB
e	Release Notes for USB2ANY DLL	5/12/2017 2:37 PM	Adobe Acrobat D	458 KB
	Release Notes for USB2ANY Firmware	5/12/2017 2:40 PM	Adobe Acrobat D	411 KB
	📄 unins000.dat	6/11/2019 3:50 PM	DAT File	19 KB
	👋 unins000	6/11/2019 3:50 PM	Application	802 KB
	USB2ANY Explorer_manifest	5/8/2017 1:29 PM	Chrome HTML Do	33 KB
	USB2ANY SupportInfo	5/8/2017 1:29 PM	Adobe Acrobat D	405 KB
	USB2ANY_Runtime_Files_manifest	5/8/2017 1:29 PM	Chrome HTML Do	14 KB

Figure 1-1. Paste .txt Firmware File into the Firmware Folder

- 5. Connect the USB2ANY to the computer. Press the Windows icon, search for USB2ANY Firmware Loader, and launch it. If the firmware loader does not show up in the search, reinstall the USB2ANY Explorer. Click Yes on the dialog box that mentions that the firmware loader is legacy software. If the firmware loader requests to update the firmware to version 2.8.2.0, press cancel. If the USB2ANY has never had its firmware updated before, additional steps a, b, and c below need to be followed to update the firmware. Otherwise, skip the below steps a, b, and c and go to step 6.
 - a. The USB2ANY firmware loader will prompt you to locate the BSL button in order to update the firmware. Follow the steps given in the Firmware Loader as below. Do not close the USB2ANY Firmware Loader at any point during this process.
 - b. Check that the USB2ANY is disconnected from the computer. Locate the BSL button, which is underneath a small hole in the USB2ANY housing next to the mini USB port.
 - c. While keeping the button depressed (a small paper clip works well), connect the USB2ANY to the computer. If it is done correctly, the USB2ANY firmware loader will no longer prompt you to locate the BSL button and instead allow you to click Update Firmware and then follow the next steps to program the latest firmware as below.
- 6. In the firmware loader, ensure that "USB2ANY_3_1_1_14 BQAutoEval.txt" appears in the field labeled **Update to firmware version:**. Press **Update Firmware**. The below image shows an earlier outdated firmware version so please ensure the above text file is correct.

1.1.4 Installing the GUI

- 1. Download and navigate to the BQ79616 installer folder (3_BQAutoEvalPG2.0_1.0_installer_win.zip). Extract the contents and run the setup executable from within the extracted folder. Keep all default installation folder locations and press yes to all prompted dialogue.
- 2. To launch the GUI, navigate to C:\Program Files (x86)\Texas Instruments\BQ79616 and run the BQ79616 application.
- 3. The device should automatically connect upon launching the GUI, and the connection status should be reflected at the bottom of the window. If the device does not connect, unplug the USB2ANY from the computer, reload the GUI, and plug the USB2ANY back into the computer. Otherwise, you may have incorrectly updated the firmware and may need to return to section 1.1.3.

USB2ANY/OneDemo device Hardware Connected.

Figure 1-2. Successful Hardware Connection Message



1.1.5 Starting Up the EVM

This section outlines connections specifically for the BQ79616EVM. For information of powering and connecting other EVMs to the host PC, please refer to BQ79600EVM Connections or BQ79631EVM Connections.



Figure 1-3. BQ79616 EVM Connections

- 1. Apply a minimum 18 V DC power supply across VSTACK and GND.
- 2. Connect the appropriate jumpers. Refer to the BQ79616 EVM user's guide.
- 3. Connect the 10-pin ribbon cable to the USB2ANY and to the EVM (key side up)

1.2 BQ79600-Q1 Device Key Differences

When using the BQ79600EVM with the BQAutoEval GUI, there are a few key differences that exist specifically for this device that may not apply directly to the information presented in the rest of the document. These details will be discussed below.

1.2.1 Connections

To properly supply power and connect the BQ79600EVM to the host PC and other EVMs, please perform the following steps:

- 1. Apply either 12 V or 5 V to TP8 or TP9 while making sure that J3 and J1 are configured for the correct supply voltage.
- 2. Connect the appropriate jumpers. Refer to the BQ79600 EVM user's guide, and pay special attention to what communications mode is needed for the device (SPI or UART).
- 3. Connect the 10-pin ribbon cable to the USB2ANY and to the EVM (key side up).



4. Use the J14 COMML and J15 COMMH connectors to connect the BQ79600EVM bridge device to any devices being used in the stack.

Note

The BQ79600EVM should always be configured as the bridge device if it is being used in the system.

1.2.2 Cell Monitor Tab

While the Cell monitor tab is used by other devices such as the BQ79616-Q1, BQ79614-Q1, and BQ79631-Q1 to monitor parameters such as VC, GPIO, and current sense data, this information is not pertinent to the function of the BQ79600-Q1 device. Therefore, this device will not appear in the cell monitor tab of the BQAutoEval GUI. This does NOT mean that the device is not connected.

1.2.3 Communication Control

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n	COMMUNICAT	ION CONTRO	OL		Mode: UAR	T (1Mbps) 🛈			START POLLING				EP ALL	WAKE ALL	🖞 ѕнитро	WIN ALL	ý re	SET ALL
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1 0	COMMUNIC.	ATION CONFI	GURATION	Comm Timeout	Long Comr	n Timeout	Long Timeout Acti	on				Sleep Timeout			Stack 1 Board Faults :	Brid	Bridge ge 🔻	•
	0	Bit periods	10s	Ŧ	1min	•	Set FAULT_SYS	S[CTL] and send o	levice to SLEEP mod	e	•	1min	•		FAULT SUMMAR	Y		
9	POWER MOI	DE CONTROL													Communication	•	Register	
A	CURRENT	DEVICE (Bridge)	0	TONES TO NEXT DE	EVICE ①	PING TO BRIDG	E DEVICE ①								System	•	Power	•
Ŷ	S	HUTDOWN		SHUTDOWN T	DNE	SHUTD	OWN											
đ		SLEEP		SLEEP TO ACTIVE	TONE	WA	Œ											
r٩	s	OFT RESET		WAKE TON	E	SLEEP TO	ACTIVE											
4				HARDWARE RESE	TTONE	сомм	CLEAR											
,				*Regist	er Controls		Ping Command											
	DEBUG						0	2										
<i>🛛</i> 😅	▲ USB2ANY/C	IneDemo devic	e Hardwar	e Connected.													🗘 Texas Ir	STRUMENTS

Figure 1-4. BQ79600 GUI Communication Control Page

This page is used for controlling the power mode and communications of the base 600 device and any stack devices that are connected. Furthermore, the user can monitor faults seen at the top right, as well as change the communications direction.

Communication Configuration

Under this tab, the user can configure the following settings:

- **TX Holdoff:** Integer between 0–255; specifies the number of bit periods the UART transmitter waits after the last bit reception before starting transmission.
- Communication Timeout Thresholds

Short Comm Timeout: Timer resets every time a valid response or command frame is received; sets CTS system fault when timer expires.

Long Comm Timeout: Allows the host to put the device in SLEEP or SHUTDOWN modes for power saving; timer resets every time a valid response or command frame is received; executes **Long Timeout Action** when the timer expires.

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• Long Timeout Action: On a long timeout, either send the device to SLEEP and set a CTL system fault or send the device to SHUTDOWN.

Power Mode Control

Under this tab, the user can configure the following settings:

- **Current Device (xxx):** This allows the user to control the power state of the currently selected device through the device selected drop-down menu. Users can send a selected device to shutdown, sleep, or soft reset.
- **Tones to Next Device:** This allows the user to control tones that are sent to the next device up the stack. When in use, users must make sure to change the device selected in the drop-down if they are trying to send tones to different devices.
- **Ping to Bridge Device:** This allows the user to control the power state of the current bridge device by sending pings to it.

Debug

Under this tab, the user can configure the following settings:

- VIF/UART Baud: This allows the user to control the baud rate of the VIf/UART. Additonally, users can see fault indicators on the COM lines of the base 600 device.
- **Debug Control and Comms Status:** Users can enable and disable the ability to modify the functionality of the COM ports. With the debug control enabled, users can turn on and off the COMH and COML ports.

1.2.4 Bridge Control

In this tab, users can control the bridge device and monitor, mask, and reset faults, as well as debug communications issues on the SPI, UART, and daisy chain interface. At the top of the screen is the communications mode that is being used for this device. Users must make sure and set this to the correct setting for their use.

- Control: Here users can enable or disable:
 - The sniff detector
 - nFault
 - Receiver tones
 - Heartbeat
 - Comm fault detection
 - INH
 - UART stop bits

These all enable and disable features on the 600 device.

- Fault Control: In this part of the page, users can reset, mask, and unmask faults for the 600 bridge device.
- **Fault Status:** This contains all of the fault registers, along with the respective bits within them, so that the user can see which specific faults of each fault register are being set. If a certain element is red, it means that this bit is active and has been set by the device.
- **Diagnostics:** Here, users can cause CRC faults on the device in order to test fault detection on 616 devices further up the stack, as well as on the 600 device itself. Additionally, users can perform power diagnostics and trigger the INH PMOS pullup.
- SPI Interface: This section can be used to set the SPI clk speed that the USB2ANY propagates to the 600 device.
- **Debug:** Expanding the debug tab offers an even more detailed breakdown of the faults, allowing the user to trace back top level faults to specific errors in the communications, power, and so forth.

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File	Options Tools Zo	pom He	elp									_ 🗇 ×
= N						Mode: UART	3PT ()					START POLLING
· · ·	BRIDGE CONTROL											
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	ENABLE nFAULT		MASKS									
27	TONE RECEIVER		RESET	(5	0	0		0	0	0	0	0
3	ENABLE HEARTBEAT TRANSMITTER		FALLET STATUS									
A	COMM EMBEDDED FAULT DETECTION		Fault Summany									
Ú.	INH DISABLE		FAULT_COMM	FAULT_REG	FAULT_SYS	FAULT_PWR						
0	UART TWO STOP BITS		Fault REG									
Ø	DIAGNOSTICS		CONF_MON_ERR	FACTLDERR	FACT_CRC							
P.P	FLIP FACTORY CRC		Fault SYS VALIDATE_DET	LFO	SHUTDOWN_REC	DRST	CTL	CTS	TSHUT	INH		
2	FLIP TRANSMISSION CRC		Fault PWR									
	CONFIG REGISTER MONITOR	0	CVDD_UV_DRST	CVDD_OV	DVDD_OV	AVDDREF_OV	AVAO_SW_FAIL					
/	POWER DIAGNOSTICS	0	Fault COMM1 FCOMM_DET	FTONE_DET	HB_FAIL	HB_FAST	UART_FRAME	COMMCLR_DET	STOP_DET			
	TRIGGER INH PMOS PULL UP	0	Fault COMM2									
	INH Status		SPLFRAME	SPLPHY	COML_FRAME	COML_PHY	COMH_FRAME	COMH_PHY				
	SPI FIFO DIAGNOSTICS		DEBUG									O
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	Speed 6 MHz (1ms service de	elay) 🔻										
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Figure 1-5. Bridge Control Tab

File	Options Tools Zo	oom H	elp								X
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(B)	CONTROL		Fault SYS	150	SHITDOWN REC	DRST	CTI.	CTS	TSHUT	INH	
21	SNIFF DETECTOR		Fault PWR			0101					
	ENABLE nFAULT		CVDD_UV_DRST	CVDD_OV	DVDD_OV	AVDDREF_OV	AVAO_SW_FAIL				
12	TONE RECEIVER		Fault COMM1	FTONE DET	UD 248	UD FAST	HADT TRANT	COMMON D. DET	STOD DET		
0	ENABLE HEARTBEAT TRANSMITTER		Fault COMM2	PTONE_DET	HD_FAIL	TID_FRS1	UART_FRAME	COMMCER_DET	STOP_DET		
A	COMM EMBEDDED FAULT DETECTION		SPLFRAME	SPLPHY	COML_FRAME	COML_PHY	COMH_FRAME	COMH_PHY			
γt	INH DISABLE		DEBUG								0
	UART TWO STOP BITS										Ű
ø	DIAGNOSTICS		FMT_ERR	COMCLE	TXDATA_UNEXP	RXDATA_UNEXP	TXFIF0_0F	TXFIF0_UF	RXFIFO_OF		
-9	FLIP FACTORY CRC		DEBUG SPI FRAME								
4	FLIP TRANSMISSION CRC		TR_SOF	TR_WAIT	RC_IERR	RC_SOF	RC_BYTE_ERR	RC_CRC			
	CONFIG REGISTER MONITOR	0	DEBUG UART FRAME	TR_WAIT	RC_JERR	RC_SOF	RC_BYTE_ERR	RC_CRC			
	POWER DIAGNOSTICS Power Diag Ready	0	DEBUG COMH PHY								
	TRIGGER INH PMOS PULL UP	0	PERR	BERR_TAG	SYNC2	SYNC1	BIT				
	INH Status		DEBUG COMH FRAME	RR_SOF	RR_BYTE_ERR	RR_UNEXP	RR_CRC				
	SPI FIFO DIAGNOSTICS		DEBUG COML PHY								
	SPI INTERFACE		PERR	BERR_TAG	SYNC2	SYNC1	BIT				
	Speed 6 MHz (1ms service de	elay) 🔻	DEBUG COML FRAME	BB SOF	DB BYTE EBB	RR LINEYP	BB CBC				
<i>∎</i> ∈	▲ USB2ANY/OneDemo device H	lardware Conn	ected		10/0710/000	AN CONTRACT	10,010				Powered By GUI Composed

Figure 1-6. Bridge Control Debug Tab

1.3 BQ79631-Q1 Device Key Differences

When using the BQ79631EVM with the BQAutoEval GUI, there are a few key differences that exist specifically for this device. These specific details will be discussed below.



1.3.1 Connections

USB2ANY Connection

To connect the BQ79631EVM directly to the PC via UART connection, connect the USB2ANY cable to the 10pin J10 connector on the BQ79631EVM as seen in the figure below.



Figure 1-7. J10 USB2ANY Connection

Daisy Chain Communications

If the user wishes to use the BQ79631EVM as part of a stack of other EVMs, connections will be made to the COMMH and COMML connections through the J27 and J26 connectors. If the BQ79631EVM is configured as the bridge device, the USB2ANY connection will provide communication between the EVM and the host PC while the J27 connector will connect the BQ79631EVM to any other EVMS in the stack.

If another device such as the BQ79600EVM is being used as the bridge device, the J26 connection on the BQ79631EVM should be connected to the bridge device and the J27 connection should be connected to the next device in the stack if other devices are being used.

This example has the BQ79631EVM configured as the bridge device and the BQ79616EVMs configured as stack devices.

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Figure 1-8. Example Configuration

BQ79631EVM Power Connections

Power to enable the use of the BQ79631EVM is supplied through the 5V_LV connector on the left side of the board. Simply connect the positive and negative terminals of a power supply set to 5V to the pins 5V_LV and GNDLV respectively.



For more information on specific connections needed to make measurements through the BQ79631EVM, please refer to the BQ79631EVM Users Guide.



Figure 1-9. 5V Connection

1.3.2 Cell Monitor Tab

While the other BQ devices use the cell monitor tab of the BQAutoEval GUI to monitor cell voltages directly, the bq79631-Q1 uses the VC channels to measure the certain pack voltage deltas according to the connections highlighted in the bq79631EVM User Guide. These pins are all configured in the same manner as discussed in Measurement Display.



2 GUI Home Page

File	Options Tools Zoom Help			- 0	×
	Menu DEVICE & USB2JARY CONNECTED See BQ79616 SafeTi Practicus Monster With Integrated Hospitale, Ubium Tasarta Battery Packs		Hi there, What would yo Cell Monitor Cell Monitor Protector Diagnostics	u like to access? Balance Cells Communication	
R o	D LDEDANY/OneDemo device Hardware Connected.	GUI Uvers Guide EVM Uvers Guide EXE Forum		TICAL INSTITUT	MENTS

Figure 2-1. GUI Home Page

Before the user can see the above home page, there is a menu to select bridge device. Ensure the user selects whichever device is connected directly to the host PC via UART connection as the bridge device and any other devices that are connected through daisy chain as stack devices. This selection also applies if the user is attempting to use the GUI with the BQ75614-Q1, BQ79614-Q1, and BQ79612-Q1 devices. If the user is attempting to use the BQ79600-Q1 device, it is recommended to select the BQ79600-Q1 as the bridge device and all other EVMs as stack devices.

After selecting the bridge device, the GUI home page appears and offers a 3D view of the EVM in the middle. On the left is space for a video that will go over basic information to get started with the EVM and GUI, and this video is currently in development. Access buttons to different pieces of technical documentation are at the bottom of the home page and will be available after the device has been fully released online.

The pages of the GUI can be accessed either using the quick access buttons on the right or the sidebar, both highlighted in red in the figure above. It is generally best to start with the cell monitor page to properly 1) Wakeup 2) Auto address and 3) Start Polling the device.



3 Cell Monitor

Access this page from the home page by pressing the **Cell Monitor** icon or by selecting the icon on the sidebar. This page should normally be the first one that the user accesses in order to properly 1) Wakeup and 2) Auto Address 3) Start Polling to start to read back data. If the user does not properly Wakeup and Auto address in that sequence, then the device will be unresponsive.

From this page, the user can easily monitor VCELL, GPIO, and temperature measurements for all connected devices. The cell monitoring page also offers a visual representation of cell measurements and faults, enabling quick evaluation of cell conditions.

The figure belows shows cell measurements for three connected devices with all 16 cells enabled.

File	Options	Tools Help																		_ 0 ×
≡ м	enu																			
÷	CELL MON	ITOR												RUN AUT	O ADDRESS	\$	() WAKE U	P	() POW	ER DOWN
(1) 12	Refresh Rate :	500ms 🔻 🔲 St	op Polling	Ø VIEW	POLL SETTING	3 8			Comm Sampi AUX A	nunication Dire ling Type : ADC (for BAT) :	ction :	North Continuous Off ⓒ Co	South O Single ontinuous				LOW PAS	6.5 Hz 🔹	LPF) Bus Bar :	6.5 Hz 🔻
2	Cell Volt	tage GPIO/	Temperatur	e					Ce	ell Mo	onito	or Se	tting	S						
0	Display Hex	Values Disp	lay Unfiltered \	/alues 🔘											(i) Ali	cell values a	re in voltages	by default	MANAG	E COLUMNS
/	S No	Device Address	Cell 1	Cell 2	Cell 3	Cell 4	Cell 5	Cell 6	Cell 7	Cell 8	Cell 9	Cell 10	Cell 11	Cell 12	Cell 13	Cell 14	Cell 15	Cell 16	BAT	Fault <mark>cu</mark>
/	1	<u>0x00</u>	1.499	1.494	1.497	1.497	1.503	1.505	1.501	1.507	1.502	1.506	1.501	1.506	1.507	1.509	1.504	1.502	24.131	• 2
	2	<u>0x01</u>	1.312	1.314	1.307	1.314	1.313	1.309	1.315	1.310	1.318	1.316	1.318	1.317	1.323	1.319	1.320	1.322	21.051	• 2
	3	<u>0x02</u>	1.688	1.685	1.686	1.685	1.688	1.692	1.687	1.684	1.685	1.691	1.690	1.687	1.696	1.695	1.693	1.694	27.079	• 🛛
	Mos	surama	nt Di	enlai	,															
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₿ ශ	 USB2ANY/C 	DneDemo device Hardwar	e Connected.																4	Texas Instruments

Figure 3-1. BQ79616 GUI Cell Monitor Page

3.1 Starting up the Device

To get started with the BQ79616 EVM and GUI:

- 1. From the Cell Monitor page, Wake Up the devices and Run Auto Address.
- 2. Under View Poll Settings, specify the number of cells for each connected device.

CELL MONITOR		AUTO ADDRESS	U WAKE UP	O POWER DOWN
Refresh Rate : 500ms Start Polling VIEW POLL SETTINGS	Communication Direction : North South Sampling Type : Continuous Sir AUX ADC (for BAT) : Off Continuous 	gle	LOW PASS FILTERIN Cells : 6.5 Hz	G (LPF) Bus Bar: 6.5 Hz



This opens up the communication channel between the GUI and the device and communicates to the device which cells to measure and monitor for fault conditions.

3.2 Cell Monitor Settings

At the top of the cell monitoring page, the user can find relevant settings that allow for easy configuration of cell measurements. These settings control all connected devices.



- **Communication Direction:** Toggles between north and south, indicates direction of auto-addressing and tone propagation. In this release of the GUI, ensure that **North** is selected.
- Low Pass Filtering: Sets the cutoff frequency of the post-ADC digital filter for both cell and bus bar voltage measurements.
- Sampling Type: Toggles between single and continuous ADC run modes.
- AUX ADC: Enables AUX ADC continuous run mode to enable a valid BAT voltage measurement.

In **Single** run mode, each press of the **Start Polling** button updates the cell measurements just once. In **Continuous** mode, the cell measurements updates automatically at the selected refresh rate once polling has started.

3.3 Measurement Display

The measurement display is where voltage and temperature measurements can be found. Under the **Cell Voltage** tab, all 16 cell measurements can be monitored along with battery module voltage (if the resistor ladder is in use, this is the supply voltage) and bus bar voltage. The user also has the option to display the unconverted hexadecimal values from the corresponding VCELL*_HI/LO registers or the unfiltered cell measurements. The user can customize which of these items to display at any time using the **Manage Columns** window to select and deselect each cell. The user can display a cell balancing status indicator. If the **Faults** column is selected, clicking on the pop-up button next to the status light displays a detailed fault summary for each device.

				P	Iling A VIEW DOLL SETT	INCE		Campling Tune :	Continuous	O Cinalo		
				m . t	0x01 - Faults Power Raii ADC Comparison	CLEAR System O	V UV	 OT UT 	• Communication	• OTP		×
					Abo companson e							
Cell 14	Cell 15	Cell 16	BAT	Fault CLEAR	SY	STEM		СОММ	1		COMM 2	
					TWARN			STOP_DET		COMH_BIT		
0.000	0.000	0.000	0.000		TSHUT			COMMCLR DET		COMH RC		
					CTS			LIART RC		COMH BR		
0.000	0.000	0.000	0.000			-		UART RR	-	COMH TR		
					DROT			UART_RR	-			
0.000	0.000	0.000	0.000	• [2]	DRST			UARI_IR		COME_BIT		
					GPIO					COML_RC		
			Click	on pop-up	SHUTDOWN_REC	•		COMM	3	COML_RR	•	
		buttor		on pop up	LFO	•		HB_FAST		COML_TR	•	
		Dulloi	1 10 au					HB_FAIL				
		W	indow	for device				FTONE_DET				
								FCOMM_DET				

Figure 3-3. Device Fault Display in Cell Monitoring Page

Upon waking the device and running auto addressing, the user can expect to see the following faults:

- FAULT_SYS[DRST]: Triggered because of power on reset (POR)
- FAULT_POWER3[AVDDUV_DRST] & [AVDDREFUV_DRST]: Triggered because of POR
- If multiple devices are connected, FAULT_COMM3[FCOMM_DET]: Device detects faults in devices further up the stack. This fault does not trigger for the device at the top of the stack. (This can be disabled by using the Embedded Fault checkbox at the top of the menu to the right of the display hex values toggle button)

Clear faults by clicking the **Clear** button at the top of the fault display and start polling to update the fault and status lights. All of the fault lights should be green with the possible exception of the OTP fault, which triggers if polling has been run since the polling sequence sets the number of active cells. To mask this fault on a single device, you can either select the Set button (Mask CUST_CRC) at the top of the menu or navigate to the **Register Map** page, search for the register FAULT_MSK2, and change bits MSK_OTP_CRC and MSK_OTP_DATA to 1. To mask this fault for multiple connected devices, perform a broadcast write from the **Debug** page using the following data:

- Initialization byte: 0xD0
- Register Address: 0x11



• Data Bytes: 0x60

Press write. The all fault lights for the devices should turn green.

Under the **GPIO/Temperature** tab, the converted measurements of the eight GPIO pin voltages and the two die temperatures are displayed. Like the previous tab, the user can also customize the display using **Manage Columns** and display the unconverted hex values directly from the corresponding registers. To enable GPIO's for ADC measurements, refer to the below section for details on the GPIO/Temperature tab.

In the **GPIO/Temperature** tab, there is a **CONFIG** button on the right hand side shown below. This allows the user to configure all GPIOs for an individual device, this button and pop up window are shown below. Please take note that after you change any GPIO settings, you will need to Stop and then Start Polling in order to properly set your GPIO settings. This is because the device requires the Main ADC to be reset to acknowledge any setting changes.

File	Options Tools	Zoom	Help											_ 0
•	Menu													
ĥ	CELL MONITOR											AUTO ADDRESS	U WAKE UP	O POWER DOWN
)	Refresh Rate : 500ms	Stop	Polling	VIEW POLL SETT	INGS			Communic Sampling	ation Direction :	North Continuous	South		LOW PASS FILTERING (LPF)
1								AUX ADC (for BAT) :	Off 🔘	Continuous		Cells : 6.5 Hz 🔻	Bus Bar : 6.5 Hz 🔻
	Cell Voltage	GPI0/Ter	nperature											
9	Display Hex Values	Mask CU	ST_CRC 🗹 Embe	dded Fault 🗹 Er	nable TSREF							All ce	Il values are in voltages by default	MANAGE COLUMNS
7	Device Addres	s GPIO 1	GPIO 2	GPIO 3	GPIO 4	GPIO 5	GPIO 6	GPIO 7	GPIO 8	TSREF	Die Temp 1 (°C)	Die Temp 2 (°C)		
	<u>0x00</u>	2.4201	0.3874	-5.0001	-5.0001	-5.0001	-5.0001	-5.0001	-5.0001	4.9987	25.5500	51.5250		
G	D USB2ANY/OneDemo of Control	levice Hardware C	onnected.											🐺 Texas Instrum

Figure 3-4. GPIO/Temperature Tab



0x00 - GPIO

GPIO Config		SPI	
GPI01	GPI02	CPOL (SCLK Polarity)	CPHA (SCLK Edge Sample MISO)
ADC and OTUT inputs	ADC and OTUT inputs	▼ Idles low and clocks high ▼	Leading Clock Transition 🔹
GPI03	GPI04	State of SS	SPI Transaction Length
Disabled	▼ Disabled	▼ Output High ▼	24 bit 🔻
GPI05	GPI06		
Disabled	▼ Disabled		
GPI07	GPI08	SPI Transmit	Receive
Disabled	• Disabled	· · · · · · · · · · · · · · · · · · ·	0.4
GPI04 - GPI07 are no long	er used for SPI.]	
 Restart poll for the GPIO m 	node changes to take effect	EXECUTE SPI	

Figure 3-5. GPIO Config Pop Up Window

3.4 Detailed Device View

The user can navigate to a detailed device view by clicking on any of the underlined device addresses. At the top of the page, a grid representation of cell voltages and cell balancing status allows the user evaluate cell status at a glance. The right-hand side of the page houses a collapsible board details pane where the status bits of each board and fault summary are displayed. Clicking on any of the individual cells in the grid displays its OVUV fault status in the bottom right-hand corner.

On the bottom of the page is a time domain cell voltage plot. While polling is running, the plot updates itself with the new data points. The user can select which cell waveforms to display by clicking on the "+" button. There are several buttons in the upper right-hand corner of the plot area to interact with the plot. The user has multiple zoom options, can display numerical data within the plot area, and can save an image of the plot. You can also export this data vs. time to a .csv file by clicking the red export button at the top right of the plotting window. Note that the user must be polling to export any data over time.







4 Cell Balancing

Access the **Cell Balancing** page from the home page or the sidebar. From this page, the user can quickly configure cell balancing settings. This includes the ability to change balancing modes, set balancing timers, and set balancing stop conditions.

Note

This page will not be used for devices that do not utilize the cell balancing feature such as the BQ79631EVM and BQ79600EVM.

File	Options Tools Help							-	ð ×		
	fenu										
÷	CELL BALANCING FUN				PAUSE BALANCING	► STAR	T POLLING	CONTROL TONES	5		
[8]	COMM MODE: Broadcast	Choose the boards for whi	ch you want to configure.				FAULTS MONITORIN	G			
$\overline{L}_{\rm B}^{\rm i}$	BALANCING SEQUENCE	ALL CELLS: Os	- Set	Selec	tt Adjacent Cells 🔵 🛃 Save 🖍 Lo	d	Device Status & Fau	Its	•		
			BALANCING TIME		BALANCING TIME		STATUS				
\odot	Auto Manual	CELL 1	Os	CELL 9	Os		Polling CTS	CTL	:		
٩	 Manual mode will balance in successive sequence 	CELL 2	Os	CELL 10	Os		MAIN RUN	OTUT RUN	:		
	Duty Cycle Time 5s •	CELL 3	0s	CELL 11	0s		CB RUN				
	Cell Balance UV	CELL 4	0s	CELL 12	Os		FAULT SUMMARY				
	2.8 • V OTCB Threshold	CELL 5	Os	CELL 13	0\$		Power Rail OV UV Communication	OT UT	÷		
	24% • % of TSREF	CELL 6	Os	CELL 14	Os		ADC Comparison	Protector	•		
	4% • f cooloff	CELL 7	Os	CELL 15	Os						
	Make sure the protector is enabled and set to Round Robin mode	CELL 8	Os	CELL 16	Os						
	Stop Balancing on Fault	Stop Balancing on Fault Vou have not selected adjacent cells									
<i>≣</i> c	 Hardware not Connected. Please plug your Targ 	get Device into your computer's US	B port, and click the Connect icon at left.					🐺 Texas In	STRUMENTS		

Figure 4-1. BQ79616 GUI Cell Balancing Page

4.1 Balancing Timetable

Here, the timers for all of the cells and the module can be set. At the top left, the user can set a timer for **All Cells**. In addition, the user can save the current timing profile as a .txt file and can load from it at any time. In **Manual** mode, check boxes appear and are used to evaluate whether or not a cell combination is valid.

To manually turn off a cell for balancing, set the timer to 0 seconds and press Run.

4.2 Balancing Sequence

The BQ79616 supports two cell balancing modes: auto and manual. Below are descriptions on how to get started with either mode.

4.2.1 Auto Balancing

In auto cell balancing, cells are balanced in an odd and even sequence, switching every duty cycle period. Because the device automatically sets up this balancing sequence, there are no constraints placed on which cells can be selected for balancing in auto balancing mode.

To configure auto balancing, set cell timers and select a **Duty Cycle Time**.



EXAS



4.2.2 Manual Balancing

In manual cell balancing, all cells selected by the user are balanced at the same time. Because of this, there must be constraints placed on the cell combinations that can be chosen. The BQ79616 has two conditions that must be satisfied by a valid cell selection.

- No more than eight cells can be selected at once.
- If the user would like to Select Adjacent Cells, no more than two consecutive cells can be selected. With the setting disabled, no adjacent cells can be selected.

If any of these conditions are not satisfied, the GUI alerts the user that the cell combination is invalid.

To configure manual balancing, select a valid cell combination and set cell timers.

4.3 Thresholds

The next step in configuring cell balancing is setting voltage and temperature thresholds, which BQ79616 can use to stop and pause cell balancing, respectively. The fields under the **Thresholds** section control the VCBDONE and OTCB thresholds. The user can also control whether or not balancing aborts upon the detection of an unmasked fault by checking **Stop Balancing on Fault**.

4.3.1 VCBDONE Threshold

Here, the VCBDONE threshold can be set, and this stop threshold is monitored for each individual cell. This means that each cell continues balancing until VCBDONE is reached independent of all other cells.

This threshold, when met, stops the balancing of each cell independent of how much time is left on the timer of the cell and also marks balancing as complete.

4.3.2 OTCB Threshold

During passive balancing, heat is generated in the internal CBFET and external balancing resistors. Because of this, the BQ79616 features an overtemperature threshold specific to cell balancing, called OTCB. If this temperature threshold is reached during cell balancing at any point, cell balancing immediately pauses for all cells. OTCB is represented as a percentage of TSREF. Balancing resumes when the temperature drops by the amount specified by **COOLOFF**.

Note that to use this feature, the OT protector must be enabled and set to round robin mode.

4.4 Start Balancing

After the user has successfully configured the balancing sequence and thresholds, the user can start balancing by pressing **Run**. Whenever balancing settings are changed, the user must press **Run** again to issue the changes to the device.

Balancing can be paused and resumed by pressing **Pause Balancing**. Note that this does not stop balancing, and the **CB RUN** indicator will still be green.

5 Protector

From this page, the user can set the voltage and temperature protection thresholds for any device in the system. Also present on this page are status and fault lights to monitor the condition of the connected devices.

o × File Options Tools ■ Menu ŧ PROTECTOR O cor (¹) POWER DOWN FALLETS MONITORING 18 COMM MODE: Broadcast Choose the boards for which you want to configure $\overline{\mathbb{T}}^{1}_{\mathbb{H}}$ **Basic Config** Advanced Config ų. VOLTAGE PROTECTION 0x00 • Device Sta atus & Faults STATUS 0 2700 mV 1200 mV Pollin сті 1 CTS AUX RUN MAIN RUN . OTUT RUN 1 OVUV RUN CB DONE . CB RUN . OTUT GPIO Selection TEMPERATURE PROTECTION FAULT SUMMARY GPI0 1 GPI0 2 GPI0 3 GPI0 4 GPIO 5 GPIO 6 GPIO 7 GPIO : . OT UT . OV UV Enable GPIOs for ADC . . OTP measurement and protection iparison 🔵 Protecto Cつ ▲ Hardware not Ce

Figure 5-1. BQ79616 GUI Protector Page

5.1 Threshold Settings

Under the **Voltage Protection** section, the user can configure the OV and UV thresholds. Voltages beyond these thresholds trigger faults, and the faults are reflected in the status lights on the right side of the page. Use the drop-down menu to edit the thresholds and press **Write OVUV** to write to the corresponding registers.

In the **Temperature Protection** section, the user can set the OT and UT thresholds. To use the temperature protections, first enable TSREF via the **Register Map** page by setting bit 0 of register CONTROL2 to 1. Like the OTCB threshold, the OTUT thresholds are represented as percentages of TSREF. Once set, press **Write OTUT** to write the thresholds to the corresponding register and start the protectors in the selected run mode.

In addition, the GPIOs that are connected to external thermistors must be selected in the selection box. This configures the GPIOs for both ADC readings and OTUT protection and tells the device not to ignore the fault detection result.

5.2 Protector Run Modes

The user can also change protector modes from this page. The standard protection mode is **Round Robin**. In round robin mode, all active cells and GPIOs are sequentially evaluated against the corresponding protection thresholds, and faults are triggered if a threshold is crossed.

The BQ79616 also supports a protector mode called **Single Channel Run**. In this mode, the user can lock the OVUV and OTUT protectors to a single cell and GPIO, respectively. The user can also configure the threshold lock setting.

The last supported protector modes are the OVUV and OTUT built-in self test cycles.





6 Communication Control

File	Options Tools Help			<u></u>	o ×
≡ №	Menu				
÷	COMMUNICATION CONTROL	POLLING	🙆 co	NTROL TONES	
[8]	COMM MODE: Broadcast Choose the boards for which you want to configure.	FAULTS MONIT	TORING		
2 2	Communication Config Control Register & Tones	0x00 0x02	:	0x01	•
2	COMMUNICATION CONFIGURATION	Device Status	& Faults	01	×00 ▼
9	Tx Holdoff Different Long Timeout Long Timeout Action	STATUS			
٩		Polling CTS	:	CTL AUX RUN	:
/		MAIN RUN	٠	OTUT RUN	•
	Debug Control COML Transmitter COML Receiver COMH Transmitter COMH Receiver ENULE DEMLE USER DAISY_EN USER UART_EN UART Transmitter UART Mirror	OVUV RUN CB RUN	:	CB DONE	•
		FAULT SUMM	ARY		
		Power Rail	٠	System	•
		OV UV	00	OT UT	:
		ADC Compari	son 🔵	Protector	
<i>8</i> G	> ▲ USB2ANY/OneDemo device Hardware Connected.			🖗 Texas Inst	TRUMENTS

Figure 6-1. BQ79616 GUI Communication Control Page

6.1 Communication Configuration

Under this tab, the user can configure the following settings:

- **TX Holdoff:** Integer between 0–255, specifies the number of bit periods the UART transmitter waits after the last bit reception before starting transmission.
- Communication Timeout Thresholds
 - Short Comm Timeout: Timer resets every time a valid response or command frame is received, sets CTS system fault when timer expires.
 - Long Comm Timeout: Allows the host to put the device in SLEEP or SHUTDOWN modes for power saving, timer resets every time a valid response or command frame is received, executes Long Timeout Action when timer expires.
- Long Timeout Action: On a long timeout, either send the device to SLEEP and set a CTL system fault or send the device to SHUTDOWN.

The GUI also supports communication debug mode of the BQ79616, which can be enabled via the **Debug Control** slider. Debug mode grants the user control of the COMH/L receivers and transmitters, and also allows the user to toggle the **UART TX** and **UART Mirror** functions on stack devices. Note that the base device should always have its UART TX enabled and UART mirror disabled, so these toggles are disabled for broadcast and base device communication modes.

6.2 Control Register and Tones

This tab provides the user with the ability to both send tones and change bits in the CONTROL1 register of a device via buttons labeled by function.

A tone is a set of pulses that travels through COMH and COML ports. For the BQ79616, WAKE and SLEEPtoACTIVE tones are propagable, meaning that once one device receives either tone, it generates the same tone to send to the next device up the stack. Conversely, SHUTDOWN and HW RESET tones are non-



propagable, meaning that a tone is not generated to the next device in the stack. Therefore, to shutdown or reset all connected devices, all CONTROL1 registers must be written to.

This tab also features other relevant tones and CONTROL1 bits. All buttons on this tab are grouped by general function: wake, shutdown, reset, sleep, and communication. Variations of each function exist to allow greater flexibility in controlling the connected devices.

7 Diagnostics

The Diagnostics tab provides the user a sneak preview into the workings of the system level diagnostics that the BQ devices provide. These are highly important functions for designing a safe, ASIL rated system.

On the left hand side, there are start/stop buttons that turn on/off the MAIN & AUX ADCs that are both required to be ON for the device to allow diagnostics to be completed. Below the start/stop buttons are AUX ADC settings that the user can configure to lock to certain channels, gpios, or change the settling time.

After the user has started the main/aux adcs and set desired aux settings the user can select a diagnostic mode in the dropdown above the green "start diagnostics" button. As an example, the Cell Voltage Measurement diagnostic is shown below. This diagnostic will do a simple comparison between the Main ADC voltage level and the AUX ADC level for the selected AUX channel(s). Once the user selects their desired voltage threshold, select green"Start Diagnostics" button which will either trigger a notification that the diagnostic completed or aborted. See the BQ79616-Q1 datasheet for details on abort conditions. If the diagnostic completed, it will update the VCCB1, VCCB2, LPF text to red or green. Green meaning that there are no failures in the corresponding register and red indicating that there are failures. Hover over VCCB1 to see the linked register (FAULT_COMP_VCCB1). If the VCCB1 text is red, then the user can then navigate to the register map and search for FAULT_COMP_VCCB1 to see the exact bit level cell failures.

File	Options Tools Zoom Help														_ Ø ×
	Venu														
ŧ	DIAGNOSTICS										AUTO	ADDRESS	START POLLING	ONTROL	TONES
(8)	COMM MODE: Broadcast	▼ Choose the	boards for which you	want to configure											
\P^1_0	MAIN & AUXILLARY ADC	DIAGNOSTICS													
1. V	0 0	MODE		Voltage Three	hold										
Ø	Start Stop	Cell Voltage N	Measurement 🔹	10 mV		•	Inject LPF Fault								
▲	Continually run the MAIN & AUX ADC														
છ	Main & AUX ADC must be enabled in	Start Diag	nostics VC	CB1 VCCE	2 LPF										
ф	AUX_CELL_SEL														
٩	All Active Channels	MEASUREMENT	rs											Display Hex Va	lues 🗩
/	AUX_GPI0_SEL	DEVICE	AUX_CELL	GPIO I	BAT REF	L VBG2	LPBG5	AVAO_REF	AVDD_REF	OV_DAC	UV_DAC	OTCB_DAC	UT_DAC	VCBDONE_DAC	VCM1
	Cycle GPIO	0x00	-6.2498	-5.0001 21	.6886 1.10	1.1014	1.2195	2.4475	2.4437	-5.0001	-5.0001	-5.0001	-5.0001	-5.0001	2.5032
	AUX_SETTLING TIME														
	4.3 ms •														
<i> e</i>	 USB2ANY/OneDermo device Hardware Connected 	ıd.												ten 🕂	AS INSTRUMENTS



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8 Command Sequence

The Command Sequence tab allows the user to easily create custom commands and organize them into a script. This allows the user flexibility to run different sets of sequences and add delays in between.

On the left side is where the user actually inputs the command of choice and can send the frame to the command dialog window in the center. To get more detailed information on how the command dialog menu works, please select the "Help" icon on the bottom right below the command dialog window. The user can send several commands to this dialog window and then Save or Load this as a .txt file. Pressing the Start button will actually send the commands to the device and the data will be shown in the transaction log on the right hand side of the page. This log can be saved to a file. The user can also save several scripts to quick access buttons using the empty slots at the top middle of the page.

Commands can be written to any device in the system through this page by changing the device address on the left side of the page.

File	Options Tools Zoom	Help										-	ð ×
≡ м	Menu												
*	COMMAND SEQUENCE							AUTO ADD	RESS	► STAR	POLLING	ONTROL TONES	
(8)	COMMAND FRAME		QUICK FILE ACCESS				LOG SETTINGS						
맨	Command	Single Device Read	Click on a empty slot to a dialog when the assigned	assign a sequence file. d slot is clicked again	File will be loaded to th	e command	Log Path						
12	Device Address (hex)	00	SLOT-1	SLOT-2	SLOT-3		🖌 Auto Log						
Ø	Register Address (hex)	0000	SLOT-4	SLOT-5	SLOT-6		TRANSACTION LOG						
A	No. of bytes to send (decimal)	1	COMMAND DIALOG		Command	Dev Addr	Reg Add	Length	CRC	Data (hex)			
Ŷ	No. of registers to read (decimal)	1	1. 80 00 00 00	00 25 DE			Single Device Read	00	00 00	1	25 DE	00	
ø	Bytes to write (hex)	00	2. DELAY 100					00	00 00	1	24 00	00	
٩	Calculated CRC (hex)	25 DE											
1	Command frame to send (hex)	80 00 00 00 00 25 DE											
	SEND COMMAND F	RAME TO STACK											
	Delay (ms)	100											
	SEND DELAY TO STACK												
				00	00	?				4			
			Clear Save Loa	d Edit Rese Com	et Start Stop m	Help			Clear	Save			

Figure 8-1. Command Sequence Tab

GD - USB2ANY/OneDemo device H



9 Faults

The Faults tab provides the user a quick access page to view all current status of the device faults and an easier method to mask faults. The user can select the SHOW button on the right hand side underneath the FAULT DETAILS column in order to view the faults in more detail, reset certain fault registers, and mask them.

nu											
FAULTS						DEVICE: 0x00		MASK ALL	UNMASK AL	L R	ESET ALL
(i) Fault details are auto-upda	ted while this page is acti	ve				REGISTER NAME		ADDRESS	VALUE	MASK	RESET
Device Address	FAULT_PROT	FAULT_COMP_ADC	FAULT_OTP	FAULT_COMM	FAULT_OTUT	FAULT COMM 1	Z	0x530	0x00		C
						FAULT COMM 2	Z	0x531	0x00		C
0x00	•	٠	•	•	•	FAULT COMM 3	Z	0x532	0x00		C
						FAULT OTP	Z	0x535	0x10		C
						FAULT SYS	Z	0x536	0x10		C
						FAULT PROT 1	Z	0x53A	0x00		C
						FAULT PROT 2	Z	0x53B	0x00		Ŭ
						FAULT OV 1	Z	0x53C	0x00		C
						FAULT OV 2	Ø	0x53D	0x00		0
						FAULT UV 1	Z	0x53E	0x00		C
						FAULT UV 2	Z	0x53F	0x00		
						FAULT OT	Z	0x540	0x00		G
						FAULT UT	Z	0x541	0x00		C
						FAULT COMP GPIO	Z	0x543	0x00		
						FAUT COMP VCCB 1	Ø	0x545	0x00		
						FAULT COMP VCCB 2	Ľ	0x546	0x00		
						FAUT COMP VC OW 1	Z	0x548	0x00		
						FAULT COMP VC OW 2	C 7	0×E40	0+00		

Figure 9-1. Faults Tab



10 Register Map

The register map page displays all relevant writeable and readable registers for any devices being used. The registers are grouped by general function, that is measurements, protection, and so forth. If there are multiple connected devices, the device being displayed can be switched with a drop-down menu at the top of the page.

File	Options Tools He	elp											_ 🗗 ×
=	Menu												
÷	Register Map						Auto Read	Off	٠	READ REGIS	TER RE/	ND ALL REGISTERS	Immediate Write 🔻
1	Q Search Registers by name or addre	ss (0x)											
, E ,	Regis	ter Name	Address	Value				Bi	ts				FIELD VIEW
					7	6	5	4	3	2	1	0	DIRU_ADDR_OTP
Υ _B	V Control		0x00	0x00			0	0	0	0	0	0	Control / DIR0_ADDR_OTP / RESERVED[7:6]
	DIRU_ADDR_OTP		0x00	0×00			0	0	0	0	0	0	RESERVED 600
-17	DIRI_ADDR_OTF		0x07	0×54		1	0	1	0	1	0	0	Control (DIRO ADDR OTR (DIRO ADDR OTR[5:0]
	DEV_CONF		0x02	0x34		,	0	1	0	,	0	0	
9	ACTIVE_CELL		0x03	0x00			-		0	0	0	0	uxiou
	OTP_SPARE12		0x04	UXUU	0	U	0	0	0	0	0	0	
a.	TX_HOLD_OFF		0X14	0000	0	0	0	0	0	0	0	0	
	DIR0_ADDR		0x306	0x00			0	0	0	0	0	0	
	DIR1_ADDR		0x307	0x00			0	0	0	0	0	0	
•	COMM_CTRL		0x308	0x00							0	0	
	CONTROL1		0x309	0x00	0	0	0	0	0	0	0	0	
	CONTROL2		0x30A	0x00							0	0	
	v ADC Settings												
	ADC_CONF1		0x07	0x00	0	0	0	0	0	0	0	0	
	ADC_CONF2		0x08	0x00			0	0	0	0	0	0	
	MAIN_ADC_GAIN		0x15	0x00	0	0	0	0	0	0	0	0	
	MAIN_ADC_OFFSET		0x16	0x00	0	0	0	0	0	0	0	0	
	AUX_ADC_GAIN		0x17	0x00	0	0	0	0	0	0	0	0	
	AUX_ADC_OFFSET		0x18	0x00	0	0	0	0	0	0	0	0	
	CS_ADC_GAIN		0x19	0x00	0	0	0	0	0	0	0	0	
	CS_ADC_OFFSET		0x1A	0x00	0	0	0	0	0	0	0	0	
	ADC CTRL1		0x30D	0x00		0	0	0	0	0	0	0	
	ADC CTRI 2		0x30E	0x00				0	0	0	0	0	
	ADC CTRL3		0x30E	0x00		0	0	0	0	0	0	0	
	* Status		0×529	0×00				0	0	0	0	0	
	ADC STATS		0×520	0×00			0	0	0	0	0	0	
	ADU_DIAIZ		0x529	0×00	0	0	0	0	0	0	0	0	
	GPIU_STAT		0x52A	0000	0	0	0	0	0	0	0	0	
	BALSIAI		UX52B	00x00	0	0	U	0	0	U	0	0	
	DEV_STAT		0x52C	0x00	1	0	0	0	0	0	0	0 🗸	Powered By GUI Composer
🗐 c	D A Hardware not Connected. Please	ase plug your Target Device into your compute	r's USB port, and click the Connect icc	n at left.									😼 Texas Instrument

Figure 10-1. BQ79616 GUI Register Map Page

10.1 Register Display

In the register display, registers are displayed with their names, addresses, and values. Double clicking on any bit flips the value and immediately writes it to the corresponding register. In **Deferred Write** mode, register writes can be queued up and executed by pressing either write button. Upon selecting a register, an information button next to the register name appears. Clicking on the button reveals more about the selected register, including the default value and information about the fields within the register.

A field view also opens up on the right side of the screen that allows the user to directly edit certain register fields. Selecting a field in the field view also displays an information button. Clicking on it displays the name of a field, the bits it occupies within the register, and its type.



gist Dote	Options Tools	Help								- 0
Register Name Address Value 7 6 5 4 3 2 1 0	Register Map				De	ice 0x00 ¥ Auto	Read Off	READ REG	ISTER READ ALL REGIST	RS Non Costs Non All Costs Immediate Write •
Page Address Value Tell Feature Featur	🞗 Search Registers by name	e or address (0x)								
vice	Regi	ster Name	Address	Value			Bits			FIELD VIEW
UV_CTRL Image: Description Product 714, / CXDONE_THR Product 714, / CXDONE_THR </td <td>OVUV_CTRL</td> <td></td> <td>0x32C</td> <td>0x00</td> <td>7</td> <td>5 5</td> <td>4 3</td> <td>2</td> <td>1 0</td> <td>OVOV_CIRE</td>	OVUV_CTRL		0x32C	0x00	7	5 5	4 3	2	1 0	OVOV_CIRE
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	7:7 VCBDONE_THR VC	CBDONE_THR								Protector / OVUV_CTRL / OVUV_GO[2]
2 0/0/U_G0 0/0/U_G0 Predector / 0/U/U_GDTR_ / 0/U/U_MODE[1:0] 0 0/0/U_MODE 0/U/U_MODE Predector / 0/U_GTTR_ / 0/U/U_MODE[1:0]	5:3 OVUV_LOCK 0	VUV_LOCK								OVUV_GO
	2:2 0VUV_G0 01	VUV_GO								Protector / OVUV_CTRL / OVUV_MODE[1:0]
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Figure 10-2. BQ79616 GUI Register Map Field View

The registers are grouped by general function, making it easy to find relevant registers when debugging or operating the device via the register map. The user can also search for the registers in the search bar by name or address (leading with 0x...).

10.2 Register Read

An individual register can be read by selecting a register and pressing **Read Register**, and pressing **Read All Registers** reads all registers for a single device. **Auto-read** can be enabled to update the values in the register map at a selected time interval.



11 Debug

From this page, the user can directly perform register reads and writes to the connected devices. This provides some extended functionality that is not present on the GUI front end.

File	Options Tools Help			_ 0 ×
≡ Me	enu			
*	Debug			
18) 193	Initialisation byte	No. of devices connected	WAKE UP	
12	Device address	AUTO ADDRESS	SLEEP TO ACTIVE	
Ø	0x 0020	Response bytes	COMM CLEAR	
٩	Data bytes ^{0x} 00		HW RESET	
	Request bytes		SHUT DOWN ALL	
	WRITE			
				Powered By GUI Composer**

Figure 11-1. BQ79616 GUI Debug Page

11.1 Write and Read

The first step in communicating with the device is to use the **initialization byte** that corresponds to the intended communication type. A summary of the different initialization bytes available to the user can be found in the table below.

Initialization Byte ⁽¹⁾
0x80
0x9*
0xA0
0xB*
0xC0
0xD*

Table 11-1. Initialization Bytes

(1) For write operations, replace asterisks with the number of bytes to be written minus one up to a maximum of 8 bytes written simultaneously.

Second, the user specifies the device to be communicated to via the **Device Address** field. This field is only available for single device communication, as the stack and broadcast communication modes communicate to multiple devices. Third, the user specifies the **Register Address** to be written to. Last, the user fills out the **Data Bytes** field. For a read, the data bytes should be the number of bytes to be read minus one. For a write, the data bytes are the bytes to be written.

For both reads and writes, the user should then press the **Write** button. For read commands, a write button press also performs a read from the device, and the response is populated in the response bytes window.



12 Current Sensing

For users attempting to take current sense measurements using the SRP/SRN pins of the BQ75614-Q1 or BQ79631-Q1 device, the steps are explained below using this GUI:

1) First open the GUI and select the bq75614-Q1 or bq79631-Q1 device as the bridge. Then initialize the device in the "CELL MONITOR" tab, and begin ADC measurements by pressing: 1. WAKE UP 2. AUTO ADDRESS 3. Start Polling

2) Get a voltage reading from the CS ADC via two columns in the cell monitor page. There is a 16 bit reading and a 24 bit reading populated in the two columns that will update by default when polling is enabled.

3) If the user would like to convert manually, they can use the values from the CURRENT_HI/MID/LO registers (hex value of the registers to read are 0x5D6-0x5D8). Then the value needs to be converted to decimal and multiplied by 14.9nV from the datasheet (when using 24-bit result).

13 Troubleshooting

13.1 Common Issues

- If the GUI does not prompt the Hardware Connected indicator at the bottom left, it is likely a USB2ANY firmware issue or connection. Please restart the steps in section 1.1.3 and carefully read over the proper steps.
- 2. If the GUI ever has any issues in terms of updating data or buttons not responding:
 - a. Right click anywhere on the GUI and **Reload app**. Wake up the devices and run auto addressing.
 - b. If the problem persists, reload the latest firmware on the USB2ANY via the USB2ANY Firmware Loader even if it has already been loaded previously.

13.2 Best Practices

The best method to first ensure proper communication is to first navigate to the cell monitor page and then 1) Click Wake Up 2) Click Auto Address (the # of devices you have powered on the stack should populate into the table with all 0's) 3) Start Polling and ensure data is updating in the table.

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