ABSTRACT
This user’s guide describes the characteristics, operation, and use of TI’s TPS62A02 and TPS62A02A evaluation modules (EVM). These EVMs are designed to help the user easily evaluate and test the operation and functionality of the TPS62A02 and TPS62A02A buck converters. The EVMs convert a 2.5-V to 5.5-V input voltage to a regulated 1.8-V output voltage that delivers up to 2-A maximum. This user’s guide includes setup instructions for the following:

- Hardware
- A printed-circuit board (PCB) layout
- Schematic diagram
- Bill of materials (BOM)
- Test results of the EVM

Throughout this document, the TPS62A02EVM-197 is used as an abbreviation representing the TPS62A02EVM-197 (001) and TPS62A02AEVM-197 (002).

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Trademarks
All trademarks are the property of their respective owners.
1 Introduction

The TPS62A02 and TPS62A02A are synchronous step-down buck DC-DC converters optimized for high efficiency and compact solution size. The TPS62A02 and TPS62A02A delivers an output current up to 2 A. The TPS62A02A variant operates in forced PWM mode (FPWM) across the whole load current range. The TPS62A02EVM-197 and TPS62A02AEVM-197 are available in a 1.6-mm × 1.6-mm SOT563 package.

1.1 Performance Specification

Table 1-1 provides a summary of the TPS62A02 and TPS62A02A performance specifications.

<table>
<thead>
<tr>
<th>Specification</th>
<th>Test Conditions</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input voltage</td>
<td></td>
<td>2.5</td>
<td>5.5</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Output voltage setpoint</td>
<td></td>
<td>1.8</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Output current</td>
<td>TPS62A02EVM-197</td>
<td>0</td>
<td>2</td>
<td></td>
<td>A</td>
</tr>
<tr>
<td></td>
<td>TPS62A02AEVM-197</td>
<td>0</td>
<td>2</td>
<td></td>
<td>A</td>
</tr>
</tbody>
</table>

1.2 Modifications

The PCB for this EVM is designed to accommodate the adjustable voltage version of this IC. On the EVM, additional input and output capacitors can also be added. Finally, a feedforward capacitor can be added as well.

1.2.1 Input and Output Capacitors

C7 is provided for an additional input capacitor. This capacitor is not required for proper operation but can be used to reduce the input voltage ripple.

C5, C6, and C8 are provided for additional output capacitors. These capacitors are not required for proper operation but can be used to reduce the output voltage ripple and to improve the load transient response. The output capacitance must remain within the recommended range in the device data sheet for proper operation.

1.2.2 Feedforward Capacitor

C4 is a feedforward capacitor. This capacitor is not required for proper operation but can be used to improve the load transient performance.

2 Setup

This section describes how to properly use the TPS62A02EVM-197 and TPS62A02AEVM-197.

2.1 Connector Descriptions

- **J1, Pin 1 and 2 – VIN**  Positive input voltage connection from the input supply for the EVM
- **J1, Pin 3 and 4 – S+/S–** Input voltage sense connections, measure the input voltage at this point
- **J1, Pin 5 and 6 – GND**  Input return connection from the input supply for the EVM
- **J2, Pin 1 and 2 – VOUT**  Positive output voltage connection
- **J2, Pin 3 and 4 – S+/S–** Output voltage sense connections, measure the output voltage at this point
- **J2, Pin 5 and 6 – GND**  Output return connection
- **J3 – PG/GND**  The PG output appears on pin 1 of this header with a convenient ground on pin 2.
- **JP1 – EN**  EN pin jumper. Place the supplied jumper across ON and EN to turn on the IC. Place the jumper across OFF and EN to turn off the IC.
- **JP2 – PG Pullup Voltage**  PG pin pullup voltage jumper. Place the supplied jumper on JP2 to connect the PG pin pullup resistor to the output voltage. Alternatively, the jumper can be removed and a different voltage can be supplied on pin 1 to pull up the PG pin to a different level. This externally applied voltage must remain below 5.5 V.

2.2 Hardware Setup

To operate the EVM, set jumper JP1 to the desired positions per Section 2.1. Connect the input supply to J1 and connect the load to J2.
3 Board Layout

This section provides the board layout and illustrations of the TPS62A02EVM-197, which is valid for variant TPS62A02AEVM-197 as well.

![Figure 3-1. Top View Mask](image1)

![Figure 3-2. Top Layer](image2)
4 TPS62A02EVM-197 Test Results

Figure 4-1 shows the efficiency results performed with the inductor part number mentioned in the BOM. See the device data sheet for the rest of the performance of this EVM.
5 Schematic and Bill of Materials

This section provides the TPS62A02EVM-197 schematic and bill of materials.

5.1 Schematic

Figure 5-1 illustrates the EVM schematic of TPS62A02EVM-197, which is also valid for the TPS62A02AEVM-197 variant.

Figure 5-1. TPS62A02EVM-197 Schematic
### 5.2 Bill of Materials

Table 5-1 lists the BOM for this EVM.

<table>
<thead>
<tr>
<th>Quantity</th>
<th>Ref Des</th>
<th>Value</th>
<th>Description</th>
<th>Size</th>
<th>Part Number</th>
<th>MFR</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>C1</td>
<td>4.7 μF</td>
<td>Capacitor, Ceramic, 10 V, X7R, ±10%</td>
<td>0805</td>
<td>GRM21BR71A475KE51L</td>
<td>Murata</td>
</tr>
<tr>
<td>1</td>
<td>C2</td>
<td>22 μF</td>
<td>Capacitor, Ceramic, 10 V, X7R, ±10%</td>
<td>0805</td>
<td>GRM21BZ71A226ME15L</td>
<td>Murata</td>
</tr>
<tr>
<td>1</td>
<td>C3</td>
<td>47 μF</td>
<td>Capacitor, Ceramic, 10 V, X7R, ±20%</td>
<td>1210</td>
<td>GRM32ER71A476ME15L</td>
<td>Murata</td>
</tr>
<tr>
<td>1</td>
<td>C4(1)</td>
<td>120 pF</td>
<td>Capacitor, Ceramic, 50 V, C0G/NP0, ±5%</td>
<td>0603</td>
<td>GRM1885C1H121J01D</td>
<td>Murata</td>
</tr>
<tr>
<td>1</td>
<td>L1</td>
<td>1 μH</td>
<td>Inductor, Shielded, 4.9 A, 0.0213 Ω</td>
<td>3.65 × 3.35 × 1.5 mm</td>
<td>XGL3515-102MEC</td>
<td>Coilcraft</td>
</tr>
<tr>
<td>1</td>
<td>R1</td>
<td>200 k</td>
<td>Resistor, Chip, 0.1 W, 1%</td>
<td>0603</td>
<td>Std</td>
<td>Std</td>
</tr>
<tr>
<td>1</td>
<td>R2</td>
<td>100 k</td>
<td>Resistor, Chip, 0.1 W, 1%</td>
<td>0603</td>
<td>Std</td>
<td>Std</td>
</tr>
<tr>
<td>1</td>
<td>R3</td>
<td>499 k</td>
<td>Resistor, Chip, 0.1 W, 1%</td>
<td>0603</td>
<td>Std</td>
<td>Std</td>
</tr>
<tr>
<td>1</td>
<td>U1</td>
<td>TPS62A02</td>
<td>IC, 5.5-V, 2-A Step-Down Converter</td>
<td>1.6 × 1.6 mm</td>
<td>TPS62A02</td>
<td>TI</td>
</tr>
<tr>
<td>0</td>
<td>U1</td>
<td>TPS62A02A</td>
<td>IC, 5.5-V, 2-A Step-Down Converter with forced PWM operation</td>
<td>1.6 × 1.6 mm</td>
<td>TPS62A02A</td>
<td>TI</td>
</tr>
</tbody>
</table>

(1) C4 is feedforward capacitor, which is optional. The device is fully functional without C4.
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Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI’s recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI’s instructions for electrical ratings of interface circuits such as input, output and electrical loads.

**NOTE:**

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FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION
This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.
Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

• Reorient or relocate the receiving antenna.
• Increase the separation between the equipment and receiver.
• Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
• Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:
This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:
(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:
Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:
Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.
Concernant les EVMs avec antennes détachables
Conformément à la réglementation d’Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d’un type et d’un gain maximal (ou inférieur) approuvé pour l’émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l’intention des autres utilisateurs, il faut choisir le type d’antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l’intensité nécessaire à l’établissement d’une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d’antenne énumérés dans le manuel d’usage et ayant un gain admissible maximal et l’impédance requise pour chaque type d’antenne. Les types d’antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l’exploitation de l’émetteur.

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3.3.1 Notice for EVMs delivered in Japan: Please see http://www.tij.co.jp/sds/ti_ja/general/eStore/notice_01.page

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If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry’s Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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