Description
The TPS51385 is a single, D-CAP3™ control mode, synchronous buck converter requiring a very low external component count. TPS51385EVM is a fully assembled and tested circuit for evaluating the TPS51385 converter. This EVM operates typical 12-V input, and provides a 5.1-V output at 7-A.

Features
- 4.5-V to 24-V input voltage range
- 0.6-V to 5.5-V output voltage range
- Support 7-A continuous output current
- Selectable PSM and OOA mode under light load
- Fast load transient response

Applications
- Notebook and PC computers
- Ultrabook, tablet computers
- TV, STB
- Distributed power systems

Top View of TPS51385EVM
1 Evaluation Module Overview

1.1 Introduction

The TPS51385 evaluation module (EVM) is a single, synchronous buck converter providing 5.1-V at 7-A from a 6-V to 24-V input. This user’s guide describes the TPS51385EVM performance.

<table>
<thead>
<tr>
<th>EVM</th>
<th>Input Voltage ($V_{IN}$) Range</th>
<th>Output Current ($I_{OUT}$) Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>TPS51385EVM</td>
<td>6 V to 24 V</td>
<td>0 A to 7 A</td>
</tr>
</tbody>
</table>

This user's guide describes the characteristics, operation, and use of the Texas Instruments TPS51385 evaluation module (EVM). The TPS51385EVM is designed to help user easily evaluate and test the operation and functionality of the TPS51385. This user's guide includes the following:

- Setup instructions for the hardware
- Printed-circuit board layouts for the EVM
- Schematic diagram
- Bills of material
- Test results for the EVM

1.2 Kit Contents

- One TPS51385EVM board
- EVM disclaimer Read Me

1.3 Specification

A summary of the TPS51385EVM performance specifications is provided in Table 1-2. Specifications are given for 12-V input voltage and 5.1-V output voltage, unless otherwise noted. The ambient temperature is 25°C for all measurement, unless otherwise noted.

<table>
<thead>
<tr>
<th>Specifications</th>
<th>Test Conditions</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input voltage</td>
<td></td>
<td>6</td>
<td>12</td>
<td>24</td>
<td>V</td>
</tr>
<tr>
<td>Output voltage</td>
<td></td>
<td>5.1</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Operating frequency</td>
<td>$V_{IN} = 12$ V, $I_{OUT} = 7$ A</td>
<td>1000</td>
<td></td>
<td></td>
<td>kHz</td>
</tr>
<tr>
<td>Output current range</td>
<td></td>
<td>0</td>
<td>7</td>
<td></td>
<td>A</td>
</tr>
<tr>
<td>Overcurrent limit</td>
<td>$V_{IN} = 12$ V, $L_{OUT} = 1$ µH</td>
<td></td>
<td>8.8</td>
<td></td>
<td>A</td>
</tr>
<tr>
<td>Output ripple voltage</td>
<td>$V_{IN} = 12$ V, $I_{OUT} = 7$ A</td>
<td></td>
<td>20</td>
<td></td>
<td>mV_{PP}</td>
</tr>
</tbody>
</table>

1.4 Device Information

TPS51385 is the main device on the EVM board. The TPS51385 is a single, D-CAP3™ control mode, synchronous buck converter requiring a very low external component count. Features include an accurate reference voltage, fast load transient response, auto-skip mode operation for light load efficiency, OOA light load operation with > 25-kHz switching frequency, D-CAP3 control mode with good line, load regulation, and does not require external compensation.
2 Hardware

2.1 Input and Output Connections

The TPS51385EVM is provided with input and output connectors and test points as shown in Table 2-1. Test point TP2 provides a place to monitor the $V_{IN}$ input voltages with TP9 providing a convenient ground reference. TP5 is used to monitor the output voltage with TP12 as the ground reference.

<table>
<thead>
<tr>
<th>Reference Designator</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>J1</td>
<td>$V_{IN}$ (see Table 1-1 for $V_{IN}$ range)</td>
</tr>
<tr>
<td>J2</td>
<td>$V_{OUT}$, 5.1 V at 7-A maximum</td>
</tr>
<tr>
<td>JP1</td>
<td>PG pullup control. Short pin 1 and pin 2 to pull PG up to VCC.</td>
</tr>
<tr>
<td>JP2</td>
<td>EN high control. Short pin 1 and pin 2 to connect $V_{IN}$ to pin 1 of JP3.</td>
</tr>
<tr>
<td>JP3</td>
<td>EN control. Short pin 1 and pin 2 of JP2 to make EN high.</td>
</tr>
<tr>
<td>JP4</td>
<td>Mode control. Float or short pin 2 and pin 3 to set PSM mode. Short pin 2 and pin 1 to set OOA mode.</td>
</tr>
<tr>
<td>TP1</td>
<td>$V_{IN}$ positive monitor point</td>
</tr>
<tr>
<td>TP2</td>
<td>$V_{IN}$ positive test point</td>
</tr>
<tr>
<td>TP3</td>
<td>Switch node (SW) test point</td>
</tr>
<tr>
<td>TP4</td>
<td>$V_{OUT}$ positive monitor point</td>
</tr>
<tr>
<td>TP5</td>
<td>$V_{OUT}$ positive test point</td>
</tr>
<tr>
<td>TP6</td>
<td>EN test point</td>
</tr>
<tr>
<td>TP7</td>
<td>PG test point</td>
</tr>
<tr>
<td>TP8</td>
<td>GND monitor point</td>
</tr>
<tr>
<td>TP9</td>
<td>GND test point</td>
</tr>
<tr>
<td>TP10</td>
<td>GND monitor point</td>
</tr>
<tr>
<td>TP11</td>
<td>Loop test point</td>
</tr>
<tr>
<td>TP12</td>
<td>GND test point</td>
</tr>
<tr>
<td>TP13</td>
<td>VCC test point</td>
</tr>
<tr>
<td>TP14, TP15, TP16</td>
<td>GND test point</td>
</tr>
</tbody>
</table>

2.2 Modifications

The evaluation module is designed to provide access to the features of the TPS51385. Some modifications can be made to this module.

2.2.1 Output Voltage Setpoint

To change the output voltage of the EVM, change the value of resistor $R_4$ ($R_{upper}$) and $R_6$ ($R_{lower}$). The value of $R_4$ and $R_6$ for a specific output voltage can be calculated using Equation 1.

$$V_{OUT} = 0.6 \times \left( 1 + \frac{R_{UPPER}}{R_{LOWER}} \right)$$

(1)

2.2.2 Mode Selection

TPS51385 has a MODE pin that can offer two different options of light load performance, as shown in Table 2-2.

<table>
<thead>
<tr>
<th>MODE Pin</th>
<th>Light Load Performance</th>
</tr>
</thead>
<tbody>
<tr>
<td>Connect Pin2(MODE) to Pin3(L) of JP4 or leave MODE pin floating</td>
<td>PSM</td>
</tr>
<tr>
<td>Connect Pin2(MODE) to Pin1(H) of JP4</td>
<td>OOA</td>
</tr>
</tbody>
</table>
3 Implementation Results

This section describes how to properly connect, set up, and use the TPS51385EVM. The section also includes test results typical for the evaluation modules.

3.1 Evaluation Setup

1. Make sure that the jumper at JP2 (Enable High Control) is covered to connect VIN to pin 1 of JP3. Also make sure JP3 (Enable Control) pin 2 and 3 are covered to shunt EN to GND to disable the output.
2. Apply appropriate input voltage to VIN (J1-1) or TP1 and GND (J1-2) or TP8. Connect a power supply capable of supplying 7-A to J1 through a pair of 20AWG wires. Note that the board cannot support hot plug-in. The input lines must be connected between J1 and an external power source first before turning on the power source.
3. Move the jumper at JP3 (Enable Control) from pin 2 and 3 (EN and OFF) to pin 2 and 1 (EN and ON) and make sure JP2 pin 1 and pin 2 are covered. Then, the output can be enabled.
4. Apply the loading to VOUT (J2-2) or TP4 and GND (J2-1) or TP10. The load must be connected to J2 through a pair of 20AWG wires. The maximum load current capability is 7 A. Minimize wire lengths to reduce losses in the wires.

3.2 Test Setup and Results

3.2.1 Start-Up

The following figures show the TPS51385EVM start-up waveform relative to EN.

![Start-Up Waveform](image)

Figure 3-1. Start-Up Relative to EN, $I_{OUT} = 0.1$ A (2ms/div)
3.2.2 Shutdown

The following figures show the TPS51385EVM shutdown waveform relative to EN.

Figure 3-2. Start-Up Relative to EN, $I_{OUT} = 7$ A (2ms/div)

Figure 3-3. Shutdown Relative to EN, $I_{OUT} = 0.1$ A (2ms/div)

Figure 3-4. Shutdown Relative to EN, $I_{OUT} = 7$ A (2ms/div)
3.2.3 Output Voltage Ripple

The following images show the TPS51385EVM output voltage ripple.

![Figure 3-5. TPS51385EVM Output Voltage Ripple, $I_{OUT} = 10$ mA, PSM Mode (100us/div)](image)

![Figure 3-6. TPS51385EVM Output Voltage Ripple, $I_{OUT} = 10$ mA, OOA Mode (10us/div)](image)

![Figure 3-7. TPS51385EVM Output Voltage Ripple, $I_{OUT} = 7$ A (1us/div)](image)
4 Hardware Design Files

4.1 Schematic

The following image shows the schematic for the TPS51385EVM.

Figure 4-1. TPS51385EVM Schematic Diagram
4.2 PCB Layout

The following figures show the board layout for the TPS51385EVM. TPS51385EVM is with four layers. The top layer contains the main power traces for VIN, VOUT, and GND. Also on the top layer are connections for the pins of the TPS51385 and a large area filled with ground. Most of the signal traces are also located on the top side. The input decoupling capacitors C4, C5, and C6 are located as close to VIN pins and PGND pins of the IC as possible. The input and output connectors, test points, and all of the components are located on the top side. The bottom layer is a ground plane along with signal ground copper fill and the feedback trace from the point of regulation to the top of the resistor divider network. Two inner layers are ground plane.

Figure 4-2. Top Assembly

Figure 4-3. Top Layer
Figure 4-4. Inner1 Layer

Figure 4-5. Inner2 Layer

Figure 4-6. Bottom Layer
4.3 Bill of Materials

The following table displays the TPS51385EVM bill of materials.

<table>
<thead>
<tr>
<th>Designator</th>
<th>Qty</th>
<th>Description</th>
<th>Part Number</th>
<th>Manufacturer</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1, C6</td>
<td>2</td>
<td>CAP, CERM, 0.1 µF, 25 V, ±10%, X5R, 0603</td>
<td>CL10A104KA8NNNC</td>
<td>Samsung Electro-Mechanics</td>
</tr>
<tr>
<td>C4, C5</td>
<td>2</td>
<td>CAP, CERM, 10 µF, 35 V, ±10%, X5R, 0805</td>
<td>GRM21BR6YA106KE43L</td>
<td>MuRata</td>
</tr>
<tr>
<td>C7, C8, C9</td>
<td>3</td>
<td>CAP, CERM, 22 µF, 10 V, ±20%, X5R, 0805</td>
<td>GRM21BR61A226ME44L</td>
<td>MuRata</td>
</tr>
<tr>
<td>C12</td>
<td>1</td>
<td>CAP, CERM, 22 pF, 50 V, ±5%, C0G/NP0, 0603</td>
<td>C0603C220J5GACTU</td>
<td>Kemet</td>
</tr>
<tr>
<td>C13</td>
<td>1</td>
<td>CAP, CERM, 1 µF, 10 V, ±10%, X5R, 0603</td>
<td>C0603C105K9PACTU</td>
<td>Kemet</td>
</tr>
<tr>
<td>C14</td>
<td>1</td>
<td>CAP, CERM, 0.022 µF, 16 V, ±10%, X7R, 0603</td>
<td>C0603C220J5GACTU</td>
<td>Kemet</td>
</tr>
<tr>
<td>C15</td>
<td>1</td>
<td>CAP, CERM, 0.01 µF, 16 V, ±10%, X7R, 0603</td>
<td>C0603C103K4RACTU</td>
<td>Kemet</td>
</tr>
<tr>
<td>D1</td>
<td>1</td>
<td>Diode, Zener, 5.1 V, 200 mW, SOD-323</td>
<td>MMSZ5231BS-7-F</td>
<td>Diodes Inc.</td>
</tr>
<tr>
<td>H1, H2, H3, H4</td>
<td>4</td>
<td>Bumpon, Hemisphere, 0.44 × 0.20, Clear</td>
<td>SJ-5303 (CLEAR)</td>
<td>3M</td>
</tr>
<tr>
<td>J1, J2</td>
<td>2</td>
<td>Terminal Block, 5.08 mm, 2 × 1, Brass, TH</td>
<td>ED120/2DS</td>
<td>On-Shore Technology</td>
</tr>
<tr>
<td>JP1, JP2</td>
<td>2</td>
<td>Header, 100mil, 2 × 1, Gold, TH</td>
<td>PBC02SAAN</td>
<td>Sullins Connector Solutions</td>
</tr>
<tr>
<td>JP3, JP4</td>
<td>2</td>
<td>Header, 100mil, 3 × 1, Tin, TH</td>
<td>PEC03SAAN</td>
<td>Sullins Connector Solutions</td>
</tr>
<tr>
<td>L1</td>
<td>1</td>
<td>Inductor, 1 µH, 14.4 A, 0.0064 Ω, SMD</td>
<td>CMLE063T-1R0MS</td>
<td>Cyntec</td>
</tr>
<tr>
<td>LBL1</td>
<td>1</td>
<td>Thermal Transfer Printable Labels, 1.250&quot; W × 0.250&quot; H – 10,000 per roll</td>
<td>THT-13-457-10</td>
<td>Brady</td>
</tr>
<tr>
<td>R1, R5</td>
<td>2</td>
<td>RES, 0, 5%, 0.1 W, 0603</td>
<td>RC0603JR-070RL</td>
<td>Yageo</td>
</tr>
<tr>
<td>R2</td>
<td>1</td>
<td>RES, 51, 5%, 0.1 W, AEC-Q200 Grade 0, 0603</td>
<td>CRCW0603510RJNEA</td>
<td>Vishay-Dale</td>
</tr>
<tr>
<td>R3</td>
<td>1</td>
<td>RES, 100 k, 5%, 0.1 W, 0603</td>
<td>CRCW0603100KJNEAC</td>
<td>Vishay-Dale</td>
</tr>
<tr>
<td>R4</td>
<td>1</td>
<td>RES, 150 k, 1%, 0.1 W, 0603</td>
<td>RC0603FR-07150KL</td>
<td>Yageo</td>
</tr>
<tr>
<td>R6</td>
<td>1</td>
<td>RES, 20.0 k, 1%, 0.1 W, AEC-Q200 Grade 0, 0603</td>
<td>ERJ-3EKF2002V</td>
<td>Panasonic</td>
</tr>
<tr>
<td>R7, R8, R9</td>
<td>3</td>
<td>RES, 20.0 k, 1%, 0.1 W, AEC-Q200 Grade 0, 0603</td>
<td>CRCW060320K0FKEA</td>
<td>Vishay-Dale</td>
</tr>
<tr>
<td>TP1, TP4, TP8, TP10</td>
<td>4</td>
<td>Terminal, Turret, TH, Triple</td>
<td>1598-2</td>
<td>Keystone</td>
</tr>
<tr>
<td>TP2, TP3, TP5, TP6, TP7, TP11, TP13</td>
<td>7</td>
<td>Test Point, Miniature, Red, TH</td>
<td>5000</td>
<td>Keystone</td>
</tr>
<tr>
<td>TP9, TP12, TP14, TP15, TP16</td>
<td>5</td>
<td>Test Point, Miniature, Black, TH</td>
<td>5001</td>
<td>Keystone</td>
</tr>
<tr>
<td>U1</td>
<td>1</td>
<td>4.5-V to 24-V Input, 7-A Synchronous Buck Converter</td>
<td>TPS51385RJNR</td>
<td>Texas Instruments</td>
</tr>
<tr>
<td>C2, C3</td>
<td>0</td>
<td>CAP, CERM, 10 µF, 35 V, ±10%, X5R, 0805</td>
<td>GRM21BR6YA106KE43L</td>
<td>MuRata</td>
</tr>
<tr>
<td>C10, C11</td>
<td>0</td>
<td>CAP, CERM, 22 µF, 10 V, ±20%, X5R, 0805</td>
<td>GRM21BR61A226ME44L</td>
<td>MuRata</td>
</tr>
</tbody>
</table>

(1) Unless otherwise noted in the Alternate Part Number or Alternate Manufacturer columns, all parts can be substituted with equivalents.
5 Additional Information

Trademarks
D-CAP3™ is a trademark of Texas Instruments.
All trademarks are the property of their respective owners.

6 References
For related documentation, see the following:

Texas Instruments, *TPS51385 4.5-V to 24-V Input, 7-A Synchronous Buck Converter* data sheet
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1. **Delivery:** TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an “EVM” or “EVMs”) to the User (“User”) in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.

1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM (“Software”) shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software.

1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.

2 **Limited Warranty and Related Remedies/Disclaimers:**

2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.

2.2 TI warrants that the TI EVM will conform to TI’s published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.

2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's warranty period shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

**WARNING**

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI’s recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI’s instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGRADATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.
3 Regulatory Notices:

3.1 United States

3.1.1 Notice applicable to EVMs not FCC-Approved:

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION
This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices
NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

FCC Interference Statement for Class B EVM devices
NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

Concerning EVMs Including Radio Transmitters:
This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:
(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:
Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l’appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l’appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:
Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.
Concernant les EVMs avec antennes détachables

Conformément à la réglementation d’Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l’impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

3.3 Japan

3.3.1 Notice for EVMs delivered in Japan: Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

3.3.2 Notice for Users of EVMs Considered “Radio Frequency Products” in Japan: EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry’s Rule for Enforcement of Radio Law of Japan.
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

【無線電波を送信する製品の開発キットをお使いになる際の注意事項】 開発キットの中には技術基準適合証明を受けていないものがあります。技術基準適合証明を受けていないものご使用に際しては、電波法遵守のため、以下のいずれかの措置を取っていただく必要があります。

1. 電波法施行規則第6条第1項第1号に基づく平成18年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用いただく。
2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。

上記を遵守頂けない場合は、電波法の罰則が適用される可能性があることをご留意ください。 日本テキサス・インスツルメンツ株式会社
東京都新宿区西新宿6丁目24番1号
西新宿三井ビル

3.3.3 Notice for EVMs for Power Line Communication: Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page 電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。
https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html

3.4 European Union

3.4.1 For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.
4  EVM Use Restrictions and Warnings:

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 Safety-Related Warnings and Restrictions:

4.3.1 User shall operate the EVM within TI’s recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User’s handling and use of the EVM and, if applicable, User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. Accuracy of Information: To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

6. Disclaimers:

6.1 EXCEPT AS SET FORTH ABOVE, EVMS AND ANY MATERIALS PROVIDED WITH THE EVM (INCLUDING, BUT NOT LIMITED TO, REFERENCE DESIGNS AND THE DESIGN OF THE EVM ITSELF) ARE PROVIDED "AS IS" AND "WITH ALL FAULTS:" TI DISCLAIMS ALL OTHER WARRANTIES, EXPRESS OR IMPLIED, REGARDING SUCH ITEMS, INCLUDING BUT NOT LIMITED TO ANY EPIDEMIC FAILURE WARRANTY OR IMPLIED WARRANTIES OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF ANY THIRD PARTY PATENTS, COPYRIGHTS, TRADE SECRETS OR OTHER INTELLECTUAL PROPERTY RIGHTS.

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7. USER'S INDEMNITY OBLIGATIONS AND REPRESENTATIONS. USER WILL DEFEND, INDEMNIFY AND HOLD TI, ITS LICENSORS AND THEIR REPRESENTATIVES HARMLESS FROM AND AGAINST ANY AND ALL CLAIMS, DAMAGES, LOSSES, EXPENSES, COSTS AND LIABILITIES (COLLECTIVELY, "CLAIMS") ARISING OUT OF OR IN CONNECTION WITH ANY HANDLING OR USE OF THE EVM THAT IS NOT IN ACCORDANCE WITH THESE TERMS. THIS OBLIGATION SHALL APPLY WHETHER CLAIMS ARISE UNDER STATUTE, REGULATION, OR THE LAW OF TORT, CONTRACT OR ANY OTHER LEGAL THEORY, AND EVEN IF THE EVM FAILS TO PERFORM AS DESCRIBED OR EXPECTED.
8. Limitations on Damages and Liability:

8.1 General Limitations. IN NO EVENT SHALL TI BE LIABLE FOR ANY SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL, OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF THESE TERMS OR THE USE OF THE EVMS, REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. EXCLUDED DAMAGES INCLUDE, BUT ARE NOT LIMITED TO, COST OF REMOVAL OR REINSTALLATION, ANCILLARY COSTS TO THE PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES, RETESTING, OUTSIDE COMPUTER TIME, LABOR COSTS, LOSS OF GOODWILL, LOSS OF PROFITS, LOSS OF SAVINGS, LOSS OF USE, LOSS OF DATA, OR BUSINESS INTERRUPTION. NO CLAIM, SUIT OR ACTION SHALL BE BROUGHT AGAINST TI MORE THAN TWELVE (12) MONTHS AFTER THE EVENT THAT GAVE RISE TO THE CAUSE OF ACTION HAS OCCURRED.

8.2 Specific Limitations. IN NO EVENT SHALL TI'S AGGREGATE LIABILITY FROM ANY USE OF AN EVM PROVIDED HEREUNDER, INCLUDING FROM ANY WARRANTY, INDEMNITY OR OTHER OBLIGATION ARISING OUT OF OR IN CONNECTION WITH THESE TERMS, EXCEED THE TOTAL AMOUNT PAID TO TI BY USER FOR THE PARTICULAR EVM(S) AT ISSUE DURING THE PRIOR TWELVE (12) MONTHS WITH RESPECT TO WHICH LOSSES OR DAMAGES ARE CLAIMED. THE EXISTENCE OF MORE THAN ONE CLAIM SHALL NOT ENLARGE OR EXTEND THIS LIMIT.

9. Return Policy. Except as otherwise provided, TI does not offer any refunds, returns, or exchanges. Furthermore, no return of EVM(s) will be accepted if the package has been opened and no return of the EVM(s) will be accepted if they are damaged or otherwise not in a resalable condition. If User feels it has been incorrectly charged for the EVM(s) it ordered or that delivery violates the applicable order, User should contact TI. All refunds will be made in full within thirty (30) working days from the return of the components(s), excluding any postage or packaging costs.

10. Governing Law: These terms and conditions shall be governed by and interpreted in accordance with the laws of the State of Texas, without reference to conflict-of-laws principles. User agrees that non-exclusive jurisdiction for any dispute arising out of or relating to these terms and conditions lies within courts located in the State of Texas and consents to venue in Dallas County, Texas. Notwithstanding the foregoing, any judgment may be enforced in any United States or foreign court, and TI may seek injunctive relief in any United States or foreign court.

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