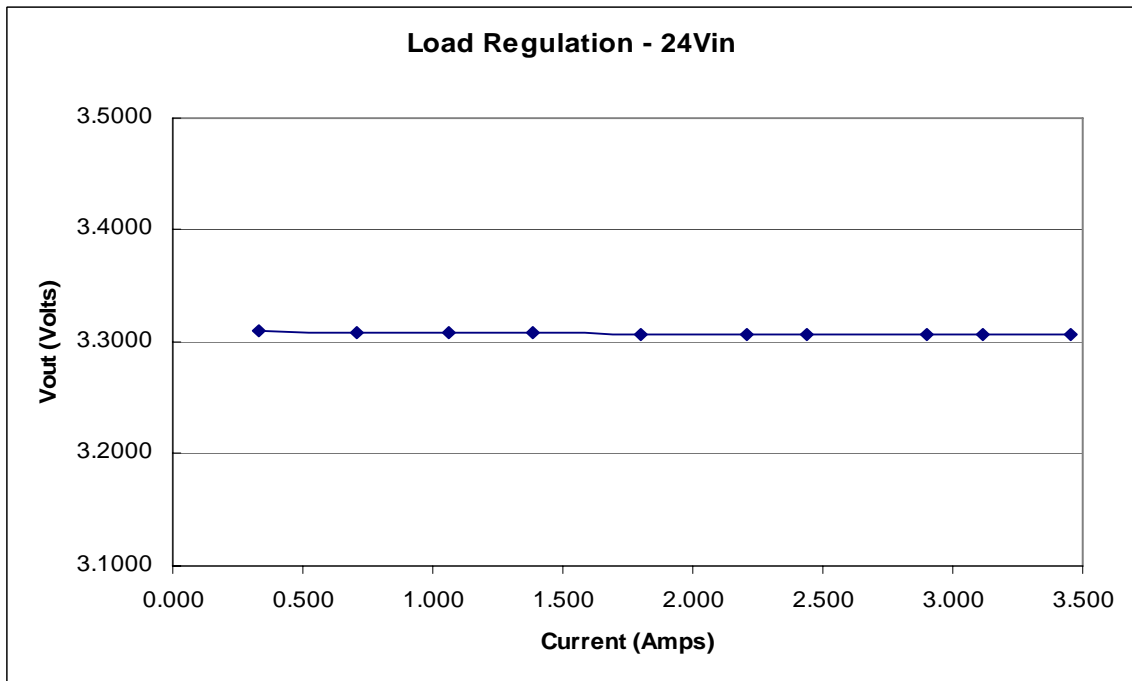
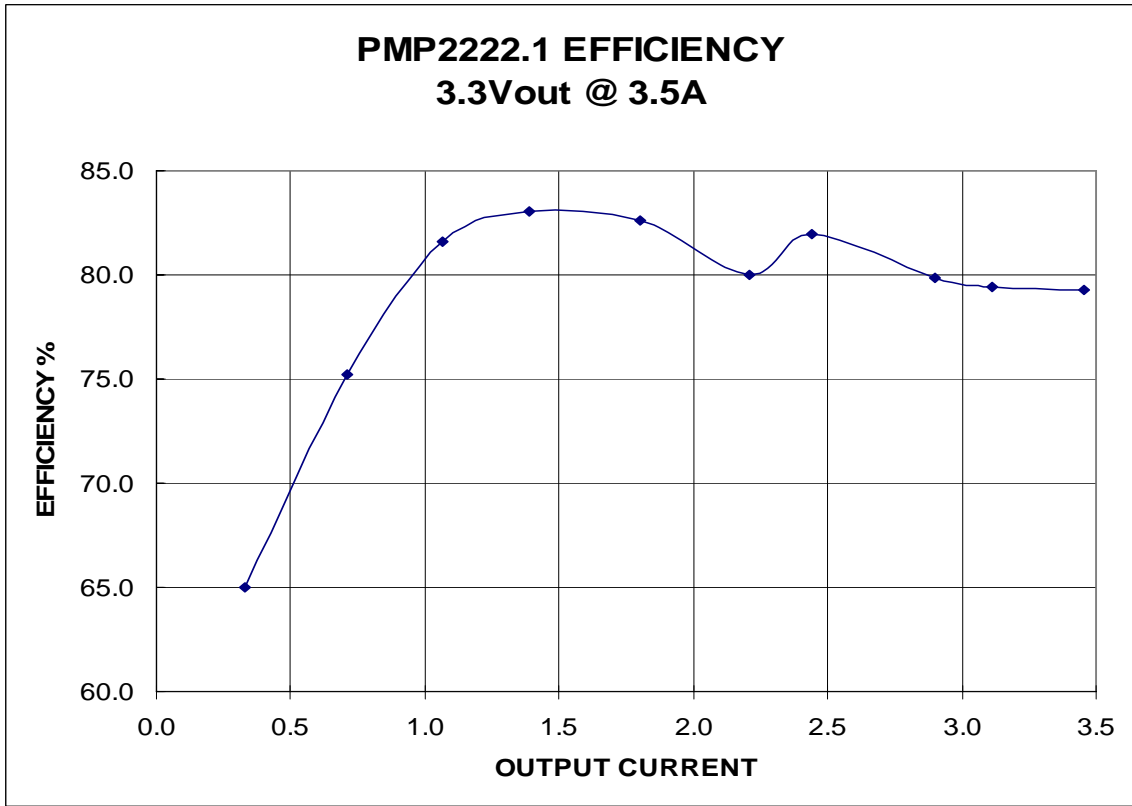


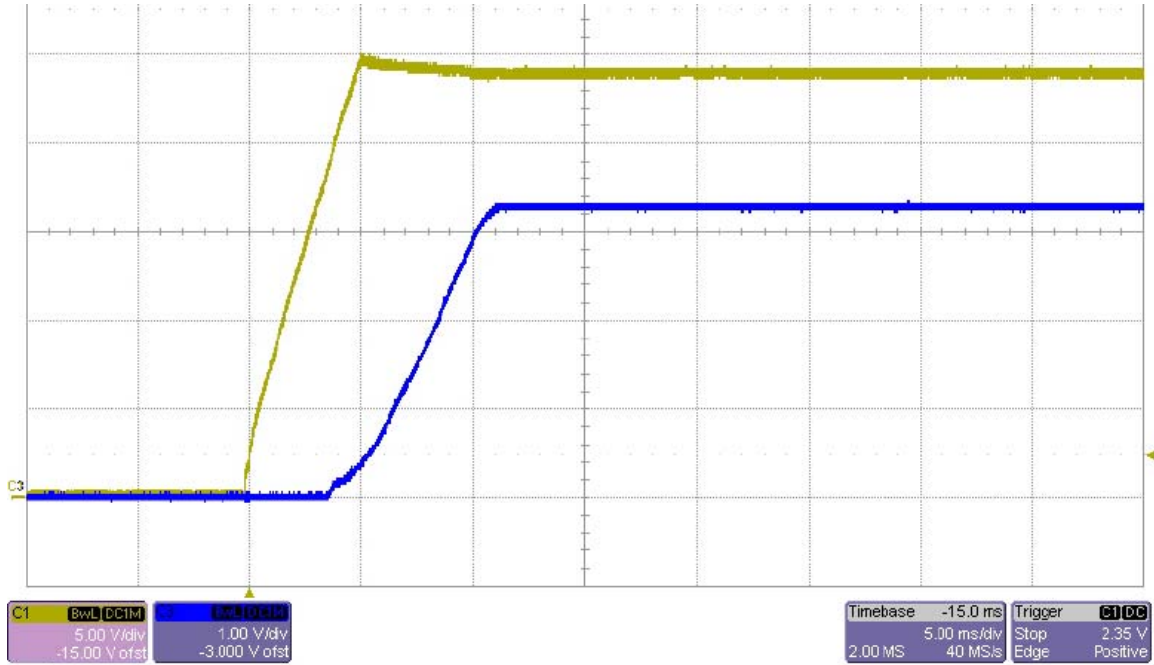
1 Efficiency/Line Regulation (3.3V output)

The converter efficiency and regulation over load are shown in the figures below.

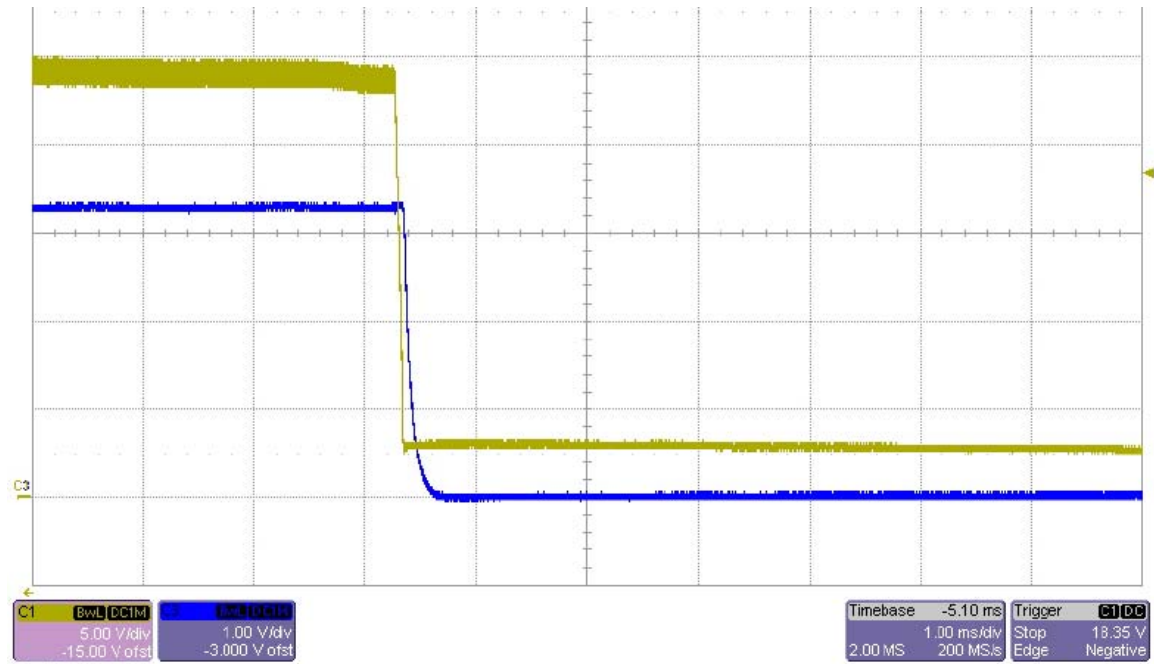


2 Startup/Shutdown (3.3V)

The photo below shows the 3.3V output voltage (Ch3) startup waveform after the application of 24Vdc in (Ch1). The output was loaded to 0A.

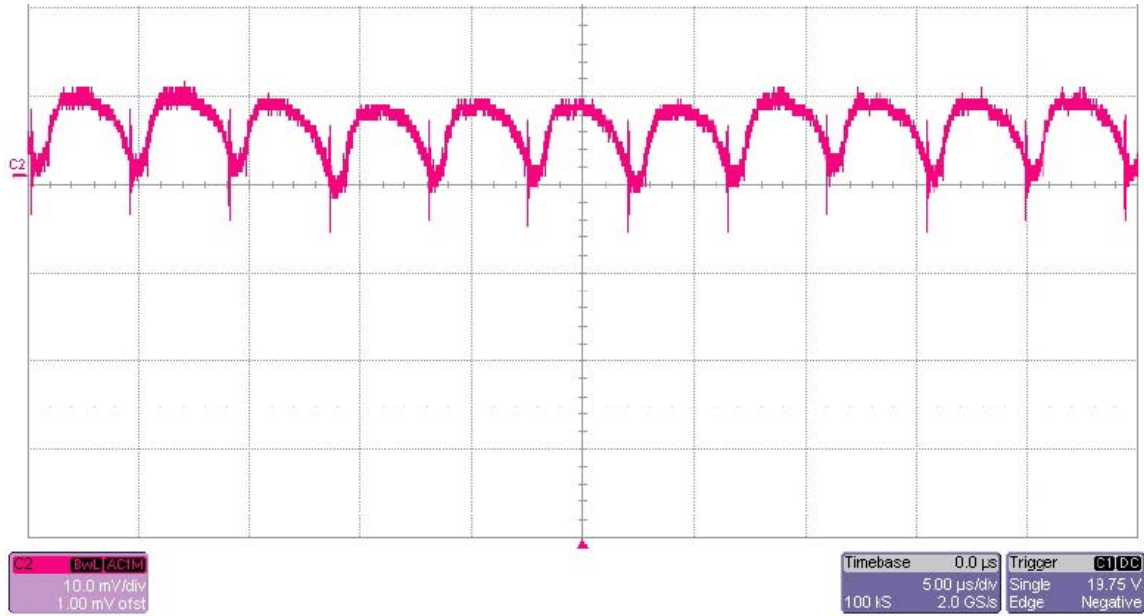


The photo below shows the 3.3V output voltage (Ch3) shutdown waveform after the application of 24Vdc in (Ch1). The output was loaded to 3.5A.



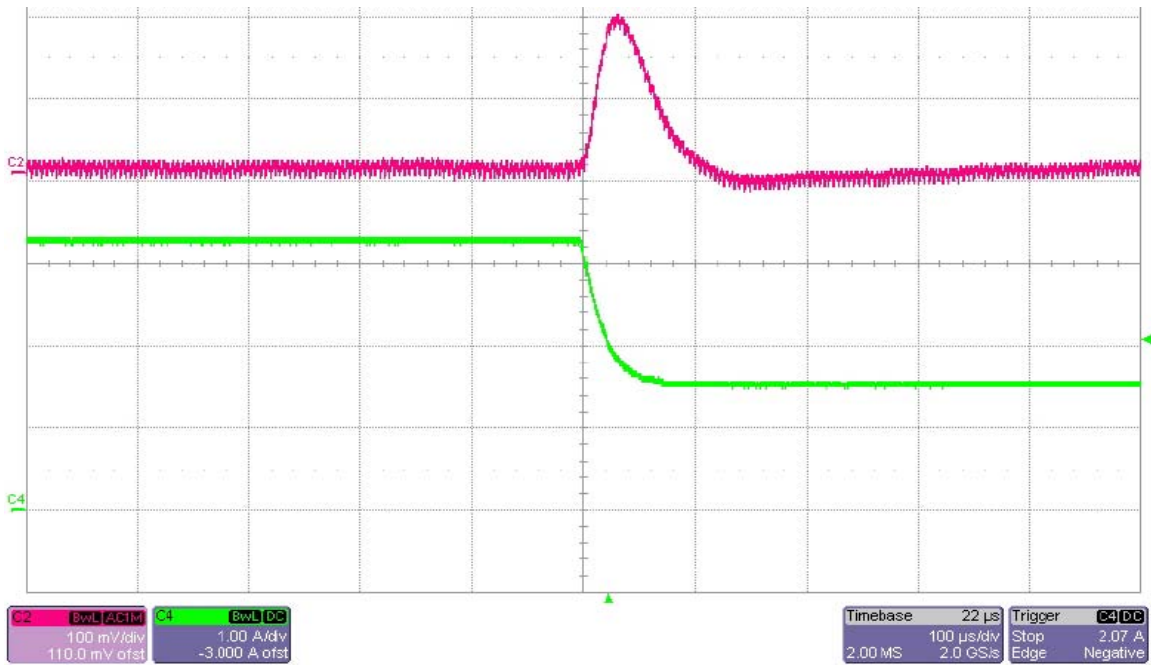
3 Output Ripple Voltage (3.3V)

The 3.3V output ripple voltage is shown in the figure below. The image was taken with the output loaded to 3.5A and the input voltage set to 24Vdc. The Vpp was approx. 10mV



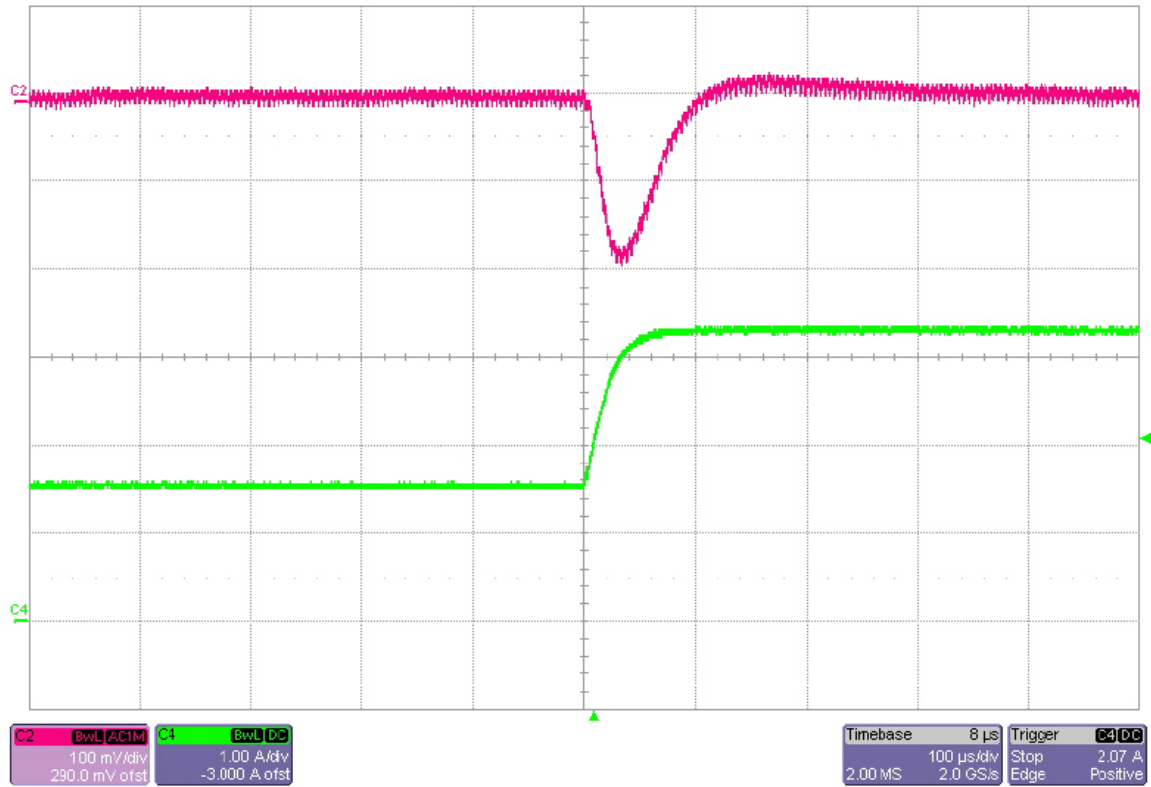
4 Load Transient (3.3V)

The photo below shows the 3.3V output voltage when the load current is pulsed between 3.5A and 1.75A. Vin = 24Vdc.



Negative Edge

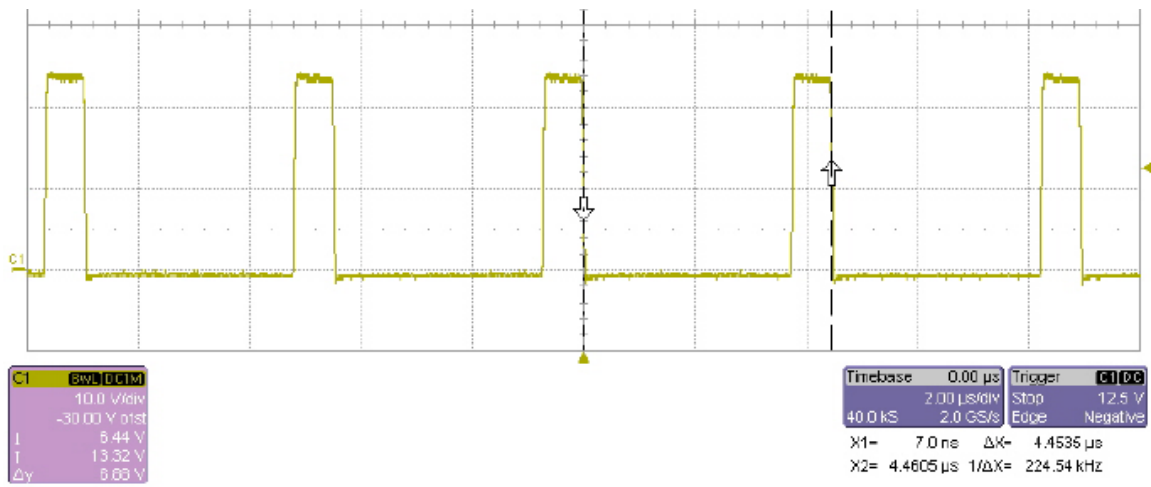
5 Load Transient (3.3V) Cont.



Positive Edge

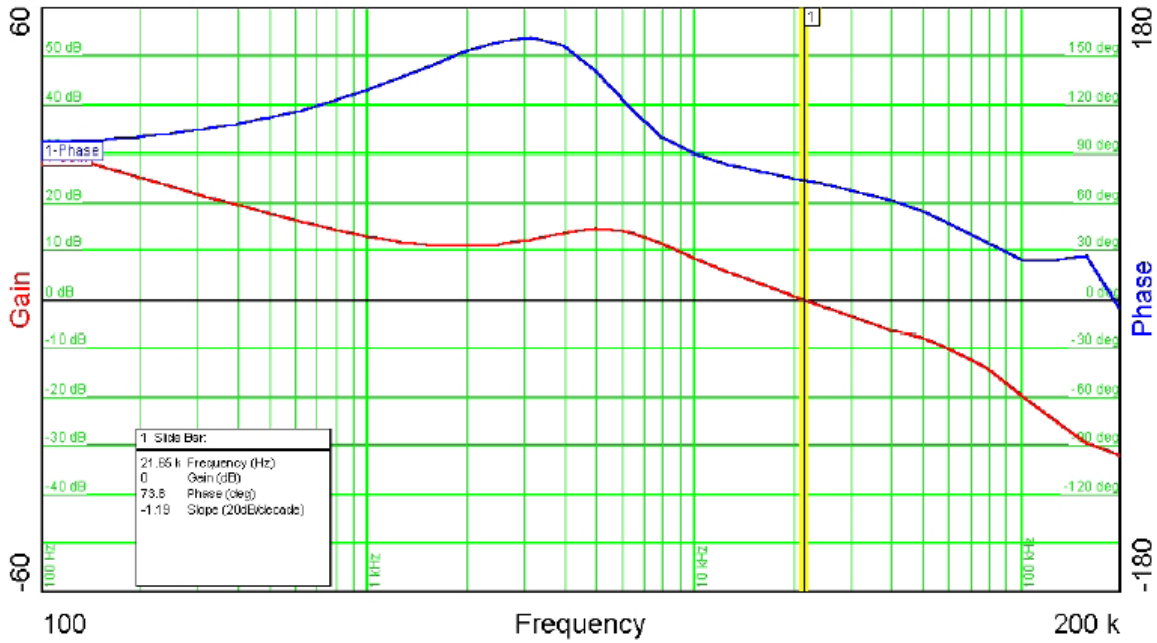
6 Switch Node Waveform (3.3V)

The photo below is of the switching node waveform. The input voltage is 24V and the output is loaded to 3.5A.



7 Control Loop Gain / Stability (3.3V)

The plot below shows the loop gain and phase margin with output voltage set to 3.3V. The output was loaded to 3.5A. $V_{in} = 24V$ Bandwidth = 21.65kHz, Phase Margin = 73.8 degrees



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