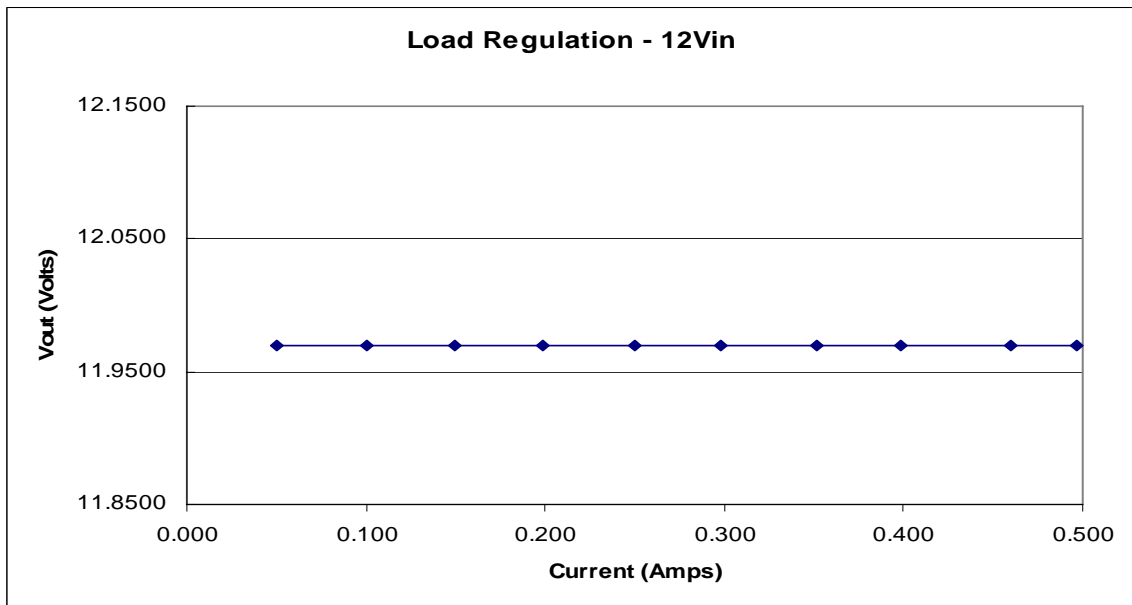
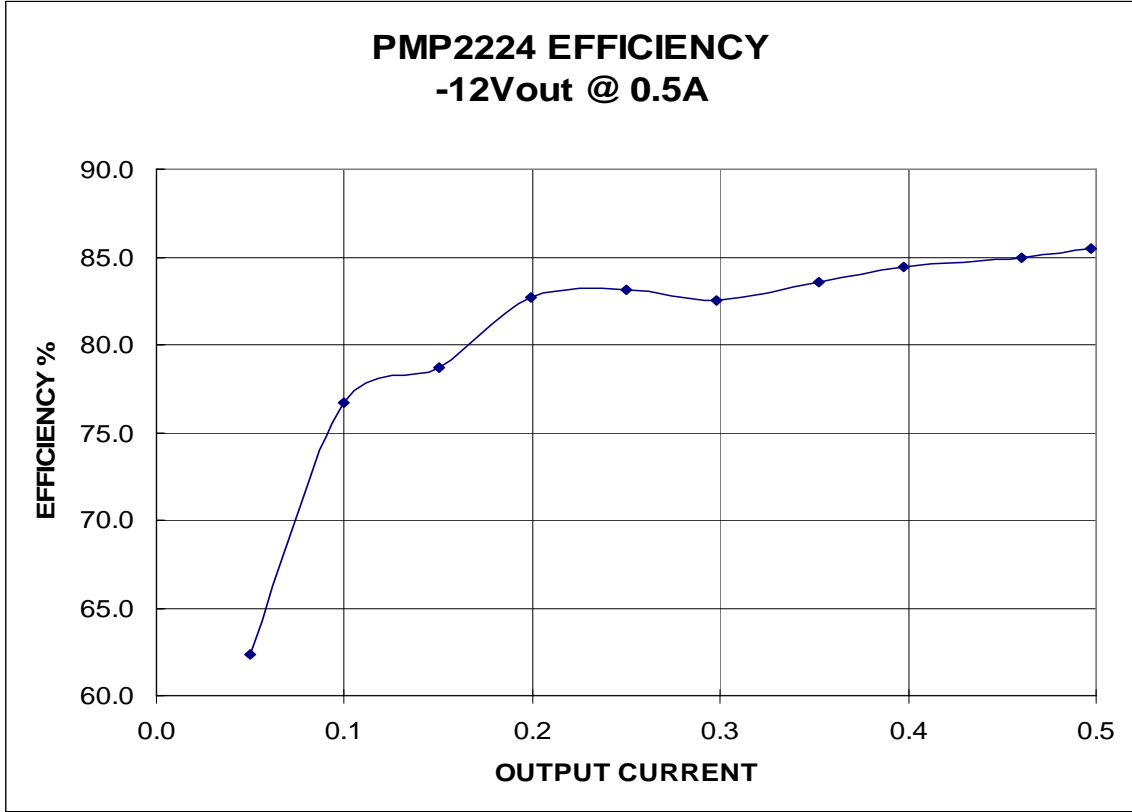


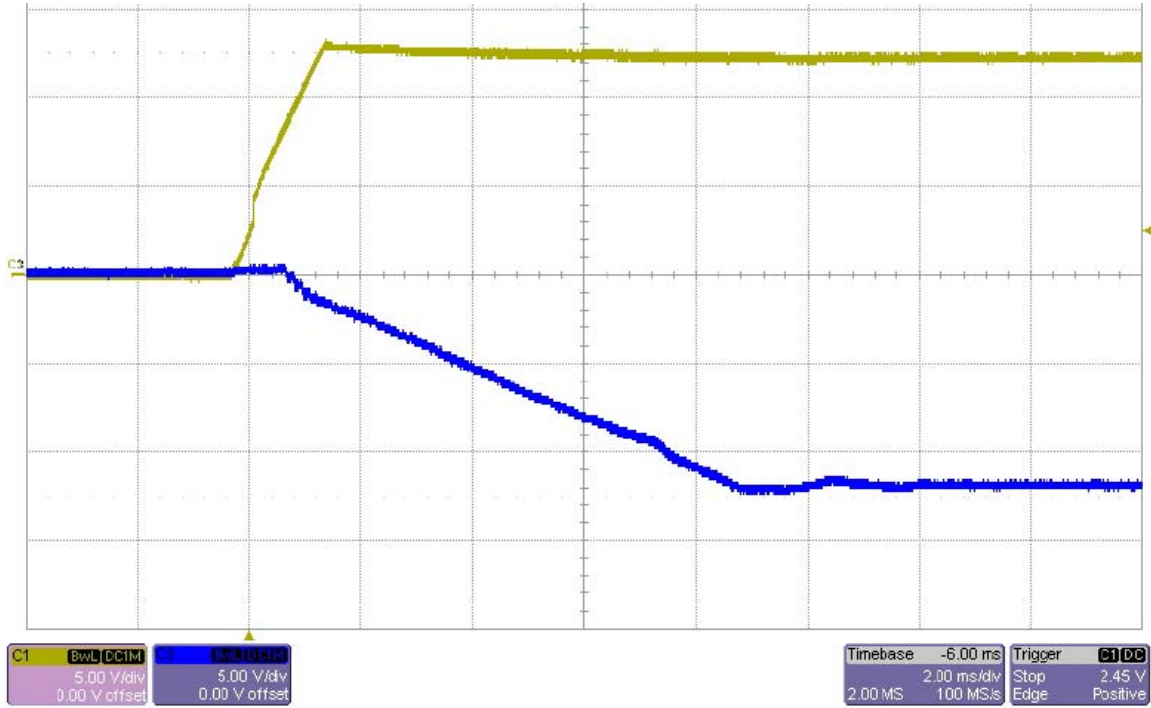
1 Efficiency/Line Regulation (-12V output)

The converter efficiency and regulation over load are shown in the figures below.



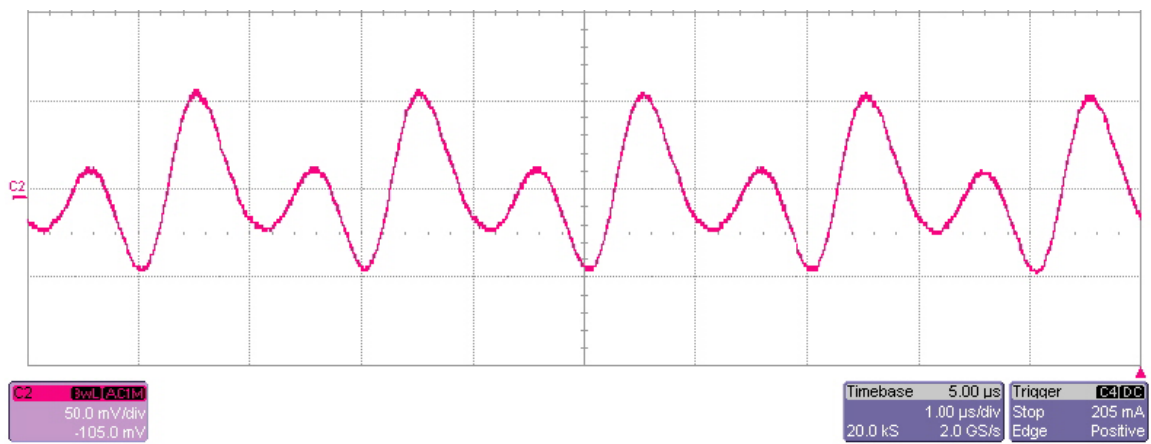
2 Startup (-12V)

The photo below shows the -12V output voltage (Ch3) startup waveform after the application of 12Vdc in (Ch1). The output was loaded to 0A.



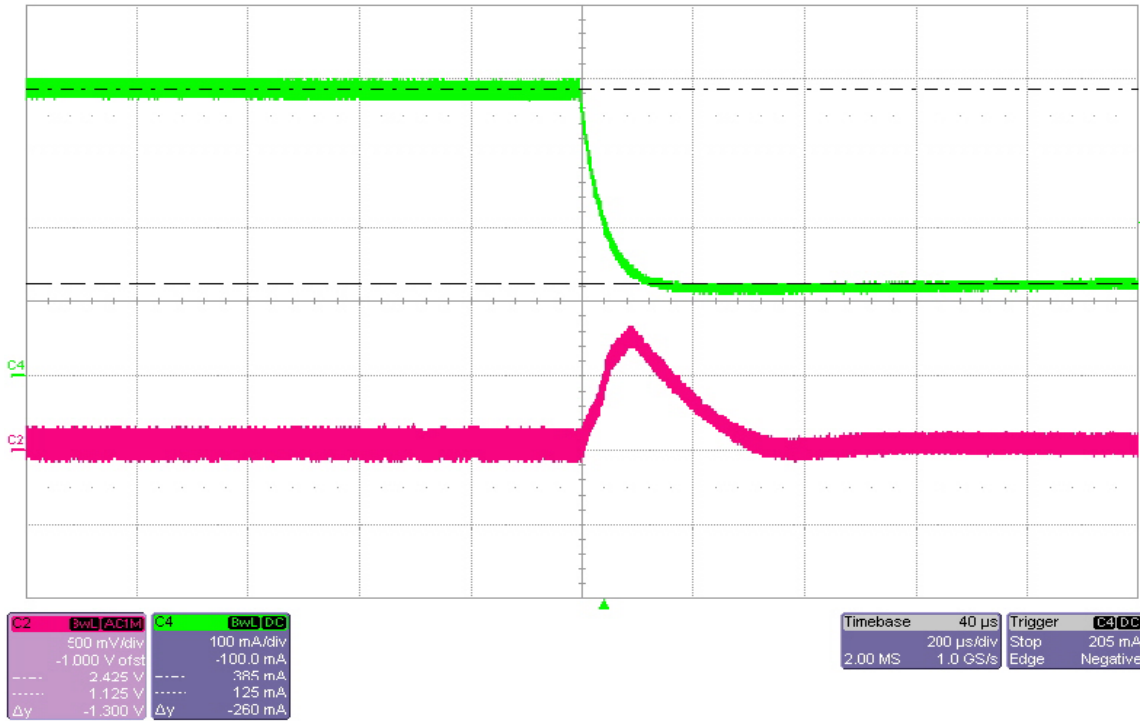
3 Output Ripple Voltage (-12V)

The -12V output ripple voltage is shown in the figure below. The image was taken with the output loaded to 500mA and the input voltage set to 12Vdc. The Vpp was approx. 120mV



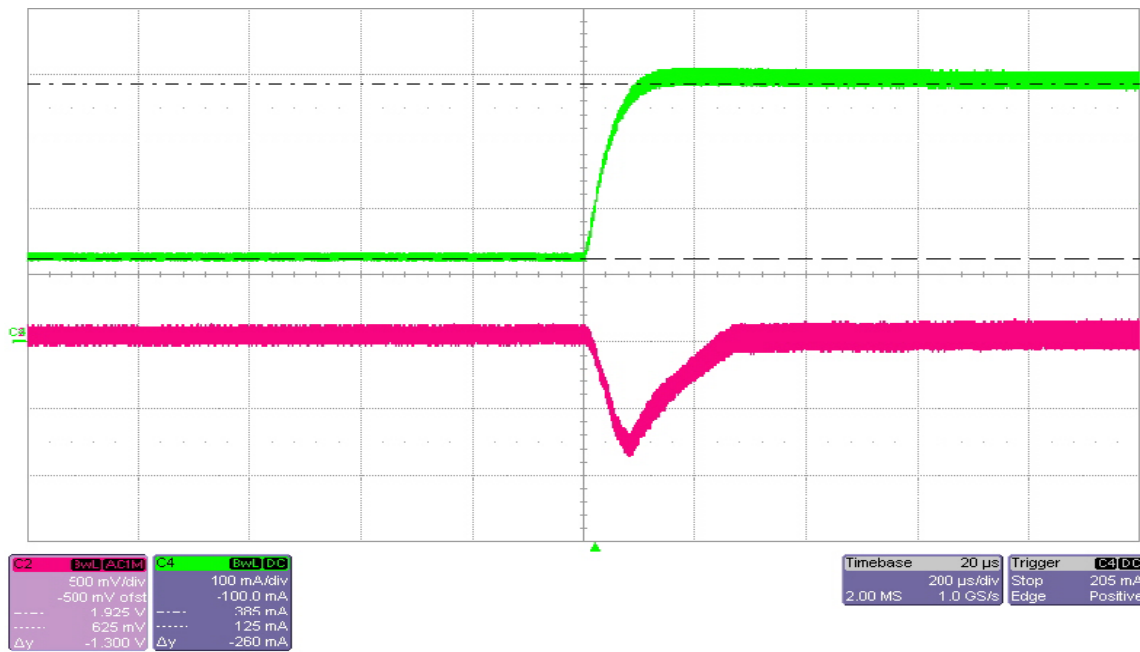
4 Load Transient (-12V)

The photo below shows the 1.8V output voltage when the load current is pulsed between 125mA and 385mA. $V_{in} = 12V_{dc}$.



Negative Edge

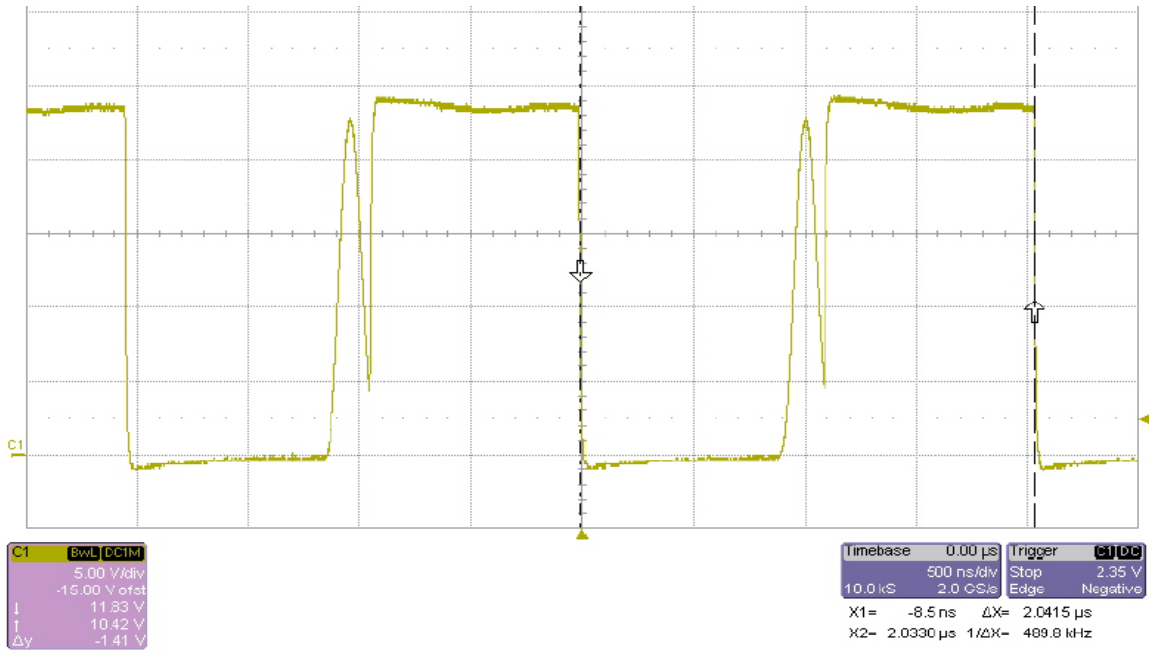
5 Load Transient (-12V) Cont.



Positive Edge

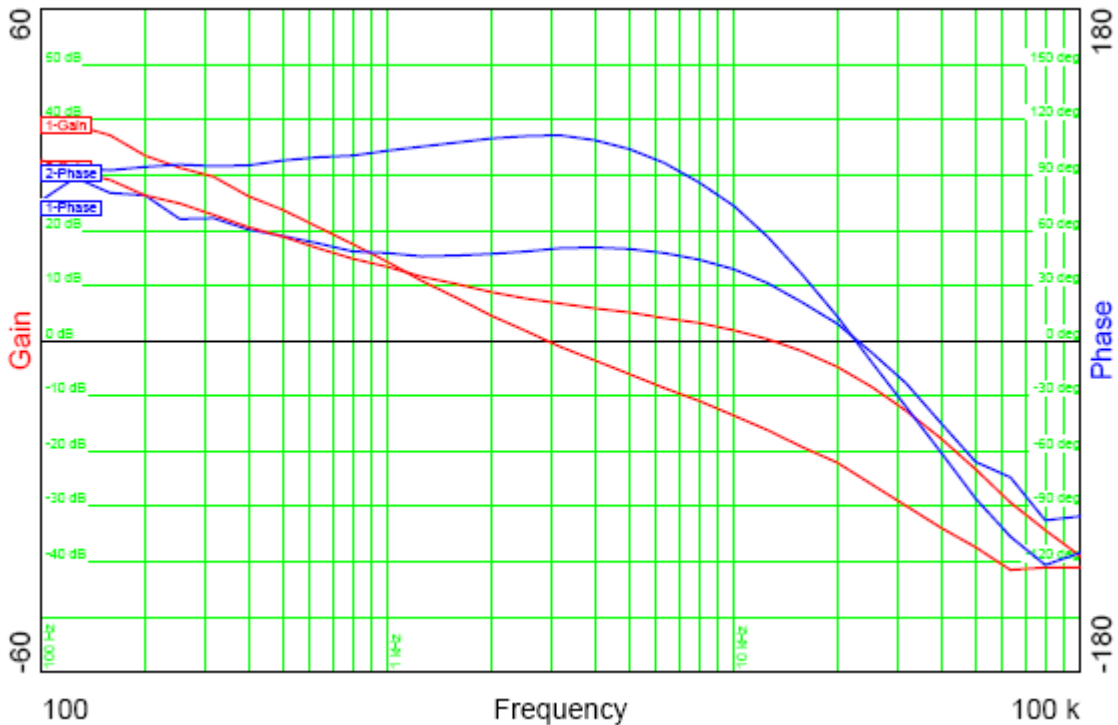
6 Switch Node Waveform (-12V)

The photo below is of the switching node waveform. The input voltage is 12V and the output is loaded to 500mA.



7 Control Loop Gain / Stability (-12V)

The plot below shows the loop gain and phase margin with output voltage set to -12V. The output was loaded to 500mA. $V_{in} = 12V$ Bandwidth = 30kHz, Phase Margin = 50.41 degrees



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