This document presents the information required to operate the TPS65235-1 for DiSEqC2.x application as well as the support documentation including schematic, layout, hardware setup, software application, key waveforms, and bill of materials.

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1 Introduction

The TPS65235-1 device is designed to provide 13-V and 18-V output voltage for satellite receiver, with an operational range of 4.5 V to 16 V.

TPS65235-1 features I2C controlled output voltage from 11 V to 20 V with 16 options; output current limit with ±10% accuracy is set by ISET pin connecting different resistors. The maximum output current limit is up to 1 A.

TPS65235-1 can also run without I2C. In non-I2C mode, the SCL pin and VCTRL pin are used to control 13-V and 18-V output. These two pins can be controlled by GPIO from a processor. A dedicated enable pin EN, is available to turn the LNB output on and off.

The evaluation module is designed to provide access to the features of the TPS65235-1 for DiSEqC2.x application. While PWR747 is designed for DiSEqC1.x application, some modifications can be made to this module to test performance at different input and output voltages, current, and switching frequency. Contact TI Field Applications Group for advice on these matters.
Figure 1 illustrates the EVM schematic.

Figure 1. TPS65235-1 EVM Schematic for DiSEqC 2.x Application
In the TPS65235-1 application, the recommended ceramic capacitors rated are at least X7R and X5R, 35-V rating and 1206 size for achieving lower LNB output ripple. For this EVM, two 22-μF, 35-V capacitors, C8 and C9, are put at the output of the boost converter. If lower cost is demanded, a 100-μF electrolytic (low ESR) and a 10-μF and 35-V ceramic capacitor also works well.

3 Board Layout

Figure 2 shows the component placement on the EVM. Figure 3 and Figure 4 illustrate the top and bottom layers, respectively.

![Component Placement (Top Layer)](image-url)
Figure 3. Board Layout (Top Layer)

Figure 4. Board Layout (Bottom Layer)
4 Bench Test Setup Conditions

4.1 Headers Description and Jumper Placement

Figure 5 shows the header descriptions and jumper placement.

Test points:
A: LX for Boost

Notes:
- At non-\(I^2C\) mode, P1 should be floating, J5 and J3 are used to set the output, refer to Table 3.
- At \(I^2C\) mode, P1 is connected to the PC through the USB-TO-GPIO box, which makes the SCL signal to be high. J5 can be used to set the default output when powered on, refer to Table 3.
- P1 and J3 cannot be connected at the same time.

Table 1 lists the I/O connections and Table 2 lists the EVM jumpers and switches.
Table 1. Input and Output Connection

<table>
<thead>
<tr>
<th>#</th>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>J1</td>
<td>VOUT Connector</td>
<td>VLNB output</td>
</tr>
<tr>
<td>J2</td>
<td>Vin Connector</td>
<td>Apply power supply through this connector</td>
</tr>
</tbody>
</table>

Table 2. Jumpers and Switches

<table>
<thead>
<tr>
<th>#</th>
<th>Function</th>
<th>Placement</th>
</tr>
</thead>
<tbody>
<tr>
<td>J3</td>
<td>Output voltage control (SCL)</td>
<td>Jumper J3-2 and J3-1 makes SCL to VCC connection and gives VLNB with output 14.6 V or 19.4 V.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Jumper J3-2 and J3-3 makes SCL to GND connection and gives VLNB with output 13.4 V or 18.2 V.</td>
</tr>
<tr>
<td>J4</td>
<td>Tone control (EXTM)</td>
<td>Toggle the EXTM signal (J4-2 to J4-3 and then J4-2 to J4-1), the internal tone signal is superimposed at the VLNB output VOUT. EXTM to GND (J4-2 to J4-3), no internal tone signal is superimposed at the VOUT.</td>
</tr>
<tr>
<td>J5</td>
<td>Output voltage control (VCTRL)</td>
<td>Jumper J5-2 and J5-3 makes VCTRL to VCC connection and gives VLNB with output 13.4 V or 14.6 V.</td>
</tr>
<tr>
<td>J6</td>
<td>I2C address set (ADDR)</td>
<td>This pin is the I2C address set pin: tie to VCC sets I2C address with 0x08H; floating sets I2C address with 0x09H; tie to GND sets I2C address with 0x10H; Resistor divider R9A and R9B make ADDR pin at the voltage to set the I2C address with 0x11H. Refer to Table 4. With 3 V-&gt; VCC- 0.8 V will set the I2C address 0x11H</td>
</tr>
<tr>
<td>J7</td>
<td>VLNB output enable (EN)</td>
<td>Jumper EN to GND disables the VLNB output (short J7-2 to J7-3). Jumper EN to Vin through a 100-kΩ resistor enables the VLNB output (short J7-2 to J7-1). Leaving J7 open also enables VLNB output.</td>
</tr>
</tbody>
</table>

Table 3. VLNB Output Control Without I2C Interface Connection

<table>
<thead>
<tr>
<th>EN</th>
<th>SCL</th>
<th>VCRTL</th>
<th>VLNB</th>
</tr>
</thead>
<tbody>
<tr>
<td>H</td>
<td>H</td>
<td>H</td>
<td>19.4 V</td>
</tr>
<tr>
<td>H</td>
<td>H</td>
<td>L</td>
<td>14.6 V</td>
</tr>
<tr>
<td>H</td>
<td>L</td>
<td>H</td>
<td>18.2 V</td>
</tr>
<tr>
<td>H</td>
<td>L</td>
<td>L</td>
<td>13.4 V</td>
</tr>
<tr>
<td>L</td>
<td>X</td>
<td>X</td>
<td>0 V</td>
</tr>
</tbody>
</table>
4.2 Hardware Requirement

This EVM requires an external power supply capable of providing 4.5 V to 16 V at 4 A.

The EVM kit includes USB-TO-GPIO interface box which, when installed on a PC and connected to the EVM, allows the user to communicate with the EVM through a GUI interface. (The EVM and USB-TO-GPIO interface box must be ordered separately.) The minimum PC requirements are:

- Microsoft® Windows® 2000, Windows XP or Windows 7 operating system
- USB port
- Minimum of 30 MB of free hard disk space (100 MB recommended)
- Minimum of 256 MB of RAM

4.3 Hardware Setup

After connecting the power supply to J2, floating J7, connect J3, J4, and J5 to GND, J6 and P1 floating, turning on the power supply, the EVM will regulate the output voltages to 13.4 V without tone superimposed.

Perform the following steps to change the output voltage by sending the digital control signal through a PC running the TPS65235-1 Controller software and USB-TO-GPIO interface box:

- Connect one end of the USB-TO-GPIO box to the PC using the USB cable and the other end to P1 of the TPS65235-1 using the supplied 10-pin ribbon cable per Figure 6. The connectors on the ribbon cable are keyed to prevent incorrect installation.
- Floating J3.
- Connect the power supply on J2, and turn on the power supply.
- Run the software as explained in Section 5.

Figure 6. USB Interface Adapter Quick Connection Diagram
5 Software Installation

If installing from the TI Web site, visit SLVC651.

NOTE: This installation page is best viewed with Microsoft Internet Explorer® browser (it may not work correctly with other browsers).

1. Click on the install button; the PC should display a security warning and ask if you want to install this application. Select Install to proceed.

2. To run the software after installation, either use the desktop icon, which is created by the installer if the user agrees to creating a desktop icon, or go to Start → All programs → Texas Instruments → TPS65235-1.

At start-up, the software first checks the firmware version of the USB-to-GPIO adapter box. If an incorrect firmware version is installed, the software automatically searches the Internet (if connected) for updates. If a new update is available, the software notifies the user of the update then downloads and installs the software. Note that after the firmware is updated, the user must disconnect and then reconnect the USB cable between the adapter and PC, as instructed during the install process. The host PC software also automatically searches on the Internet (if connected) for updates. If a new update is available, the software notifies the user of the update and downloads and installs it. During future use of the software, you are prompted to install a new version if one becomes available on the Web.

NOTE: VERISIGN® Code Signing is used to prevent any malicious code from changing this application. If at any time in the future the binaries are modified, the code will no longer attempt to run.

6 Software Operation

This section provides descriptions of the EVM software.

The supplied software is used to communicate with the TPS65235-1EVM. Click on the icon on the host computer to start the software. The software first displays the home page for the user interface. Two entrances are available for the expert user or beginner.

Figure 7. Home Page for GUI Interface
By clicking on the B button, the Basic information about TPS65235-1 page comes up which lists the features and application information for TPS65235-1. Follow the steps to do the basic check for the EVM. The GUI will guide the beginner to set up the EVM and GUI.

6.1 Register Map Page

By clicking the E button, the Register Map interface comes out. See Figure 8.

Single click on a register name to show FIELD VIEW. This shows the detail setting of each bit.

Double click on the bit to change the bit to 0 or 1.

Single click the ? for the register name to show the detail description page for the register.

For Write Registers option, when the Immediate option is selected, any change is sent to the EVM immediately; otherwise, Write Registers button for each register must be clicked to send the control signal. Register values can be read back from the EVM by clicking Read Register for each register or Load All Registers or set the Auto Read option.

The Status for USB Adapter Connection, only shows the status for the adapter, not the EVM board. The user can select the Write Registers on Register Map page to check whether the board communication is ok or not.
6.2 Basic Settings

Click on the main control panel to show the Basic Settings interface. This interface allows the user to set the registers easily. By clicking Auto Read, the status is monitored automatically.

![Basic Settings Interface]

Figure 9. Main Control Panel for GUI

Figure 7 through Figure 9 show the control GUI interface. There are three 8-bit registers embedded in TPS65235-1, two to control the output voltage characteristics and one for status feedback. Select and check the components on the Basic Settings page in the GUI to change the settings, or by also directly clicking the bits of each register on the Register Map page. \(^{\text{I}^2\text{C}}\) address is set by J6 for ADDR pin controlling, refer to Table 2 and Table 4.

### Table 4. \(^{\text{I}^2\text{C}}\) Address Selection

<table>
<thead>
<tr>
<th>ADDR Pin</th>
<th>(^{\text{I}^2\text{C}}) Address</th>
</tr>
</thead>
<tbody>
<tr>
<td>Connect to VCC</td>
<td>0x08H</td>
</tr>
<tr>
<td>Floating</td>
<td>0x09H</td>
</tr>
<tr>
<td>Connected to GND</td>
<td>0x10H</td>
</tr>
<tr>
<td>Resistor divider makes voltage in ADDR pin range from 3 V to VCC - 0.8 V</td>
<td>0x11H</td>
</tr>
</tbody>
</table>
7 Test Procedure Example

7.1 Voltage output check

Use the following voltage output checks while testing the EVM:
1. Connect I²C adapter to P1
2. Floating J3, J6, and J7, J4 to GND, J5 to GND
3. Apply 12 V to J2
4. Apply loads or non-load to the output connector J1, check the output
5. Set the control register 0x00H and 0x01H to the expected output value and then check the output
6. Monitor the status register 0x02H for the IC status

7.2 Tone Output

Use the following tone output checks while testing the EVM:
1. Connect I²C adaptor to P1
2. Floating J3, J6, and J7, J5 to GND
3. Apply 12 V to J2
4. Toggle the EXTM (J4) from low to high, check the tone output at VOUT
5. Apply loads or non-load to the output connector J1, check the output

Figure 10. EXTM Has 22-kHz External Tone Input
Figure 11. EXTM Has Envelope Input for Tone Output Control

Figure 12. DOUT Output When Tone Received
## Bill of Materials

Table 5 lists the EVM BOM.

### Table 5. Bill of Materials

<table>
<thead>
<tr>
<th>Designator</th>
<th>Description</th>
<th>Manufacturer</th>
<th>Part Number</th>
<th>Qty</th>
</tr>
</thead>
<tbody>
<tr>
<td>PCB</td>
<td>Printed Circuit Board</td>
<td>Any</td>
<td>PWR694</td>
<td>1</td>
</tr>
<tr>
<td>C1</td>
<td>CAP, CERM, 10 μF, 25 V, 10%, X5R, 1206</td>
<td>Murata</td>
<td>GRM31CR61E106KA12L</td>
<td>1</td>
</tr>
<tr>
<td>C2, C3, C6</td>
<td>CAP, CERM, 1 μF, 25 V, 10%, X5R, 0603</td>
<td>Murata</td>
<td>GRM188R61E105KA12D</td>
<td>3</td>
</tr>
<tr>
<td>C5, C12</td>
<td>CAP, CERM, 0.022 μF, 50 V, 10%, X7R, 0603</td>
<td>Murata</td>
<td>GRM188R71H223KA01D</td>
<td>2</td>
</tr>
<tr>
<td>C7, C14</td>
<td>CAP, CERM, 0.01 μF, 50 V, 10%, X7R, 0603</td>
<td>Murata</td>
<td>GRM188R71H103KA01D</td>
<td>2</td>
</tr>
<tr>
<td>C8, C9</td>
<td>CAP, CERM, 22 μF, 35 V, 20%, X5R, 1206_190</td>
<td>TDK</td>
<td>C3216X5R1V226M</td>
<td>2</td>
</tr>
<tr>
<td>C10</td>
<td>CAP, CERM, 0.1 μF, 16 V, 5%, X7R, 0603</td>
<td>AVX</td>
<td>0603YC104JAT2A</td>
<td>1</td>
</tr>
<tr>
<td>C11</td>
<td>CAP, CERM, 0.1 μF, 50 V, 10%, X7R, 0603</td>
<td>Murata</td>
<td>GRM188R71H104KA93D</td>
<td>1</td>
</tr>
<tr>
<td>D1</td>
<td>Diode, Schottky, 40 V, 3 A, SMA</td>
<td>Diodes Inc.</td>
<td>B340A-13-F</td>
<td>1</td>
</tr>
<tr>
<td>D3</td>
<td>Diode, Schottky, 40 V, 2 A, SMA</td>
<td>Diodes Inc.</td>
<td>B240A-13-F</td>
<td>1</td>
</tr>
<tr>
<td>H1, H2, H3, H4</td>
<td>Bumpen, Hemisphere, 0.44 X 0.20, Clear</td>
<td>3M</td>
<td>SJ-5303 (CLEAR)</td>
<td>4</td>
</tr>
<tr>
<td>J1, J2</td>
<td>Terminal Block, 6A, 3.5mm Pitch, 2-Pos, TH</td>
<td>On-Shore Technology</td>
<td>ED555/2DS</td>
<td>2</td>
</tr>
<tr>
<td>J3, J4, J5, J7</td>
<td>Header, 100mil, 3x1, Gold, TH</td>
<td>Samtec</td>
<td>TSW-103-07-G-S</td>
<td>4</td>
</tr>
<tr>
<td>J6</td>
<td>Header, 100mil, 3x2, Gold, TH</td>
<td>Samtec</td>
<td>TSW-103-07-G-D</td>
<td>1</td>
</tr>
<tr>
<td>L1</td>
<td>Inductor, Shielded, Ferrite, 10 μH, 4 A, 0.0312 Ω, SMD</td>
<td>TDK</td>
<td>CLF10040T-100M</td>
<td>1</td>
</tr>
<tr>
<td>L2</td>
<td>Inductor, Shielded, Ferrite, 220 μH, 1.2 A, 0.273 Ω, SMD</td>
<td>TDK</td>
<td>SLF12565T-221M1R0-PF</td>
<td>1</td>
</tr>
<tr>
<td>P1</td>
<td>Header (shrouded), 100mil, 5x2, Gold, TH</td>
<td>Omron Electronic Components</td>
<td>XG4C-1031</td>
<td>1</td>
</tr>
<tr>
<td>Q1</td>
<td>MOSFET, N-CH, 30 V, 4.5 A, SOT-23</td>
<td>Vishay-Siliconix</td>
<td>SI2316BDS-T1-E3</td>
<td>1</td>
</tr>
<tr>
<td>R0</td>
<td>RES, 15.0, 1%, 0.25 W, 1206</td>
<td>Vishay-Dale</td>
<td>CRCW120615R0FKEA</td>
<td>1</td>
</tr>
<tr>
<td>R1</td>
<td>RES, 110 k, 1%, 0.1 W, 0603</td>
<td>Vishay-Dale</td>
<td>CRCW060310K0FKEA</td>
<td>1</td>
</tr>
<tr>
<td>R2, R6, R7, R9</td>
<td>RES, 100 k, 1%, 0.1 W, 0603</td>
<td>Vishay-Dale</td>
<td>CRCW060310K0FKEA</td>
<td>4</td>
</tr>
<tr>
<td>R3, R4, R8</td>
<td>RES, 10 k, 5%, 0.1 W, 0603</td>
<td>Vishay-Dale</td>
<td>CRCW060310K0FKEA</td>
<td>3</td>
</tr>
<tr>
<td>R5</td>
<td>RES, 33.0 k, 1%, 0.1 W, 0603</td>
<td>Vishay-Dale</td>
<td>CRCW060333K0FKEA</td>
<td>1</td>
</tr>
<tr>
<td>SH-J1, SH-J2</td>
<td>Shunt, 100mil, Gold plated, Black</td>
<td>3M</td>
<td>969102-0000-DA</td>
<td>2</td>
</tr>
<tr>
<td>TP1, TP2, TP3</td>
<td>Test Point, Multipurpose, Red, TH</td>
<td>Keystone</td>
<td>5010</td>
<td>3</td>
</tr>
<tr>
<td>TP4, TP5, TP6, TP7, TP8</td>
<td>Test Point, Multipurpose, White, TH</td>
<td>Keystone</td>
<td>5012</td>
<td>5</td>
</tr>
<tr>
<td>U1</td>
<td>LNB VOLTAGE REGULATOR WITH I2C INTERFACE,</td>
<td>Texas Instruments</td>
<td>TPS65235-1RUKR</td>
<td>1</td>
</tr>
</tbody>
</table>
STANDARD TERMS AND CONDITIONS FOR EVALUATION MODULES

1. Delivery: TI delivers TI evaluation boards, kits, or modules, including demonstration software, components, and/or documentation which may be provided together or separately (collectively, an “EVM” or “EVMs”) to the User (“User”) in accordance with the terms and conditions set forth herein. Acceptance of the EVM is expressly subject to the following terms and conditions.

1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM (“Software”) shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms and conditions that accompany such Software.

1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.

2 Limited Warranty and Related Remedies/Disclaimers:

2.1 These terms and conditions do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.

2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for any defects that are caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI. Moreover, TI shall not be liable for any defects that result from User's design, specifications or instructions for such EVMs. Testing and other quality control techniques are used to the extent TI deems necessary or as mandated by government requirements. TI does not test all parameters of each EVM.

2.3 If any EVM fails to conform to the warranty set forth above, TI's sole liability shall be at its option to repair or replace such EVM, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

3 Regulatory Notices:

3.1 United States

3.1.1 Notice applicable to EVMs not FCC-Approved:
This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.
FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

• Reorient or relocate the receiving antenna.
• Increase the separation between the equipment and receiver.
• Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
• Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210

Concerning EVMs Including Radio Transmitters:
This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions:
(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:
Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:
Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables
Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

3.3 Japan

3.3.1 Notice for EVMs delivered in Japan: Please see http://www.tij.co.jp/lads/ti_ia/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
http://www.tij.co.jp/lads/ti_ia/general/eStore/notice_01.page

3.3.2 Notice for Users of EVMs Considered “Radio Frequency Products” in Japan: EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.
If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required by Radio Law of Japan to follow the instructions below with respect to EVMs:

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry’s Rule for Enforcement of Radio Law of Japan.
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transeree. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.
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3.3.3 Notice for EVMs for Power Line Communication: Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page

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4 EVM Use Restrictions and Warnings:
4.1 EVMs ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 Safety-Related Warnings and Restrictions:
4.3.1 User shall operate the EVM within TI’s recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User’s handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

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