# User's Guide DRV87xx-Q1EVM & DRV8106-Q1EVM GUI User's Guide

# **TEXAS INSTRUMENTS**

#### ABSTRACT

This document is provided as a supplement to evaluate the DRV8705x-Q1, DRV8706x-Q1, DRV8714x-Q1, DRV8718x-Q1 & DRV8106x-Q1 customer evaluation modules (EVMs). This General User Interface (GUI) user's guide details how to use the DRV87xx-Q1EVM & DRV8106-Q1EVM GUI Application. The application's features include programming the EVM's MCU through the GUI when an MSP-FET tool is utilized, evaluation of six variants of the motor driver family, a Register Map page with complete register map manipulation, saving/loading register settings, register resetting to default GUI or device sets, Javascript register watch scripting, GUI log file saves, Motor Control page for three of the four device PWM modes, a live, dynamic view of the PWM truth table changes, a live, dynamic view of the FET stage showing which FETs are ON and OFF, ramp rate editing of IN1/EN and IN2/PH duty cycle changes, peak SO, peak current and fault monitoring, open load and short circuit detection, and a Collaterals page. More information on the EVM motor drivers can be found on the *DRV8106-Q1 Automotive Half-Bridge Smart Gate Driver With Wide Common Mode Current Sense Amplifier data sheet*, *DRV8705-Q1* Automotive H-Bridge Smart Gate Driver With Wide Common Mode Current Sense Amplifier and DRV871x-Q1 Automotive Multi-Channel Smart Half-Bridge Gate Drivers With Wide Common Mode Current Sense Amplifier and DRV871x-Q1 Automotive Multi-Channel Smart Half-Bridge Gate Drivers With Wide Common Mode Current Sense Amplifier and DRV871x-Q1 Automotive Multi-Channel Smart Half-Bridge Gate Drivers With Wide Common Mode Current Sense Amplifier and DRV871x-Q1 Automotive Multi-Channel Smart Half-Bridge Gate Drivers With Wide Common Mode Current Sense Amplifier and DRV871x-Q1 Automotive Multi-Channel Smart Half-Bridge Gate Drivers With Wide Common Mode Current Sense Amplifier and DRV871x-Q1 Automotive Multi-Channel Smart Half-Bridge Gate Drivers With Wide Common Mode Current Sense Amplifier and DRV871x-Q1 Automotive Multi-Channel Smart Half-Bridge Gate Drivers With

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Hardware and Software Setup

## 1 Hardware and Software Setup

The hardware (HW) and software (SW) tools that follow are required for EVM evaluation:

- DRV8705x-Q1EVM, DRV8706x-Q1EVM, DRV8714x-Q1EVM, DRV8718x-Q1EVM or DRV8106x-Q1EVM, x stands for S or H, SPI or Hardware interface
- A micro-USB cable
- Brushed motor(s) or other inductive load(s)
- Voltage supply 4.9 V to 37 V
- Jumpers (for jumper header(s))
- DRV87xx-Q1EVM & DRV8106-Q1EVM GUI
- To reprogram or manipulate the firmware, an MSP-FET tool

For additional details on hardware connections and how to install the GUI, refer to the *DRV87xx-Q1EVM* & *DRV8106-Q1EVM User's Guide*.

## **2 GUI Application**



Figure 2-1. Read me Window on Home/Landing Page

Note

Please, read the entirety of the Read Me window before proceeding to utilize the GUI.

## 2.1 Using the DRV87xx-Q1EVM & DRV8106x-Q1EVM GUI

Use the following steps to get started with the GUI:

- 1. Attach zero motor/loads, or one to two brushed DC motors or resistive/inductive loads.
- 2. Enable the motor power supply with connector labels VBAT and GND. For additional details on hardware connections refer to the *DRV87xx-Q1EVM & DRV8106-Q1EVM User's Guide*.
- 3. Plug in the micro-USB cable to the PC.
- 4. Click on DRV87xx-Q1EVM & DRV8106-Q1EVM GUI shortcut either on the desktop or from the start menu to run the GUI application.





#### Figure 2-2. GUI Home/Landing Page

On the GUI home page, the top taskbar, the sidebar, the bottom taskbar, e2e support link, and EVM variant selection options can be found. The bottom taskbar contains a status bar indicating EVM connection status and a GUI log pane that records notable GUI events.

If there is a need to reprogram the EVM to its default firmware, the GUI facilitates this need with the File->Program Device menu option.

#### Note

To reprogram the device, the MSP-MCU Programmer and Debugger tool called MSP-FET must be utilized and connected to the 14 pin, 2 row JTAG connector. Once connection is established, Program Device should be initiated. Once clicked, the GUI will try to program the connected EVM to the GUI's embedded firmware.





Figure 2-3. Program Device Window

If multiple USB devices are connected, verify which COM Port is utilized by the connected EVM in the computer's Device Manager options and make sure the Options->Serial Port... menu item shows the correct port that corresponds to the EVM's USB connection. The baud rate can be configured here. Make sure it matches the firmware's baud rate. By default, the MCU UART is configured to 115200.

		1.000	
Serial Port Configuration	on		
Ports: ▼ Baud Rates: 115200 (recommended) ▼			Q1EVM MENTS
Not enough USB ports detected. Please	check your USB co	onnections	
REFRESH	OK	CANCEL	
		DRV8705	S-Q1EVM

#### Figure 2-4. Serial Port Configuration Menu

The Log Pane can be accessed through the Tools Menu. It is a feature designed to keep track of GUI activities. The benefit of this feature is the facilitation to the user of knowing what is happening with the GUI in case



debugging of any issue is required. It captures all actions done by the GUI that can manipulate the EVM. The log pane allows the user to filter events by categories such as info, warning, error, debug, save the active log (as a .csv file), clear log events, and to hide it. It resides at the bottom taskbar and is represented by a book icon.

Log	s				= FILTER 🖬	0	×
18	Mon Oct 28 2019	16:15:15	0	FROM GUI: soReadCompletehas been set to1			Î
17	Mon Oct 28 2019	16:15:11	0	FROM GUI: soReadCompletehas been set to1			1
16	Mon Oct 28 2019	16:15:09	0	FROM REGISTERS: REGISTER::vds_ctrl_2_reg -FIELD::vds_hs_M set to 13			
15	Mon Oct 28 2019	16:15:09	0	FROM REGISTERS: REGISTER: vds_ctrl_2.reg -FIEL0: vds_ls_tM set to 13			
14	Mon Oct 28 2019	16:15:09	0	FROM REGISTERS: REGISTER: drv_otd(.3,reg - FIELD:: vgs_mode set to 1			
<b>F</b>					Powered By GUI	Compos	ser 🍽
90	<ul> <li>COM3:9600</li> </ul>	Hardware Conne	ected: DRV	/8705S-Q1EVM	TEXAS	INSTRUM	IENTS

#### Figure 2-5. Log Pane

The About option is in the Help Menu and it gives general information about the GUI. An important item here is the Version of EVM's Firmware text, which shows the version of the firmware of the EVM connected to the GUI.

#### About DRV87xx\_DRV8106-Q1EVM-GUI... Application Name: DRV87xx\_DRV8106-Q1EVM-GUI Version: 0.3.3 Published Date: Wed, 01 Apr 2020 17:20:58 GMT Installed Software: Name Version More Info... https://github.com/nwjs/nw.js/tree/nw31 v0.31.5 nw.js http://nodejs.org/docs/v10.6.0/api/ io.js / node.js v10.6.0 chromium v67.0.3396.99 https://www.chromestatus.com/features GUI Composer Runtime v7.4.0 https://dev.ti.com/gc https://www.polymer-project.org/1.0/docs/devguide/feature-overview polymer v1.10.1 GUI Composer Manifest v7.4.0 docs/GUI\_Composer\_manifest.html GUI Composer License v7.4.0 docs/GUI\_Composer\_Standalone\_Software\_License\_Agreement.pdf Application Manifest manifest.html Application License license.pdf Details: Version of EVM's Firmware: unknown Rev Id: unknown © Copyright 2020. Texas Instruments Incorporated. All rights reserved. Visit http://dev.ti.com/gc Powered By GUI Composer™v2 CLOSE

#### Figure 2-6. About Window

Select the EVM variant of interested to open the EVM Variant's Motor Driver page. For the image's found in this user's guide, the evaluation conducted is on the DRV8718S-Q1EVM.

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### Figure 2-7. Motor Driver Page of DRV8718S-Q1EVM

The EVM variant that is connected to the PC will now begin connection to the GUI application. If no hardware is physically connected to the PC or the wrong variant is connected, a message will display stating Hardware Not Connected." Please, proceed back to the GUI Home/Landing Page, verify the selected Device Page matches the hardware variant connected, and check your hardware connections before proceeding.





#### Figure 2-8. File Menu Options in Motor Driver Page

The top taskbar will have additional items on all GUI pages different than the ones on the GUI Home page:

File menu now has options to Save/Load register values set the register map page. It also has options to reset the register map values to the values on the GUI or on the datasheet by default. Note these options are available once the EVM is connected to the GUI.

DRV	87xx_DRV8106-Q1EVM-GUI File Options	Tools	Help
	lenu	Scrip	ting Window
	$\frown$	虧 Log p	bane
	Automotive H-Bridge Smart Gate Driver With Low-	185-Q TI	EVIVI
Ð	Side Current Sense Amplifier		
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#### Figure 2-9. Open Scripting Window

Tools will have a feature called Open Scripting Window. The differences between this script window and the log pane is that the script window stores only register manipulation, not all hardware manipulation, yet it lets you save those register settings that occur while you record changes. The main benefit is to debug issues and share with the support team in case the issue has to do with the register setup. When you access it, it will open a new window that that allows the user to upload/download a javascript file coded to write register settings.



#### Figure 2-10. Scripting Window

It allows for the code to be executed via the Run button after it has been uploaded to the window or recorded. To record changes on the register map and save them as a Javascript file, click the Start Recording button, make the changes on the register map, press Stop Recording when finished, then click Download Script to save the Javascript file.

GUI Application

	Menu
	EVM Home
	Register Map
	Driver Control
х	Smart Gate Drive
Ē	Collateral Documents
A	GUI Home

#### Figure 2-11. Sidebar Options

On the GUI, click on the "Menu" hamburger icon below the top taskbar to open the sidebar menu.

Use the side-bar menu to navigate to the following pages or sub-pages after entering a Device Page. The pages that follow are in context to the launched device:

- Motor Driver (current page)
- Register Map
- Motor Control
- Collateral Documents
- Back (to GUI Home/Landing Page if on a Motor Driver page)

On Motor Driver EVM web pages, links to Register Map, Collateral Documents, Motor Control, EVM and Device Collateral links (of the selected EVM variant), and e2e Support link can be found on the page.



DRV	7xx_DRV8106-Q1EVM-GUI File Options Tools Help										_ 🗇 >
	enu										
۰	ntiver 🛈 👝 Awake Fault/Warning 🔗 Clear					Aut	o Read Eve	ry3 sec 🔻			went ecositer Went AL ecosites Immediate Write 🔻
	Q Search Registers by name or address (0x)								Search Bitfie	lds 🔽 Show Bits	
	Register Name	Address	Value				Bit	ts			FIELD VIEW
	ingrote i talle			7	6	5	4	3	2	1 0	IC_STAT1
Ð	* STATUS	0×00	0×90	1	0	0	0	0	0	0	STATUS / IC_STAT1 / OT_DRV[0]
_		0x00	0x00	0	0	0	0	0	0	0 0	• OT_DRV
₿		0x01	0x00	0	0	0	0	0	0	0 0	
	VSS STATI	0x02	0x00	0	0	0	0	0	0	0 0	STATUS / IC_STAT1 / OV[1]
<b>f</b>	VSS STAT2	0x04	0x00	0	0	0	0	0	0	0 0	• ov
	IC STAT2	0x05	0x00	0	0	0	0	0	0	0 -	
	IC STAT3	0x06	0x08					1	0	0 0	STATUS / IC_STAT1 / UV[2]
	- CONTROL										• uv
	IC_CTRL1	0x07	0x96	1	0			0	1	1 0	
	IC_CTRL2	0x08	0x02		0	0	0	0	0	1 0	STATUS/IC_STATT/DS_GS[3]
	BRG_CTRL1	0x09	0x00	0	0	0	0	0	0	0 0	DS_GS
	BRG_CTRL2	0x0A	0x00	0	0	0	0	0	0	0 0	STATUS / IC STAT1 / WARNIA
	PWM_CTRL1	0x0B	0x05	0	0	0	0	0	1	0 1	
	PWM_CTRL2	0x0C	0xAF	1	0	1	0	1	1	1 1	WARN
	PWM_CTRL3	0x0D	0x00	0	0	0	0	0	0	0 0	STATUS / IC_STAT1 / FAULT[5]
	PWM_CTRL4	0x0E	0x00	0	0	0	0	0	0	0 0	FAILT
	IDRV_CTRL1	0x0F	0xDD	1	1	0	1	1	1	0 1	- HAL
	IDRV_CTRL2	0x10	0xDD	1	1	0	1	1	1	0 1	STATUS / IC_STAT1 / POR[6]
	IDRV_CTRL3	0x11	0xDD	1	1	0	1	1	1	0 1	• POR
	IDRV_CTRL4	0x12	0xDD	1	1	0	1	1	1	0 1	
	IDRV_CTRL5	0x13	0xDD	1	1	0	1	1	1	0 1	STATUS / IC_STAT1 / SPI_OK[7]
	IDRV_CTRL6	0x14	0xDD	1	1	0	1	1	1	0 1	SPI_OK
	IDRV_CTRL7	0x15	0xDD	1	1	0	1	1	1	0 1	
	IDRV_CTRL8	0x16	0xDD	1	1	0	1	1	1	0 1	
	IDRV_CTRL9	0x17	0x00	0	0	0	0	0	0	0 0	
	DRV_CTRL1	0x18	0x00	0	0	0	0	0	0	0 0	
	DRV_CTRL2	0x19	0x09	-		0	0	1	0	0 1	
	DRV_CTRL3	0x1A	0x09			0	0	1	0	0 1	Powered By GUI Compose

#### Figure 2-12. Register Map Page

The Registers Page shows all the registers and their fields present on the DRV8718S-Q1EVM, DRV8714S-Q1EVM, DRV8706S-Q1EVM, DRV8106S-Q1EVM or DRV8705S-Q1EVM device. The page allows register manipulation of any nonreserved control register address field(s) and/or bit(s).

The status registers highlight the fault/warning diagnostics of the device. These registers are read from the motor driver as long as the device is connected. If a fault or a warning occurs, it will be indicated here. The control registers highlight motor driver manipulation such as driver enabling, clear faults, PWM Mode, IDRIVE, VDS\_LVLs, offline diagnostics, fault configuration, and amplifier configuration.

For manipulating register bits, either click a bit to change it. In the Field View, change address fields by selecting specific address field options. By default, the register map writes the registers immediately (when the firmware gets to the GUI register read function call). If deferred writing is preferred, select Deferred in the drop-down that states Immediate and select Write Register once a register has been selected to write it. Write All Registers can be selected as well.



37xx_DRV8106-Q1EVM-	GUI File Options Tools He	lp			- ¤
enu					
LOAD CONTROL SMART GATE	DRIVER ADVANCED GATE DRIVER CSA		Sta	atus	
Independent Half-Bridge Contro	LOAD CONTROL			ak Sense tput 1 (SO) ①	Peak Curren
DRIVER CONFIGURATION			3	.3v ·	11.79
Driver 🕕 🦳 Awake					
Enable Driver ①                 0n			Pea	ak Sense	Peak Curre
Brake () Disabled	1	HB1 Ctrl HI-Z ▼	0		_11 7
DRVOFF nFault ()			U	v	
Driver Outputs ① 💽 On		HI-Z V	East	ult/Warning 👩	
ffline Diagnostics 🛈 🍈 Disabled		H82		no maning 🍑	
		HBS Ctrl	OT	_DRV	
WM CHANNELS		HB3 HE2 Y	Ove	ervoltage indicator	
●1 ●2 ●3 ●4	1	HB4 Ctrl	Unc	dervoltage indicator	
		HE4 HI-Z V	DS.	_GS	
		HBS Ctrl	Wa	ming indicator	
100		HBS HI-Z V	Por	wer-on-reset	
N1 Ramp Rate ①		HR6.Cml	SPI	_OK	
255 PWM Cycles		HIZ V	SPI	clock fault	
			War	tchdog timer fault	
		HB/ Ctri	Ove	intemperature shutdown	
			018	Pundervoltage fault	
		HB8 Ctrl	PVI	DD overvoltage	
		нва	PVI	DD undervoltage	
			► VD	S OverCurrent	
			► VG	S Gate Fault	
			► Uni	known current direction	
			► Cur	rrent direction	
			► Pre	-discharge overflow	
	<u> </u>		▶ Pre	-charge overflow	Powered By G



enu					
Driver Control	(	Register Map Smart Gate Drive	Collateral Documents	Status	
DRVER CONFIGURATION Driver ① ① ① Araise Estable Driver ① ① ① Off Driver Objusto ② ① ① Off Offine Diagnostics ③ ① ① Disabled MODE ① Helf Bridge Control ① Helfdge Control ① Hel	BRUDE CONFIGURATION  Direction (N2/FH) Start Gate Drive O Direction (N2/FH) C P Direction (N2/FH) C DIrection (N2/FH) DIrection (N2/FH) C DIrection (N2/FH			Peak Sense Output () (SO) N/A Fault/Warning PVDD Undervoltage PVDD Overvoltage PVDD Overvoltage PVDD Overvoltage PVDErvoltage Power-on-Reset	Peak Current () N/A Clear
TRUTH TABLE	Bridge view ()			Thermal Warning	
Phase Enable           INI/IN				Offline Open Load Offline Short Circuit V UDS Overcurrent V USS Gate Fault	

Figure 2-14. Motor Control DRV870x-Q1 (S variants)

The Motor Control page has different widget controls to control the load and tune the parameters using the PWM Mode based algorithms. It also allows for Offline Diagnostics.

Putting the device back to sleep and then awaking or resetting the firmware through the reset button it will reset the register values to the default GUI values. The latter will require that GUI connection to EVM be reestablished.



Descriptions of GUI widgets on the Motor Control page:

- Driver Configuration allows for manipulating the nSLEEP pin (Driver), Enable Driver bit, the DRVOFF pin (Driver Outputs), Offline Diagnostics, Gain (on H variants it is a widget that must be configured correctly for the Peak Motor Current to be calculated correctly, and Operation Mode.
- Bridge Configuration allows for manipulating Start/Stop Motor, IN1/EN and IN2/PH pins and nHIZ1 and nHIZ2 for Independent Half-Bridge Control, IN1/EN pin for PWM and IN2/PH for direction in PH/EN Mode, and IN1/EN and IN2/PH for PWM Mode. For both H-Bridge Controls, the Active Free Wheel Diode bit can be manipulated (S variants only). For all PWM Modes, there are two widgets for PWM ramp rate manipulation, one for each PWM pin. Ramp rate widget values are set to 0 to 255 PWM Cycles, which translates to a 0 ms to 1000 ms delay when changing a PWM duty cycle from 0% to 100%.
- Load Control allows for manipulating Start/Stop Motor, configuring Half Bridges for independent control in low side, high side, zone control, or PWM mode.
- Status Outputs allows for Peak SO Sense, Peak Motor Current, and Fault/Warning Status feedback, along with Clear Fault bit manipulation.
- Refer to the DRV8706x-Q1EVM, DRV8106x-Q1EVM or DRV8705x-Q1EVM User's Guide by clicking its link in the Home/Landing Page to understand Motor Control operation and get details on each of the parameters on this page.
- The truth table highlights the truth table row being executed as the PWMs change during a drive period.
- The FET stage diagram highlights the FETs that are ON during a drive period. To properly show the correct Peak Motor Current calculation the correct FET stage diagram must be selected.

Motor Control	
DRIVER CONFIGURAT	ION
Driver 🛈	🔵 Awake
Enable Driver 🛈	Off
Driver Outputs 🕕	On
Offline Diagnostics (i)	Configure



ine Diagnostics 🗊	x	х
HS_LVL ① 1 V ·	VDS_LS_LVL () 1V .	
F-Bridge 1 Pull Up Diagnostics	Half-Bridge 1 Pull Down Diagnostics	
Enabled	Enabled	
-Bridge 2 Pull Up Diagnostics	Half-Bridge 2 Pull Down Disgnostics	
Enabled	Enabled	





xx_DRV8106-Q1EVM-G	UI File Options Tools Help		- ť
OAD CONTROL SMART GATE D	RIVER ADVANCED GATE DRIVER CSA PROTECTION	Status	
ndependent Half-Bridge Control	Half Bridge 1-2 × Apply to AI Hits	Peak Sense Output 1 (SO) ①	Peak Curren
RIVER CONFIGURATION		<b>3.3</b> v	11.79
river 🕕 🛛 💽 Awake			
nable Driver 🕕 🛛 🔴 Off	V.o	Peak Sense Output 2 (SO) ①	Peak Curre
rake ① Disabled		<b>0</b> v	-11.7
DRVOFF nFault ①			
Wine Diagnastice () (Cashlad	VIGLUR.2	Fault/Warning 🥑	
nine Diagnostics ()			
WM CHANNELS		OT_DRV	
		Overvoltage indicator	
1 2 3 4		Undervoltage indicator	
		DS_GS	
0		Warning indicator	
100		Power-on-reset	
11 Ramp Rate ①		SPI_OK	
255 PWM Cycles		SPI clock fault	
		Watchdog timer fault	
		Overtemperature shutdo	wn
		Overtemperature warnin	9
		VCP undervoltage fault	
		PVDD overvoltage	
		PVDD undervoltage	
	$ \begin{array}{c} \begin{array}{c} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	VDS OverCurrent	
	PGND/SL <sub>2</sub>	<ul> <li>VGS Gate Fault</li> </ul>	
		Unknown current direction	on
	-	Current direction	
		▶ Pre-discharge overflow	
		Pre-charge overflow	

#### Figure 2-17. Offline Diagnostics Pull Up, Pull Down DRV871x-Q1EVM

• To do offline diagnostics, Enable Driver must be off and the VDS\_LVLs are recommended to be at 1 V or greater. To learn more about offline diagnostics, refer to the evaluated device's datasheet.

Follow these steps to run the motor:

• For S variants, write appropriate values to the registers from the Register Map Page.

Note

When the driver is disabled, the register read-write (R/W) operations are not allowed.

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- For H variants, configure the four hardware configuration jumper headers (GAIN, MODE, IDRIVE, VDS) you
  wish to evaluate the driver at before waking up the driver (nSLEEP pin). Also, set the Gain widget in the GUI
  to the value set on the GAIN jumper header and select the PWM Mode on the GUI to the value set on the
  MODE jumper header. Set these widget up correctly before waking up the driver. To change any of these four
  settings after evaluation has started, the device must be asleep and the widgets must be configured correctly.
- Wake up the driver with the Driver button.
- For S variants, enable the driver (EN\_DRV bit) with the Enable Driver button.
- Enable driver outputs (DRVOFF pin) through the Driver Outputs button.

#### Note

Since the SO voltage saturates to VREF, the EVM could be providing current to a load that is greater than what the GUI can exhibit or the driver feeds back to the MCU's ADC. Therefore, manipulate Gain carefully and calculate what current the evaluation requires. The EVM has been tested at 15 A RMS, 20 A peak with a gain of 20 V/V.

- Set the PWM Mode desired in the Operation Mode section. The PWM Mode can only be change when Start Motor is not enabled.
- When Driver, Enable Driver, and Driver Outputs are Awake, On, and On respectively, Start Motor can be pressed.

**GUI** Application



For PH/EN mode, change the percentage of duty cycle using the H-Bridge Control IN1/EN Duty Cycle slider. The direction can be changed by clicking the Motor Direction arrow (IN2/PH pin).

For PWM or 1/2 bridge mode, change the percentage of duty cycle using the H-Bridge Control IN1/EN Duty Cycle and H-Bridge Control IN2/PH Duty Cycle sliders. In 1/2 bridge mode, each bridge can be disabled via the Hi-Z Half Bridge X buttons (nHIZx pins).

In PWM Mode, note that Low Side and High Side Active Freewheel is Brake. By default, the PWM Mode starts with the truth table in Coast state. If the IN1/EN and IN2/PH are both at 100%, Brake state has been reached and current recirculation occurs through the FET body diodes.

- Monitor the Status Output section when the motor is running.
- The Peak SO Sense and Peak Motor Current values are updated every two seconds and the calculation takes the largest peak SO Sense value the firmware has stored for the past two seconds.

Note

The GUI calculates the motor current by interpreting the ADC reading of SO, where the reference point readings for each of those parameters are 2044 and 1.65 V, respectively. The motor current calculation on the S and H variants is as follows:

SOADCSample / 4095 \* 3.30 V) - 1.65 V / [Gain \* 0.007])

- If any fault occurs, the motor stops spinning and the corresponding fault is reported in this section.
- At any time while the driver is Awake, the registers can be manipulated from the GUI register page. This
  feature allows for changing register settings on the fly, even while the PWMs are interpreted by the driver.

The Smart Gate Drive pages provide resources to understand Smart Gate Drive and manipulation of the registers that contain Smart Gate Drive based bit fields. Utilize this page to better understand the behavior of Smart Gate Drive and to set it in a faster manner.







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DRV87xx\_DRV8106-Q1EVM-GUI File Options Tools Help \_ @ × 
 Register Map
 Driver Control
 Collateral Documents
 View Intro
 Smart Gate Drive Status CONFIGURATION Peak Sense Output Peak Current ① TDRIVE IDRIVEN HS (SINK) IDRIVEN LS (SINK) ISTRONG (SINK) PROP-DELAY(TPD) TDEAD 62 mA 350 ns 4 μs 62 mA 300 ns 62 mA N/A N/A INS-DEAD TIME IDRIVEP\_HS (SOURCE) IDRIVEP\_LS (SOURCE) IHOLD (SOURCE) VGS\_LVL QGD 1.4 V 13.3 nC 62 m/ 62 mA 16 mA Fault/Warning 🥑 PVDD Undervolta 0 PVDD Overvoltage 0 VCP Undervoltage 0 TDEAD + TDEAD\_D TDEAD + TDEAD\_D Power-on-Reset 0 IGHx Thermal Warning 0 Thermal Shutdowr 0 Offline Open Load Ø Offline Short Circuit 0 VGLx VDS Overcurrent Ø ▶ VGS Gate Fault 0 IGLx 0mA (LS) ISTRONO ISTRON

C > A Power the EVM through J2 and co nnect it to the

Figure 2-19. Smart Gate Drive - Configuration Page DRV870x-Q1EVM



Figure 2-20. Smart Gate Driver - Configuration Page DRV871x-Q1EVM

The CSA page allows the manipulation of two high-performance, wide common-mode, bidirectional, currentshunt amplifiers for current measurements using shunt resistors in the external half-bridges.

**GUI** Application

- TEXAS INSTRUM



**GUI** Application

#### www.ti.com DRV87xx\_DRV8106-Q1EVM-GUI File Options Tools Help \_ 🗇 × **≡** Menu LOAD CONTROL SMART GATE DRIVER ADVANCED GATE DRIVER CSA Status Peak Sense Output 1 (SO) ① Independent Half-Bridge Control Peak Current 1 ① ï CSA 1 DRIVER CONFIGURATION **3.3**v **11.79**A 0 Driver ( Aw Peak Sense Output 2 (SO) ① Peak Current 2 ① Enable Dr 🔵 On ₿ Brake () 🔵 Disa **0**v -11.79A ÷ ŧ Fault/Warning 📀 Clear 0 0 OT\_DRV VELS æ 0000000000000000000 Overvoltage inc SA\_BLK\_LVL\_1 CSA\_BLK\_SEL 0%.Disabled ¥ Half-bridge 1 • 1 0 2 • 3 • 4 • age indicator DS\_GS Warning indicate SPI\_OK IN1 Ramp Rate ① SPI clock fault CSA 2 255 Watchdog timer fa -PVDD ow PVDD unde VDS OverCurrent VGS Gate Fault 000 ▶ Unkn LVL\_2 CSA\_BLK\_SEL\_2 bled V Half-bridge 5 V Current direction Pre-discharge ov 0 🗐 🕒 🔺 Hardware Con ected: DRV8718S-Q1EVM

#### Figure 2-21. CSA - Configuration Page DRV871x-Q1EVM

The Collaterals page contains links to find other gate drivers, TI Reference Designs, blogs and learning videos to understand brushed DC motor driving even more.

DRV87xx_DRV8106-Q1EVM-GUI File Options Tools Help _ D ×								
	Collateral Documents						Register N	Tap Driver Control Smart Gate Drive
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	Brushed DC Driver Technical Documents							
<i>∎</i> с:	<ul> <li>Power the EVM through J2 and conr</li> </ul>	lect it to the PC.						Powered By Gill Compose

## Figure 2-22. Collaterals Page

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