EVM User's Guide: TPSM828510EVM TPSM828511EVM

TPSM828512EVM

# TPSM82851xEVM Evaluation Module



## **Description**

TPSM82851xEVM evaluation module (EVM) is a simple, easy-to-use synchronous step-down DC/DC power modules with integrated inductors. The EVM is available for different output current versions such as TPSM828510EVM for 0.5A, TPSM828511EVM for 1A and TPSM828512EVM for 2A. The EVMs regulate the output voltage to 1.8 V with the input voltage from 2.7 V to 6 V.

### **Get Started**

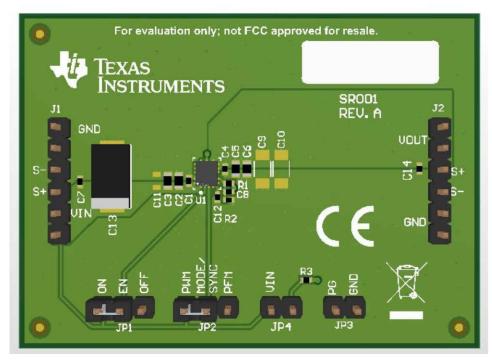
- 1. Order the EVM.
- 2. Download the data sheet.
- 3. Use data sheet or WEBENCH® to modify the soft-start time and output voltage.

### **Features**

- Input voltage range: 2.7 V to 6 V
- Output voltage range: 0.6 V to 5.5 V
- 17-µA typical guiescent current
- Feedback voltage accuracy ±1% (PWM operation)
- Switching frequency of 2.25 MHz (PWM operation)
- · External synchronization from 1.8 MHz to 4 MHz
- Selectable forced PWM or PFM/PWM operation
- · Active output discharge
- · Adjustable soft-start or tracking
- Power-good output with window comparator

## **Applications**

- · Factory automation and control
- Signal measurement, source generation, instrumentation
- Patient monitoring and diagnostics
- · Wireless infrastructure
- Ruggedized Communication: sensors, imaging, and radar



TPSM82851xEVM Hardware Image (Top View)



### 1 Evaluation Module Overview

### 1.1 Introduction

The TPSM82851xEVM facilitates the evaluation of the TPSM82851x 0.5-A, 1-A and 2-A pin to pin compatible step-down converter power modules in a 2.7-mm × 3-mm × 2-mm overmolded QFN package. SR001-001 uses the 0.5-A TPSM828510 device, SR001-002 uses the 1-A TPSM828511 device and SR001-003 uses the 2-A TPSM828512 device. The EVMs regulate the output voltage to 1.8 V with the input voltage from 2.7 V to 6 V. This user's guide describes the characteristics, operation, and use of the TPSM82851xEVM Evaluation Module (EVM) with a complete schematic diagram, printed circuit board layouts, and bill of materials are included in this document.

The TPSM82851x device is a synchronous, step-down converter power module optimized for a small solution size and high efficiency. The devices focus on high-efficiency, step-down conversion over a different output current range. The internal compensation circuit allows a compact solution and small external components. The device is available in a 2.7-mm × 3-mm × 2-mm overmolded QFN package.

#### 1.2 Kit Contents

Table 1-1. TPSM82851xEVM Kit Contents

Item	Description	Quantity		
TPSM82851xEVM	PCB	1		

## 1.3 Performance Specifications

**Table 1-2. Performance Specifications Summary** 

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FICATIONS	TEST CONDITIONS	MIN	TYP	MAX	UNIT			
		2.7		6.0	V			
			1.8		V			
TPSM828510EVM		0		0.5	Α			
TPSM828511EVM		0		1.0	Α			
TPSM828512EVM		0		2.0	Α			
	TPSM828510EVM TPSM828511EVM	TEST CONDITIONS  TEST CONDITIONS  TPSM828510EVM  TPSM828511EVM	FICATIONS         TEST CONDITIONS         MIN           2.7         2.7           TPSM828510EVM         0           TPSM828511EVM         0	TEST CONDITIONS	TEST CONDITIONS			

### 1.4 Device Information

The EVM is for TPSM82851x device. TPSM82851x is a family of pin-to-pin compatible 0.5-A, 1-A and 2-A high efficiency and easy to use synchronous step-down DC/DC power modules with integrated inductors. The switching frequency is internally fixed at 2.25 MHz and can also be synchronized to an external clock in the range from 1.8 MHz to 4 MHz. In PFM/PWM mode, the TPSM82851x automatically enters Power Save Mode at light loads to maintain high efficiency across the whole load range.

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### 2 Hardware

## 2.1 Setup

This section describes how to correctly use the TPSM82851xEVM.

#### 2.1.1 Connector Descriptions

J1, Pin 1 and 2 - VIN	Positive input voltage connection from the input supply for the EVM.			
J1, Pin 3 and 4 - S+/S-	Input voltage sense connections. Measure the input voltage at this point.			
J1, Pin 5 and 6 – GND	and 6 – GND Input return connection from the input supply for the EVM.			
J2, Pin 1 and 2 – VOUT	Positive output voltage connection.			
2, Pin 3 and 4 – S+/S- Output voltage sense connections. Measure the output voltage at this point.				
J2, Pin 5 and 6 – GND	Output return connection.			
JP1 – EN	EN pin jumper. Place the supplied jumper across ON and EN to turn on the IC. Place the jumper across OFF and EN to turn off the IC.			
JP2 - MODE/SYNC	MODE/SYNC pin jumper. Place the supplied jumper across VIN and MODE/SYNC to force the device in fixed frequency PWM operation at all load currents. Place the jumper across MODE/SYN and GND to enable power save mode. Connect a clock signal to MODE/SYNC referenced to GND synchronize the switching frequency to the clock signal.			
JP3 – PG	The PG output appears on pin 1 of this header with a convenient ground on pin 2.			
JP4	PG pullup resistor jumper.			

### 2.1.2 Hardware Setup

To operate the EVM, set jumpers JP1 and JP2 to the desired positions per Section 2.1.1. Connect the input supply to J1 between VIN and GND and connect the load to J2 between VOUT and GND.

### 2.2 Modifications

The printed-circuit board (PCB) for this EVM is designed to accommodate some modifications by the user. Additional input and output capacitors or a feedforward capacitor can be added and the soft-start time can be modified. Also, the MODE setting and output voltage setting configuration can be changed.

### 2.2.1 Input and Output Capacitors

Footprints for an additional input capacitor (C11) and output capacitors (C9 and C10) are provided. These capacitors are not required for proper operation but can be used to reduce the input and output voltage ripple and to improve the load transient response. For proper operation, the total output capacitance must remain within the recommended range described in the TPSM82851x 6-V input, 0.5-A / 1-A / 2-A Step-down Module with Integrated Inductor in a QFN Package.

#### 2.2.2 Adjustable-Output IC U1 Operation

U1 is configured for evaluation of the adjustable-output version. This unit is set to 1.8 V. Resistors R1 and R2 can be used to set the output voltage between 0.6 V and 5.5 V. For recommended values, see the TPSM82851x 6-V input, 0.5-A / 1-A / 2-A Step-down Module with Integrated Inductor in a QFN Package.

## 2.2.3 Feedforward Capacitor

C8 is the feedforward capacitor. For recommended values, see the TPSM82851x 6-V input, 0.5-A / 1-A / 2-A Step-down Module with Integrated Inductor in a QFN Package.

### 2.2.4 Soft-Start Time

C16 controls the soft-start time of the converter and can be changed for a faster or slower ramp up of output voltage. For recommended values, see the TPSM82851x 6-V input, 0.5-A / 1-A / 2-A Step-down Module with Integrated Inductor in a QFN Package.



## 3 Implementation Results

## 3.1 TPSM82851xEVM Test Results

The TPSM82851xEVM was used to take the typical characteristics data in the TPSM82851x data sheet. See the TPSM82851x 6-V input, 0.5-A / 1-A / 2-A Step-down Module with Integrated Inductor in a QFN Package. for the performance of this EVM.

## 4 Hardware Design Files

### 4.1 Schematic

Figure 4-1 shows the EVM schematic.

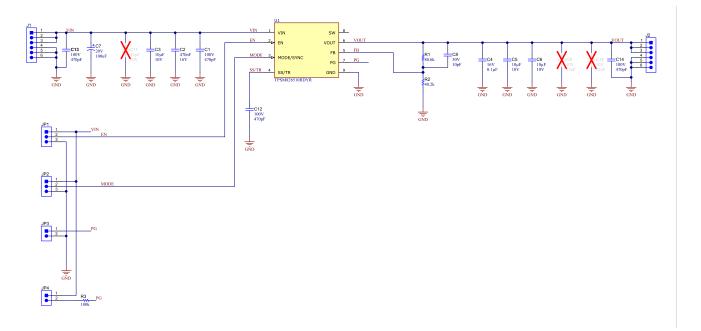


Figure 4-1. TPSM82851xEVM Schematic



## 4.2 PCB Layouts

This section provides the TPSM82851xEVM board layout. See the TPSM82851xEVM tool page for more details.

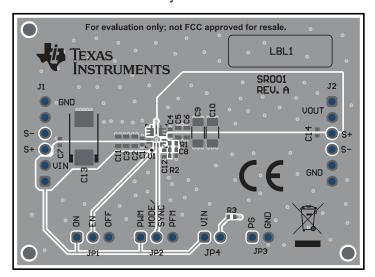


Figure 4-2. Top Silk

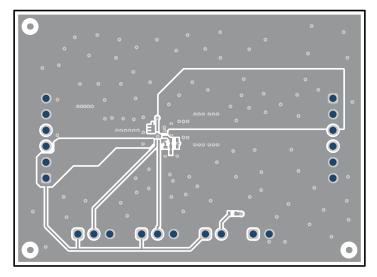


Figure 4-3. Top Layer

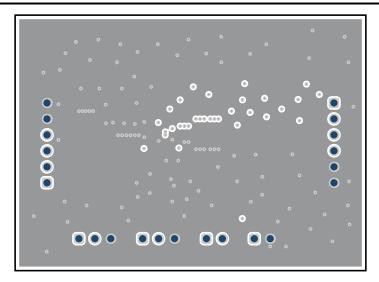


Figure 4-4. Layer 2

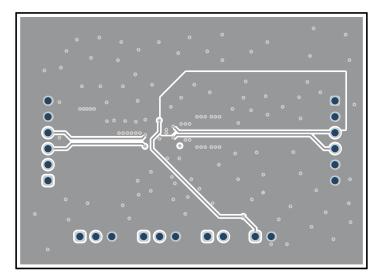


Figure 4-5. Layer 3

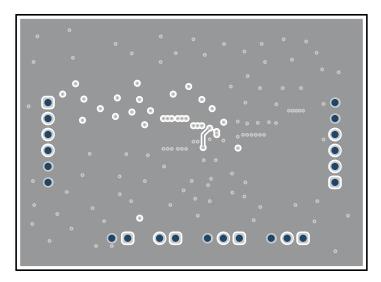


Figure 4-6. Bottom Layer

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Hardware Design Files

## 4.3 Bill of Materials

Table 4-1 lists the bill of materials for this EVM.

Table 4-1. TPSM82851xEVM Bill of Materials

QTY		REF DES VA	VALUE	VALUE DESCRIPTION	SIZE	PART NUMBER	MFR		
-001	-002	-003	KET DES VALU	KET DES VA	VALUE	DESCRIPTION	SIZE	PART NUMBER	WIFK
1	1	1	C1, C12, C13,C14	470 pF	Ceramic Capacitor, 50 V, X7R	0402		any	
1	1	1	C2	0.47 µF	Ceramic Capacitor, 16 V, X7R	0603	GRM188R71C474KA88D	MuRata	
3	3	3	C3, C5, C6,C11	10 μF	Ceramic Capacitor, 10 V, X7R	0603	GRM188Z71A106MA73D	MuRata	
1	1	1	C4	0.1 µF	Ceramic Capacitor, 16 V, X7R	0402		any	
1	1	1	C7	100 μF	Polymer Capacitor, 20 V	7.3x4.3 mm	20TQC100MYF	Panasonic	
1	1	1	C8	10 pF	Ceramic Capacitor, 50 V, COG/NPO	0402	GRM1555C1H100JA01D	MuRata	
2	2	2	C9, C10	22 µF	Ceramic Capacitor, 10 V, X7R	1206	GRM31CR71A226KE15L	MuRata	
1	1	1	R1	80.6 kΩ	Resistor 1%, 0.1 W	0402		any	
1	1	1	R2	40.2 kΩ	Resistor 1%, 0.1 W	0402		any	
1	1	1	R3	100 kΩ	Resistor 1%, 0.1 W	0402		any	
0	0		U1		2.7-V to 6-V Adjustable-Frequency Step-Down Converter	QFN	TPSM828510RDYR	Texas Instruments	
0	1	0	U1		2.7-V to 6-V Adjustable-Frequency Step-Down Converter	QFN	TPSM828511RDYR	Texas Instruments	
1	0	0	U1		2.7-V to 6-V Adjustable-Frequency Step-Down Converter	QFN	TPSM828512RDYR	Texas Instruments	



## **5 Additional Information**

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NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGREDATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

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**FCC NOTICE:** This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

#### CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

#### FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

### 3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

#### **Concerning EVMs Including Radio Transmitters:**

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

## Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

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Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types lated in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

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- 2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
- 3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above. User will be subject to penalties of Radio Law of Japan.

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- 3.4 European Union
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