**Description**

The REF54EVM is a precision voltage reference evaluation module that demonstrates the performance of high precision series reference device (REF54) from Texas Instruments (TI). The REF54 is a family of high precision, low drift, low current consumption series voltage reference devices. The REF54 family offers low temperature drift coefficient (0.5 ppm/°C), low flicker noise (0.16 ppm p-p with 100uF capacitor on NR pin) and high accuracy (±0.02%), while consuming 260 μA current.

**Get Started**

1. Order the EVM at [ti.com](http://ti.com).
2. Configure EVM jumpers (if required).
3. Connect VIN and EN (optional) to power supplies.
4. Test the output.

**Features**

- Enable and disable the output
- Capacitor at NR pin is configurable with 0 ohm resistor to optimize the noise performance

**Applications**

- Semiconductor test equipment
- Precision data acquisition systems
- Precision weight scales
- Ultrasound scanner
- X-ray systems
- Industrial instrumentation
- PLC analog I/O modules
- Field transmitters
- Power monitoring
- Battery management system
1 Evaluation Module Overview

1.1 Introduction

The REF54EVM is a series voltage reference evaluation module that demonstrates the REF54 performance in SOIC package. The REF54 with low long-term drift (30 ppm), excellent load and line regulation helps meet strict performance requirements of high precision applications. This device also comes with an enable pin (EN) that allows the device to be set in shutdown mode to improve power efficiency.

This user's guide describes the characteristics, operation, and recommended use cases of the REF54EVM. This document provides examples and instructions on how to use the REF54EVM board. Throughout this document, the terms evaluation board, evaluation module, and EVM are synonymous with the REF54EVM. This document also includes a schematic, reference printed circuit board (PCB) layouts, and a complete bill of materials (BOM).

1.2 Kit Contents

Table 1-1 details the content of the EVM kit. Contact the TI Product Information Center at (972) 644-5580 if any component is missing.

<table>
<thead>
<tr>
<th>Item</th>
<th>Quantity</th>
</tr>
</thead>
<tbody>
<tr>
<td>REF54EVM</td>
<td>1</td>
</tr>
</tbody>
</table>

1.3 Device Information

The documents in Table 1-2 provide information regarding Texas Instruments integrated circuits used in the assembly of the REF54EVM. This user's guide is available from the TI web site under literature number SNAU289. Any letter appended to the literature number corresponds to the document revision that is current at the time of the writing of this document. Newer revisions are available from the TI web site at www.ti.com, or call Evaluation Module the Texas Instruments Literature Response Center at (800) 477-8924 or the Product Information Center at (972) 644-5580. When ordering, identify the document by both title and literature number.

<table>
<thead>
<tr>
<th>Item</th>
<th>Literature Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>REF54250CDR product data sheet</td>
<td>SNAS837</td>
</tr>
</tbody>
</table>
2 Hardware

2.1 Setup

Schematic shown in Figure 2-1 is representative of the REF54EVM.

The REF54EVM is designed to allow users to evaluate the configuration shown in Figure 2-1. Multiple footprints are provided for passive input, output and NR pin capacitors, so that the user can change the passive components for best performance in the application. Enable can be connected to individual voltage source, VIN or ground with jumper settings to enable or disable the device.
2.2 EVM Connection

The headers of REF54EVM are named similar to pin name of REF54250CDR for easy understanding. Default EVM setup is shown in Figure 2-2. Input power supply must be connected between VIN1 and GND1 header. Pin 2 of EN1 header can be connected to power supply directly or pin 1 of EN1 header to enable the device. The user can also leave EN1 header floating to enable the output. PIN 2 of EN is connected to PIN 3 to disable the device. Output is measured between pin 2 of VOUT1 header and GND_S header. The user can connect R3 to get lower noise output with NR pin. Please refer to the REF54 data sheet for more details on NR pin.

![Figure 2-2. REF54EVM Default Setup](image)

Table 2-1 lists the detailed function of VIN1 and EN1 header.

<table>
<thead>
<tr>
<th>Terminal</th>
<th>Pin Connection</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>VIN1</td>
<td>VIN</td>
<td>Connect to power supply.</td>
</tr>
</tbody>
</table>
| EN1      | EN             | Connect jumper between pin 2 and 3 to enable the output  
            |                | Connect Jumper between pin 2 and 1 to disable the output |

2.3 Jumper Information

REF54EVM can be configured to enable or disable the output through jumpers ate EN1 header.

<table>
<thead>
<tr>
<th>Terminal</th>
<th>Pin Connection</th>
<th>Jumper Function</th>
</tr>
</thead>
</table>
| EN       | EN             | Connect Jumper between pin 2 and 3 to enable the output  
            |                | Connect Jumper between pin 2 and 1 to disable the output |

2.4 Best Practices

- Use the power supply which has very good PSRR.
- Measure the output with high resolution instrument (DMM, oscilloscope) for best performance.
- Connect VOUT1 and GND_S pin to data converter EVM for best performance.
3 Implementation Results

3.1 Evaluation Setup

EVM is tested with the conditions listed below.

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>VIN</td>
<td>Connect the force_high pin of the SMU 2420 at VIN1 pin and Force_low pin at GND_F connector.</td>
</tr>
<tr>
<td>VREF</td>
<td>Connect the DMM 3548 A between pin 2 of VOUT1 and GND</td>
</tr>
<tr>
<td>Enable test case</td>
<td>Check the output for EN1 floating, connected to GND and connected to VIN through the jumper</td>
</tr>
</tbody>
</table>

3.2 Performance Data and Results

Output voltage of REF54 is measured with dropout conditions meets the initial accuracy and temperature drift spec limit. Device meets line regulation spec and shutdown condition for relevant tests.
4 Hardware Design Files

4.1 Schematics

The schematic for the REF70EVM is illustrated in Figure 3-3

![Schematic of REF54 EVM](image_url)

Figure 4-1. REF54 EVM schematic
4.2 PCB Layouts

REF54EVM is a two layer board. The layout is illustrated in this section.

![Figure 4-2. REF54EVM Top Layer](image-url)
Figure 4-3. REF54EVM Bottom Layer
4.3 Bill of Materials (BOM)

Table 4-1 lists the bill of materials for REF54EVM.

<table>
<thead>
<tr>
<th>Designator</th>
<th>Qty</th>
<th>Description</th>
<th>Footprint</th>
<th>Package Reference</th>
<th>Part Number</th>
<th>Manufacturer</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1, C5</td>
<td>2</td>
<td>10 µF ±10% 50 V Ceramic Capacitor X7R 1206 (3216 Metric)</td>
<td>FP-GMC31X7R106K50NT_1206-MFG</td>
<td>CMP-0093201-1</td>
<td>GCM31X7R106K50NT</td>
<td>Cal-Chip Electronics</td>
</tr>
<tr>
<td>C2, C4</td>
<td>2</td>
<td>1 µF ±10% 50 V Ceramic Capacitor X7R 0603 (1608 Metric)</td>
<td>FP-GMC10X7R105K50NT_0603-MFG</td>
<td>CMP-0095295-1</td>
<td>GCM10X7R105K50NT</td>
<td>Cal-Chip Electronics</td>
</tr>
<tr>
<td>C3</td>
<td>1</td>
<td>0.1 µF ±5% 50 V Ceramic Capacitor X7R 0603 (1608 Metric)</td>
<td>FP-06035C104J4Z2A_0603-MFG</td>
<td>CMP-0094511-1</td>
<td>06035C104J4Z2A</td>
<td>KYOCERA AVX</td>
</tr>
<tr>
<td>C6, C8</td>
<td>2</td>
<td>100 µF Molded Tantalum Polymer Capacitor 16 V 2917 (7343 Metric) 50mOhm @ 100 kHz</td>
<td>FP-16TDC100MYF2917-MFG</td>
<td>CMP-0095356-1</td>
<td>16TDC100MYF</td>
<td>Panasonic</td>
</tr>
<tr>
<td>C7</td>
<td>1</td>
<td>10 µF Molded Tantalum Capacitors 25 V 0201 (0603 Metric) 1.4Ohm @ 100 kHz</td>
<td>TAZG106K025CRSZ0000-Footprint-1</td>
<td>CMP-0089355-2</td>
<td>TAZG106K025CRS</td>
<td>AVX</td>
</tr>
<tr>
<td>D1</td>
<td>1</td>
<td>Diode, Zener, 43 V, 500 mW, AEC-Q101, SOD-123</td>
<td>SOD-123</td>
<td>CMP-0085174-1</td>
<td>DDZ43Q-7</td>
<td>Diodes Inc.</td>
</tr>
<tr>
<td>EN1, GND_F, GND_S, VOUT1</td>
<td>4</td>
<td>Header, 100mil, 3x1, Gold, TH</td>
<td>TSW-103-07-G-S</td>
<td>CMP-0075407-2</td>
<td>TSW-103-07-G-S</td>
<td>Samtec</td>
</tr>
<tr>
<td>FID1, FID2, FID3</td>
<td>3</td>
<td>Fiducial mark. There is nothing to buy or mount. Fiducial10-20</td>
<td>N/A</td>
<td>CMP-0077181-1</td>
<td>N/A</td>
<td>N/A</td>
</tr>
<tr>
<td>H1, H2, H3, H4</td>
<td>4</td>
<td>Bumpon, Hemisphere, 0.44 X 0.20, Clear Bumpon_SJ5003Transparent</td>
<td>SJ-5303 (CLEAR)</td>
<td>CMP-0003620-1</td>
<td>SJ-5303</td>
<td>3M</td>
</tr>
<tr>
<td>R1, R2</td>
<td>2</td>
<td>1 kOhms ±1% 0.1W, 1/10W Chip Resistor 0603 (1608 Metric) Thick Film</td>
<td>FP-CRCW06031K00FKEAC_0603-MFG</td>
<td>CMP-0090742-1</td>
<td>CRCW06031K00FK</td>
<td>Vishay</td>
</tr>
<tr>
<td>R3, R4</td>
<td>2</td>
<td>0 Ohms Jumper 0.1W, 1/10W Chip Resistor 0402 (1005 Metric) - Thick Film</td>
<td>FP-CR0402-10W-000T_0402-MFG</td>
<td>CMP-0094948-1</td>
<td>CR0402-10W-000T</td>
<td>Venkel</td>
</tr>
<tr>
<td>SH-J1, SH-J2, SH-J3, SH-J4</td>
<td>4</td>
<td>Shunt, 100mil, Flash Gold, Black</td>
<td>SPC02SYAN</td>
<td>CMP-0003642-1</td>
<td>SPC02SYAN</td>
<td>Sullins Connector Solutions</td>
</tr>
<tr>
<td>U1</td>
<td>1</td>
<td>0.5 ppm/°C Maximum Drift, 0.1 ppmp-p 1/f Noise, 350 µA current, Precision Voltage Reference</td>
<td>D0008A-MFG</td>
<td>CMP-0095913-1</td>
<td>REF54250QDRQ1</td>
<td>Texas Instruments</td>
</tr>
<tr>
<td>VIN1</td>
<td>1</td>
<td>Header, 100mil, 2x1, Gold, TH</td>
<td>TSW-102-07-G-S</td>
<td>CMP-0075405-3</td>
<td>TSW-102-07-G-S</td>
<td>Samtec</td>
</tr>
</tbody>
</table>

5 Compliance Information

5.1 Compliance and Certifications

REF54EVM EU Declaration of Conformity (DoC) for Restricting the Use of Hazardous Substances (RoHS) (SSZQR85).

6 Additional Information

6.1 Trademarks

All trademarks are the property of their respective owners.
STANDARD TERMS FOR EVALUATION MODULES

1. Delivery: TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an “EVM” or “EVMs”) to the User (“User”) in accordance with the terms set forth herein. User’s acceptance of the EVM is expressly subject to the following terms.

1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM (“Software”) shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software.

1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.

2 Limited Warranty and Related Remedies/Disclaimers:

2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.

2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects within ten (10) business days after the defect has been detected.

2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

WARNING

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI’s recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI’s recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:
EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGRADATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.
Regulatory Notices:

3.1 United States

3.1.1 Notice applicable to EVMs not FCC-Approved:

**FCC NOTICE:** This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

**CAUTION**

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user’s authority to operate the equipment.

**FCC Interference Statement for Class A EVM devices**

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

**FCC Interference Statement for Class B EVM devices**

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

**Concerning EVMs Including Radio Transmitters:**

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

**Concernant les EVMs avec appareils radio:**

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L’exploitation est autorisée aux deux conditions suivantes: (1) l’appareil ne doit pas produire de brouillage, et (2) l’utilisateur de l’appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d’en compromettre le fonctionnement.

**Concerning EVMs Including Detachable Antennas:**

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.
Concernant les EVMs avec antennes déetchables

Conformément à la réglementation d’Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d’un type et d’un gain maximal (ou inférieur) approuvé pour l’émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l’intention des autres utilisateurs, il faut choisir le type d’antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l’intensité nécessaire à l’établissement d’une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d’antenne énumérés dans le manuel d’usage et ayant un gain admissible maximal et l’impédance requise pour chaque type d’antenne. Les types d’antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l’exploitation de l’émetteur.

3.3 Japan

3.3.1 Notice for EVMs delivered in Japan: Please see [http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page](http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page) 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。


3.3.2 Notice for Users of EVMs Considered “Radio Frequency Products” in Japan: EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry’s Rule for Enforcement of Radio Law of Japan.
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

【無線電波を送信する製品の開発キットをお使いになる際の注意事項】 開発キットの中には技術基準適合証明を受けていないものがあります。技術基準適合証明を受けていないものご使用に際しては、電波法遵守のため、以下のいずれかの措置を取っていただく必要があります。

1. 電波法施行規則第6条第1項第1号に基づく平成16年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用いただく。
2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。上記を遵守頂けない場合は、電波法の罰則が適用される可能性があることをご留意ください。日本テキサス・インスツルメンツ株式会社
東京都新宿区西新宿6丁目24番1号
西新宿三井ビル

3.3.3 Notice for EVMs for Power Line Communication: Please see [http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page](http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page)

電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。


3.4 European Union

3.4.1 For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.
EVM Use Restrictions and Warnings:

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 Safety-Related Warnings and Restrictions:

4.3.1 User shall operate the EVM within TI’s recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User’s handling and use of the EVM and, if applicable, User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. Accuracy of Information: To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

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