

1.5 Gbps 4x4 LVDS Crosspoint Switch

DS10CP154 Evaluation Kit

USER MANUAL

Part Number: DS10CP154EVK NOPB

For the latest documents concerning these products and evaluation kit, visit lvds.national.com. Schematics and gerber files are also available at lvds.national.com.

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Overview

The DS10CP154EVK is an evaluation kit designed for demonstrating performance of DS10CP154, a 1.5 Gbps 4x4 LVDS Crosspoint Switch. The evaluation kit is comprised of the DS10CP154 with its associated input and output SMA connectors and jumpers to manually configure the switch.

The purpose of this document is to familiarize the user with the DS10CP154EVK, to suggest test setup procedures and instrumentation to test the device optimally, and to guide the user through some typical measurements that demonstrate the performance of the DS10CP154 in typical applications.



Figure 1. Photo of the DS10CP154EVK

Description

Figure 2 shows the top layer drawing of the PCB with the silkscreen annotations. The 5.25 by 5.25 inch, four-layer PCB is designed to evaluate the functions of the DS10CP154.

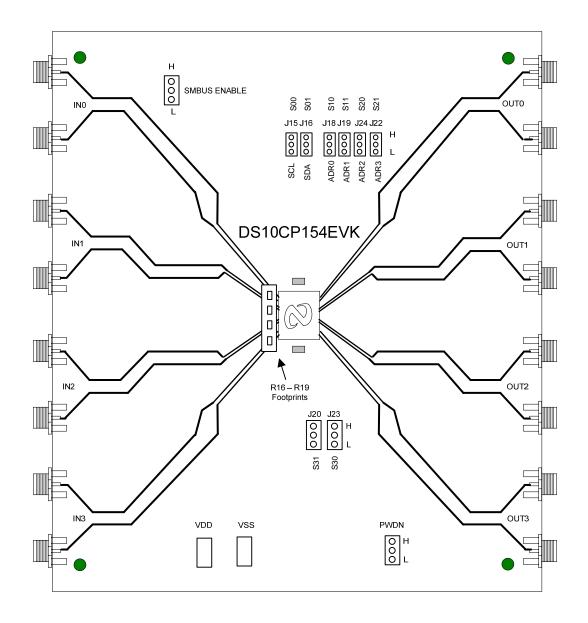


Figure 2. Top Layer DS10CP154EVK

Evaluation

This section provides recommended test setup procedure for the device evaluation. Figure 3 depicts a typical setup and instrumentation you may use for the device evaluation.

- 1. Configure the test setup as shown in Figure 3.
- 2. Set the desired INn to OUTn drivers by selecting S00, S01, S10, S11, S20, S21, S30 and S31 according to Tables 1 4.
- 3. Apply + supply (3.3V typical) to the VDD and supply (ground) to the VSS connectors.
- 4. Set the PWDN* pin (J17) to a high state.
- 5. Connect a signal source (signal generator, data source, or an LVDS driver) to the desired INn inputs on the board and adjust the signal parameters (VOH, VOL, VCM) so that they comply with the device input recommendations.
- 6. Connect an oscilloscope to the selected OUTn outputs and view the output signals with an oscilloscope with the analog bandwidth of at least 5 GHz.

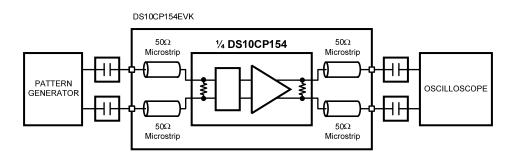


Figure 3. DS10CP154 Test Setup Example

Switch Configuration Truth Tables

S01	S00	Input Selected
0	0	INO
0	1	IN1
1	0	IN2
1	1	IN3

Table 1. Input Select Pins Configuration for the Output OUT0

S11	S10	Input Selected
0	0	INO
0	1	IN1
1	0	IN2
1	1	IN3

Table 2. Input Select Pins Configuration for the Output OUT1

S21	S20	Input Selected
0	0	INO
0	1	IN1
1	0	IN2
1	1	IN3

Table 3. Input Select Pins Configuration for the Output OUT2

S31	S30	Input Selected
0	0	INO
0	1	IN1
1	0	IN2
1	1	IN3

Table 4. Input Select Pins Configuration for the Output OUT3

Typical Performance

This section of the User Manual shows a typical eye diagram you can expect to see when evaluating the DS10BR150EVK.

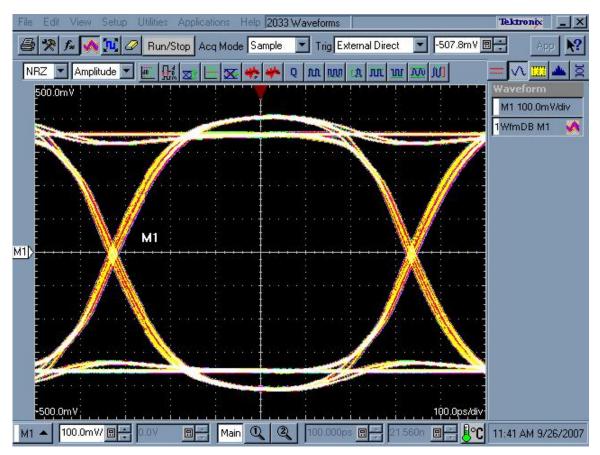
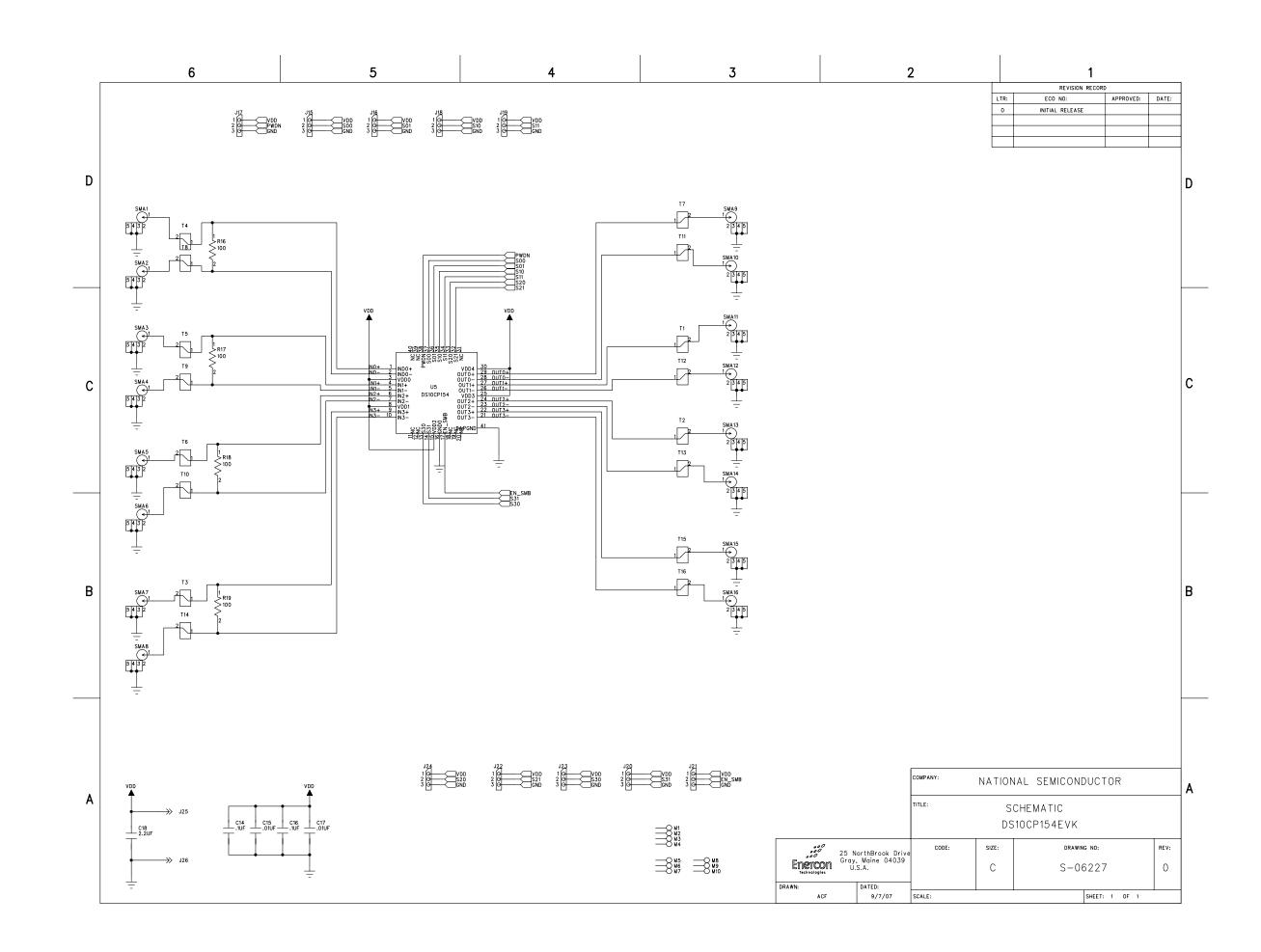


Figure 4. DS10CP154 1.5 Gbps NRZ PRBS-7 Output Eye Diagram

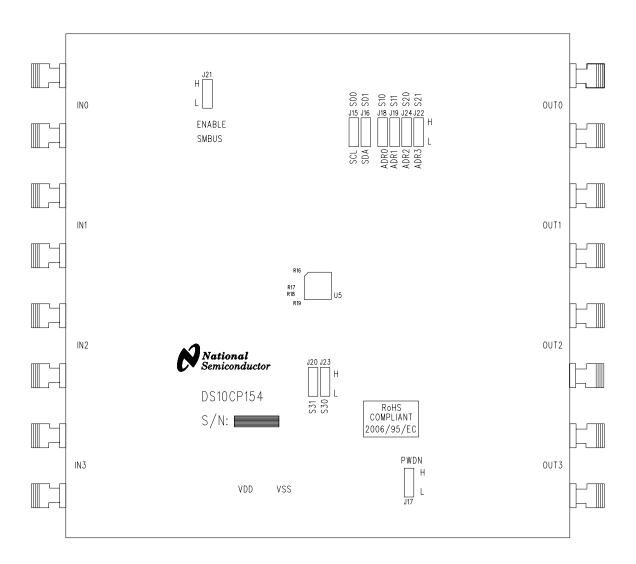


ENERCON - BILL OF MATERIALS				PCBA, DS10CP154EVK, ROHS					ate: PL Status: 2/2007 Release Creation Dat	ased	
		CP154 EVK, ROHS			DS10CP154			Arlene			
ltem	Part Type	Part Number/Value	Mfg	NoSub	Description	Qty	SMT	Ref Des	Notes	Rev	
1 PCB		P-06244R0	ENERCON		DS10CP154EVK: 5.25x5.25x.060in, 4 layer				Bd: (133.35x 133.35mm) Panel: (10.60x5.25in) (269.24x 133.35mm) 2 bds/panel	0	
2											
3	IC	DS10CP154TSQ/NOPB	NAT		LVDS Crosspoint Switch,, LLP40, Pb-Free	1	х	υ5	Customer Supplied	0	
4											
5	CAP	0402YC103KAT	AVX		.01µF, 16V, ±10%, 0402, Ceramic, X7R, Pb-Free	2	X	C15,17		0	
	<alt></alt>	C0402C103K4RAC	KEMET		.01µF, 16V, ±10%, 0402, Ceramic, X7R, Pb-Free						
	<alt></alt>	ECJ-0EB1C103K	PANA		.01µF, 16V, ±10%, 0402, Ceramic, X7R, Pb-Free						
6	CAP	C0402C104K8RAC	KEMET		.1µF, 10V, ±10%, 0402, Ceramic, X7R, Pb- Free	2	X	C14,16		0	
7	CAP	C1206C225K4RAC	KEMET		2.2µF, 16V, ±10%, 1206, Ceramic, X7R, Pb-Free	1	х	C18		0	
	<alt></alt>	ECJ-3YB1C225K	PANA		2.2µF, 16V, ±10%, 1206, Ceramic, X7R, Pb-Free						
8											
9	CONN	1287-ST	KEYSTONE		Faston, Male, .250x.032, Pb-Free	2		J25-26		- 0	
10	CONN	142-0701-851	EMERSON		SMA, Jack Receptacle, 50 OHM, Pb-Free	16		SMA1-16		0	
11	CONN	TSW-103-07-G-S	SAMTEC		Header, 3p, Male, .100"sp, Gold, Pb-Free	10		J15-24		0	
12											
13	STENCL	T-06246R0	ENERCON		STENCIL FABRICATION, TOP, DS10CP154EVK	1				0	
14	STENCL	T-06247R0	ENERCON		STENCIL FABRICATION, BOTTOM, DS10CP154EVK	1				0	
15											
16	REF	C-06243R0	ENERCON		PALLET DWG, DS10CP154EVK					0	
17	REF	C-06245R0	ENERCON		FABRICATION DWG, DS10CP154EVK		1			0	

E	ENERCON - BILL OF MATERIALS Main Product: PCBA, DS10CP154 EVK, ROHS						Vumber: 181–(Rev Date: 9/12/2007	PL Status: Released	
					PCBA, DS10CP154EVK, ROHS DS10CP154		Responsible Eng/Mgr: Crea Ar			tor: .ene Fox	Creation Date 9/12/20	
ltem	Part Type	Part Number/Value	Mfg	NoSub	Description	Qty	SMT	Re	f Des		Notes	Rev
18	REF	S-06227R0	ENERCON		SCHEMATIC, DS10CP154EVK							0
19												

Notes: DO NOT STUFF:

R16-19



	6	5	4	3	2		1		
		L	L			REV	REVISION RECORD ECO NO: APPROVED:	DATE:	
	SPECIFICATIONS: P/N	P-06244R0							
2. 3.	DIMENSIONS: INTERPRE' DIMENSIONS ARE IN INC MATERIAL: ROHS COMP POLYCLAD PCL-FR-37C FLAMMABILITY RATING: OF UL796 WITH A FLAM PER UL796, BOARD FAE	CORDANCE WITH (IAW) IPC-6012 CLASS 2. T DRAWING, DIMENSIONS AND TOLERANCES PE HES. 2LIANT/LEAD FREE ASSEMBLY CAPABLE WOV DHR, PCL-FRP-370HR PREPREG. FINISHED BOARD SHALL MEET THE REQUIRE! ME RATING OF 94V-0 OR BETTER. BRICATOR SHALL APPLY THEIR TRADEMARK, D THE FLAME RATING IN ETCH OR	'EN E-GLASS LAMINATE,		CD 5.25 + + + + + + + + + + + + + + + + + + +	+ X	CI 	1	D
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	FINISHED CONDUCTOR W	/SPACING: WIDTH 0.010 in. SPACING 0.008	in		$\begin{array}{cccccccccccccccccccccccccccccccccccc$	+ + + + + + + + + + + + + + + + + + +			
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		IGH RESISTANCE TRACES PER IPC-7721.			+ + + + + + + + + + + + + + + + + + + +	+ + + + + + + + + + + + + + + + + + + +			
(CI) 8.		USING DRILL DATA, DRILL PATTERN AND HO VARY WITHIN 0.004 in. (RADIAL ERROR) MAX		+ + + # + + + + +	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	+ + + # + + + # + + + +			
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(CI) 9.		LPI GLOSS BLUE OVER BARE COPPER IAW I	PC-SM-840.	t		1	u		
	SOLDERMASK MISREGIST NO OVERLAP PERMITTED	RATION SHALL NOT EXCEED +0.004 in. D ON PADS.						1	в
CI) 10.		DLESS NICKLE IMMERSION GOLD.							5
(CI) 11.		LOWING LAYERS SHALL BE SCREEN PRINTED Y INK IAW MIL-1-43553 PER APPROPRIATE R 1-TOP	USING WHITE						
	INK NOT PERMITTED ON		· • •		[]	LAYER 1	(FOIL) 50 ohm REF .018 TRACE	.s	
(LI) IZ.		TWIST SHALL NOT EXCEED 0.75% (0.00075 PC-A-600F WHEN MEASURED WITH 2.4.22.	in. PER in.)			LAYER 2	2 - GROUND PLANE	F	
13.	INSPECTION: IAW IPC-					LAYER 3	3 - VDD PLANE		
CI 14.	USING CAD GENERATED SHALL SUPPLY CERTIFIC	AL TEST: BARE BOARDS SHALL BE ELECTRI NET LIST DATA. VENDOR CATION OF BOARD CONTINUITY SUPPLIED AND SHALL MARK				ID MV.	4 - (FOIL)		
15	CERTIFICATION SHALL S	STATE HOW ACCEPTED BOARDS HAVE BEEN N	MARKED. 58 4 🛇 YES +/	/-0.003 /-0.003	25 NorthBrook Drive Gray, Maine 04039 U.S.A.		ABRICATION DWG	'	A
15.	MISC: ALL LAYERS VIE REFERENCES TO IPC ST	EWED FROM LAYER 1. TANDARDS ASSUME CURRENT REVISION.	125 4 × NO +,	/-0.003	DRAWN: ACF DATED: 9/10/07		OCP154EVK, ROHS		
				-	TOLERANCES UNLESS OTHERWISE NOTED:	CODE: SIZE:	DRAWING NO:	REV:	
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					.XX +/010 SC	ALE:	SHEET: 1 OF 1		

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