

**Test Data  
For PMP7882 Rev2  
11/16/2012**



**Test SPECIFICATIONS**

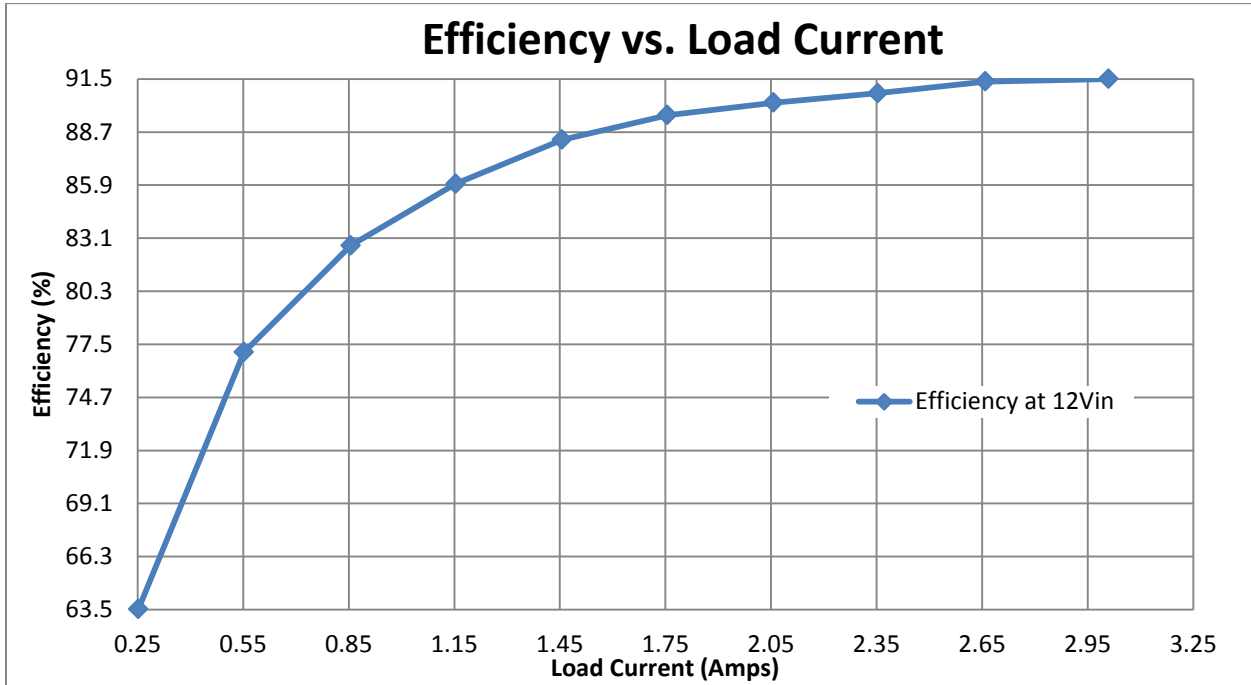
|                    |               |
|--------------------|---------------|
| <b>Vin nominal</b> | <b>12V</b>    |
| <b>Vout</b>        | <b>1.2V</b>   |
| <b>Iout</b>        | <b>3A Max</b> |
| <b>Fsw</b>         | <b>200kHz</b> |

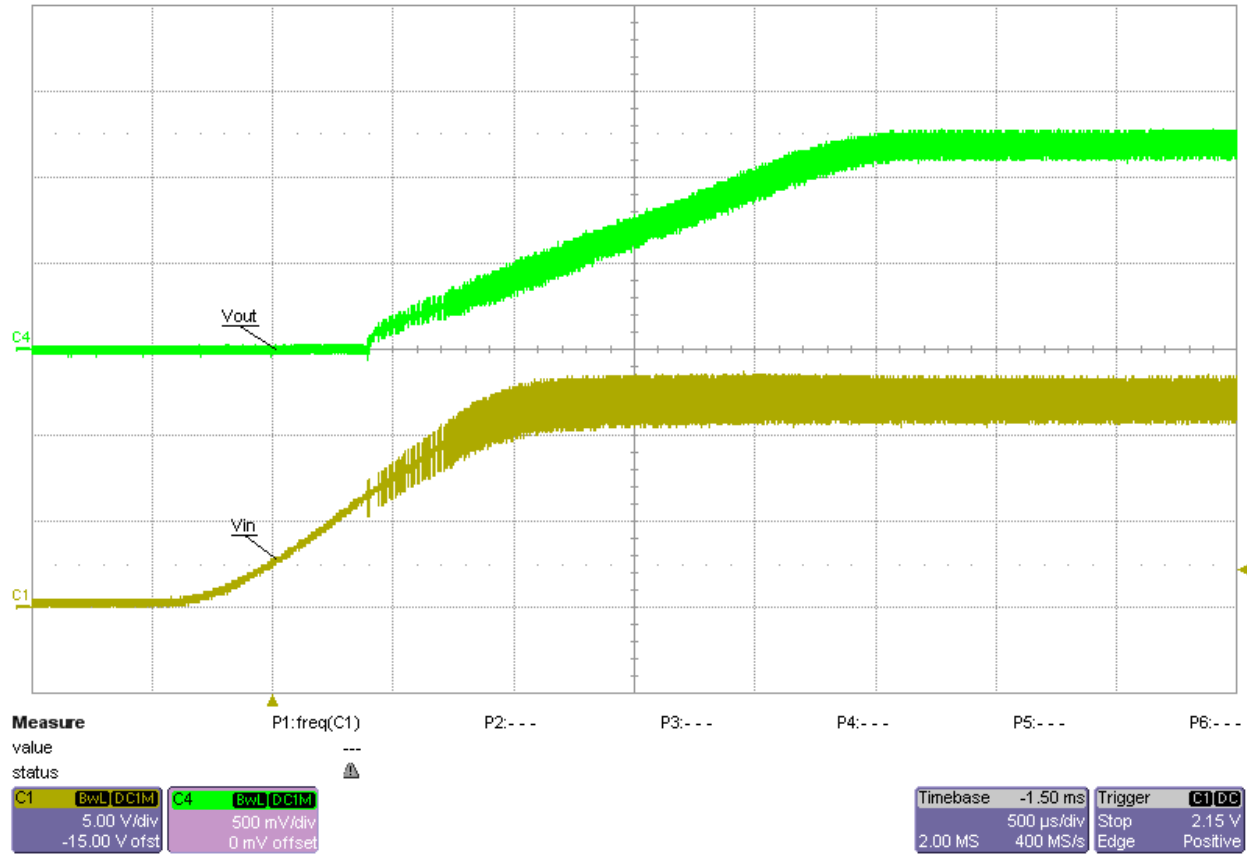
**Note: Rev2 change was replacing bottom FET with CSD17306Q5A to increase efficiency.**

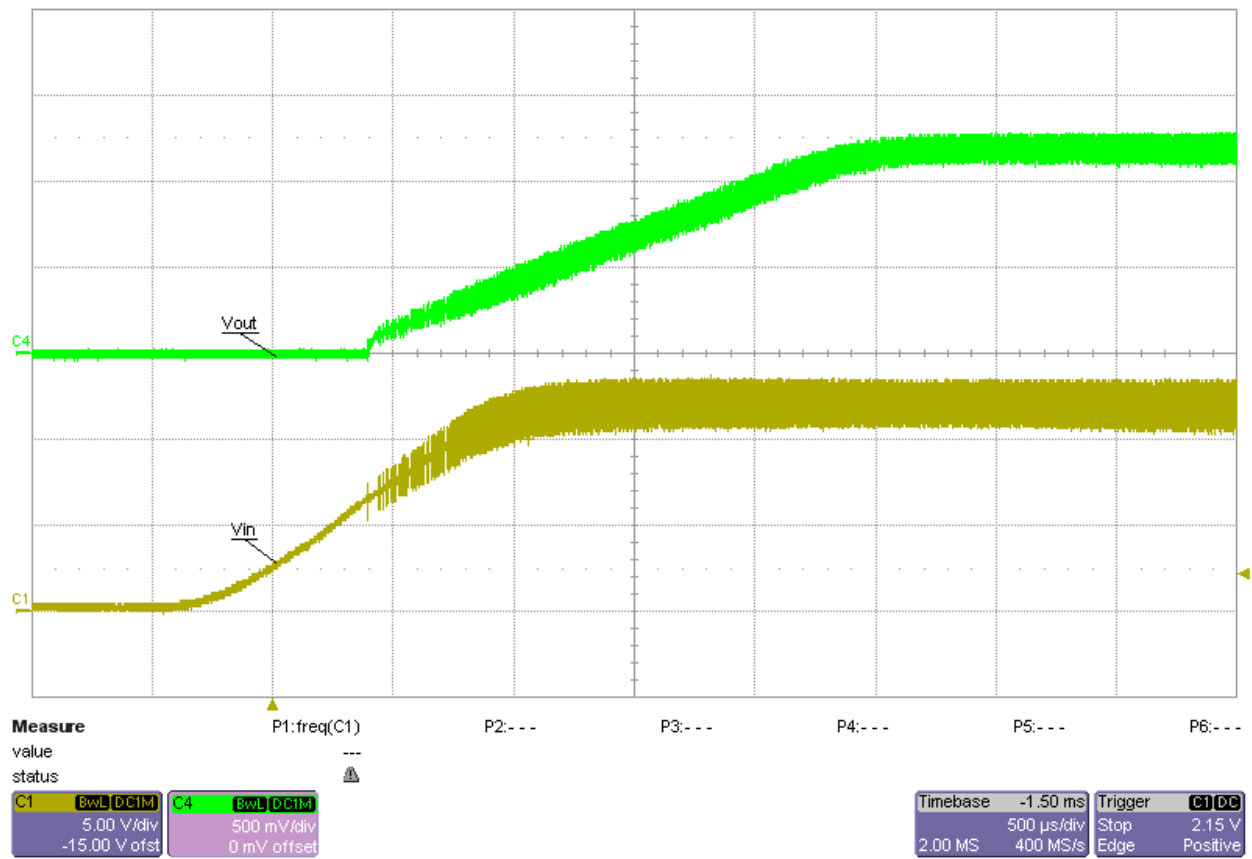
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## TYPICAL PERFORMANCE

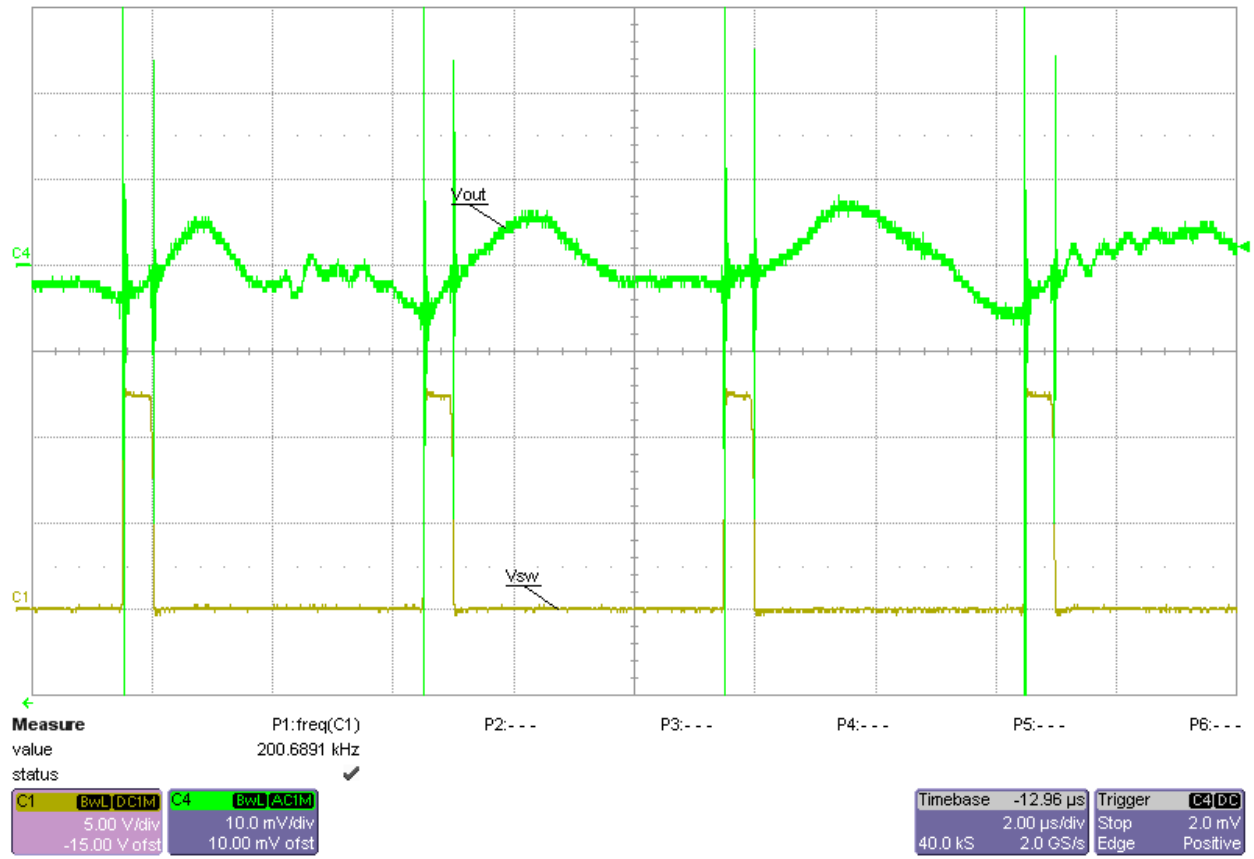
### EFFICIENCY



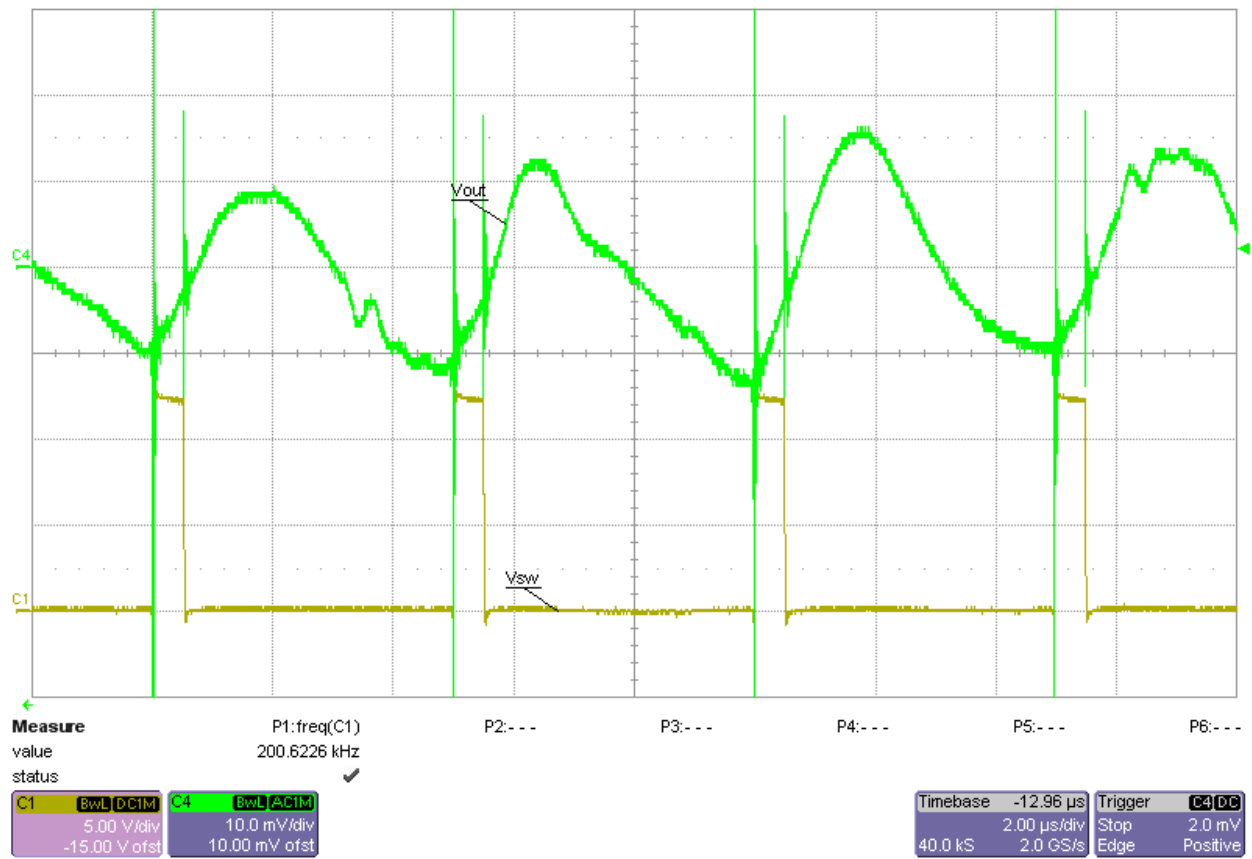
**Waveforms**

**Startup Into No Load at 12Vin**



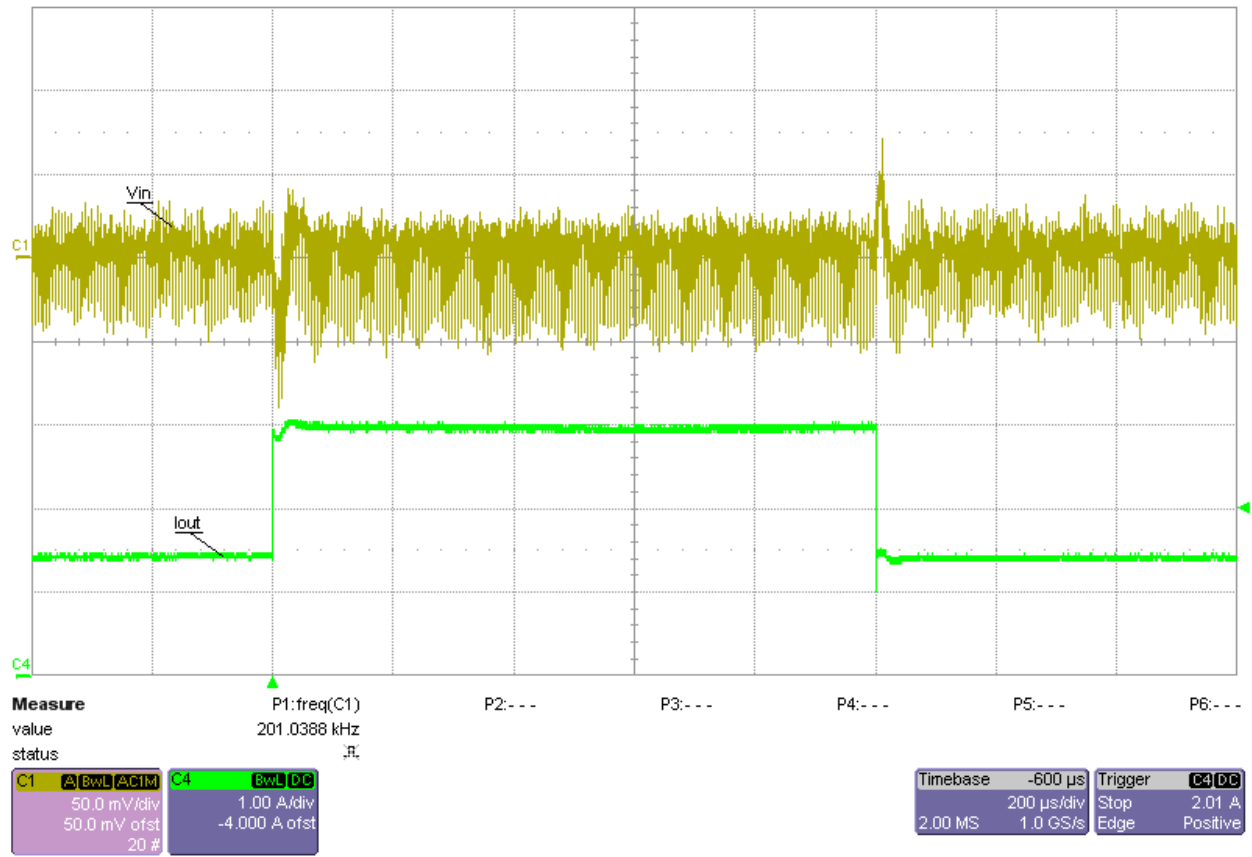
### Startup Into Full Load of 3A at 12Vin



**12Vin VSwitch with Vout ripple at No Load (Vripple  $\approx$  13mV )**

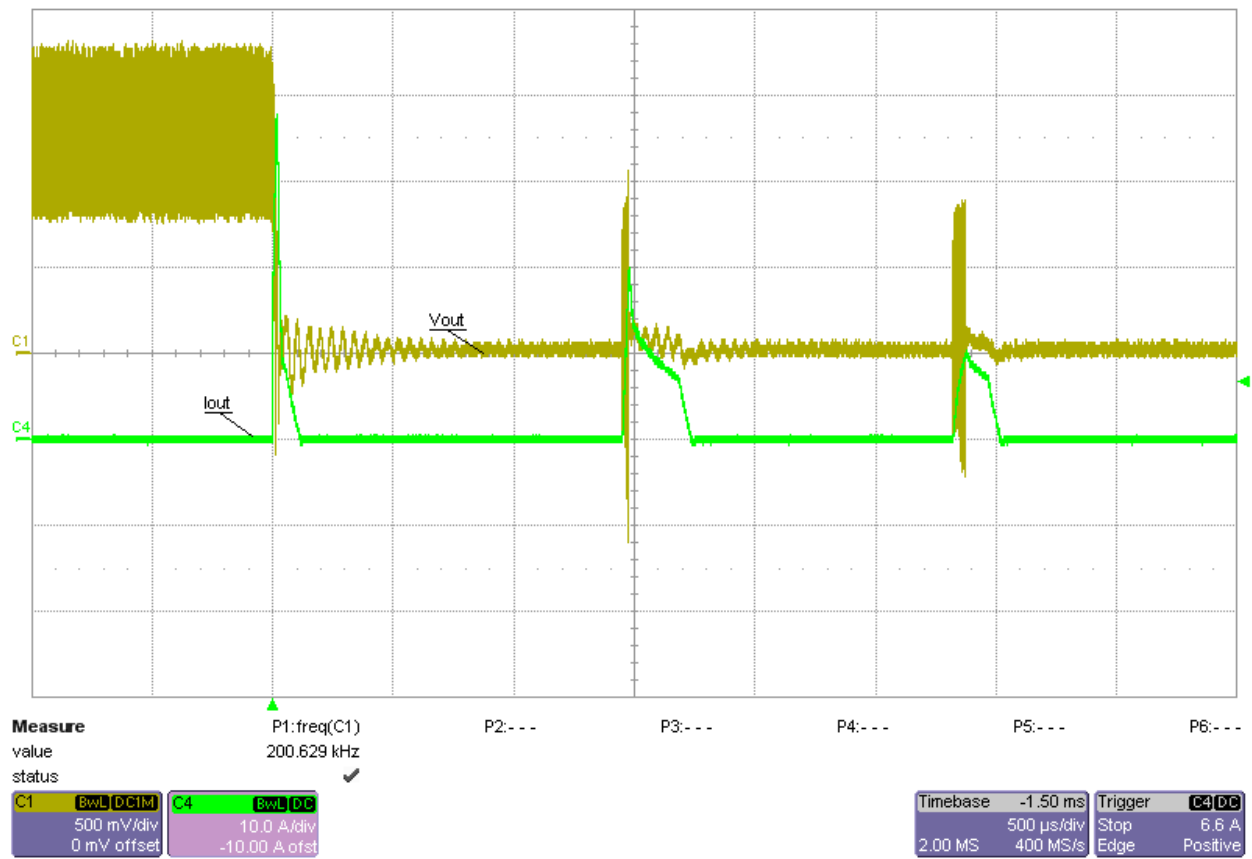


**12Vin VSwitch with Vout ripple at Full load of 3A (Vripple  $\approx$  28mV )**

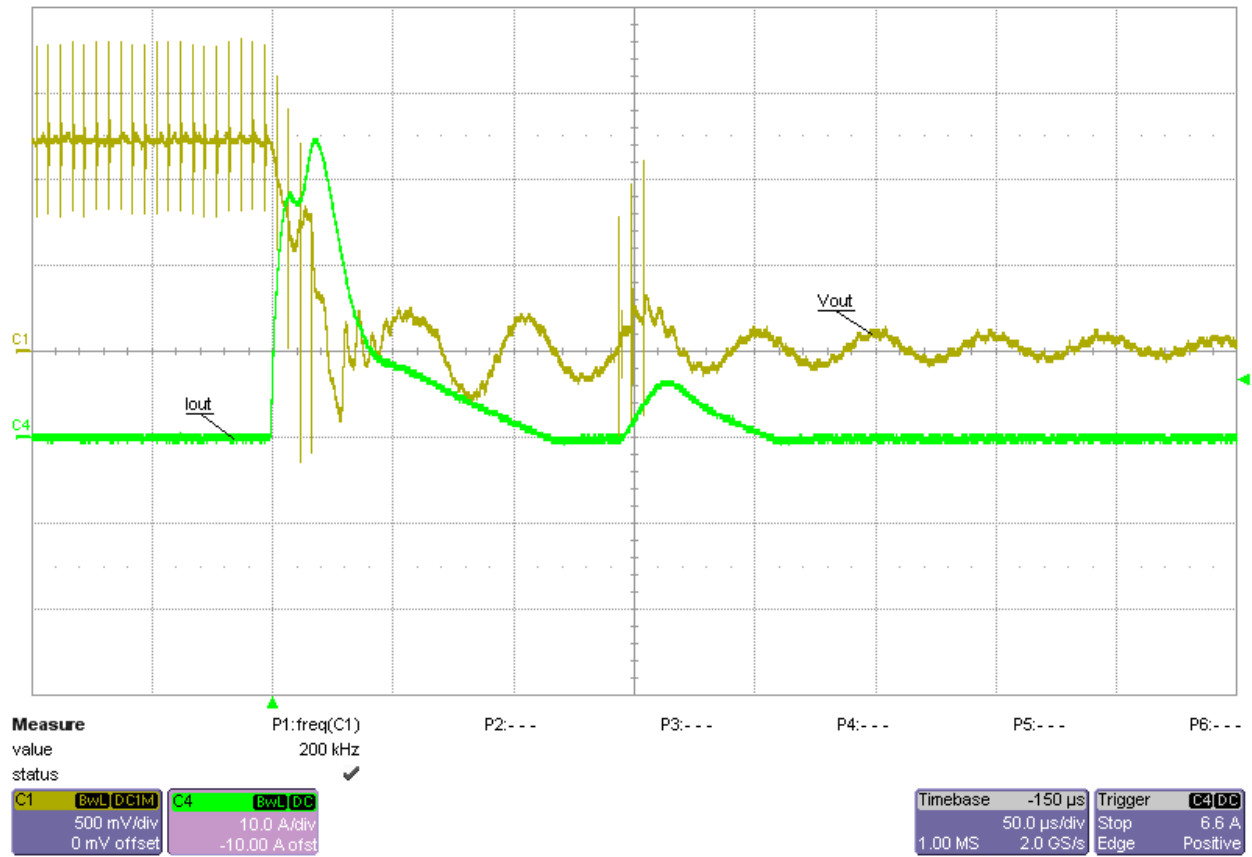


**Transient Response Load Step from 1.5A to 3A (50% to 100% load) at 12Vin (Note: CH1 is actually Vout)**

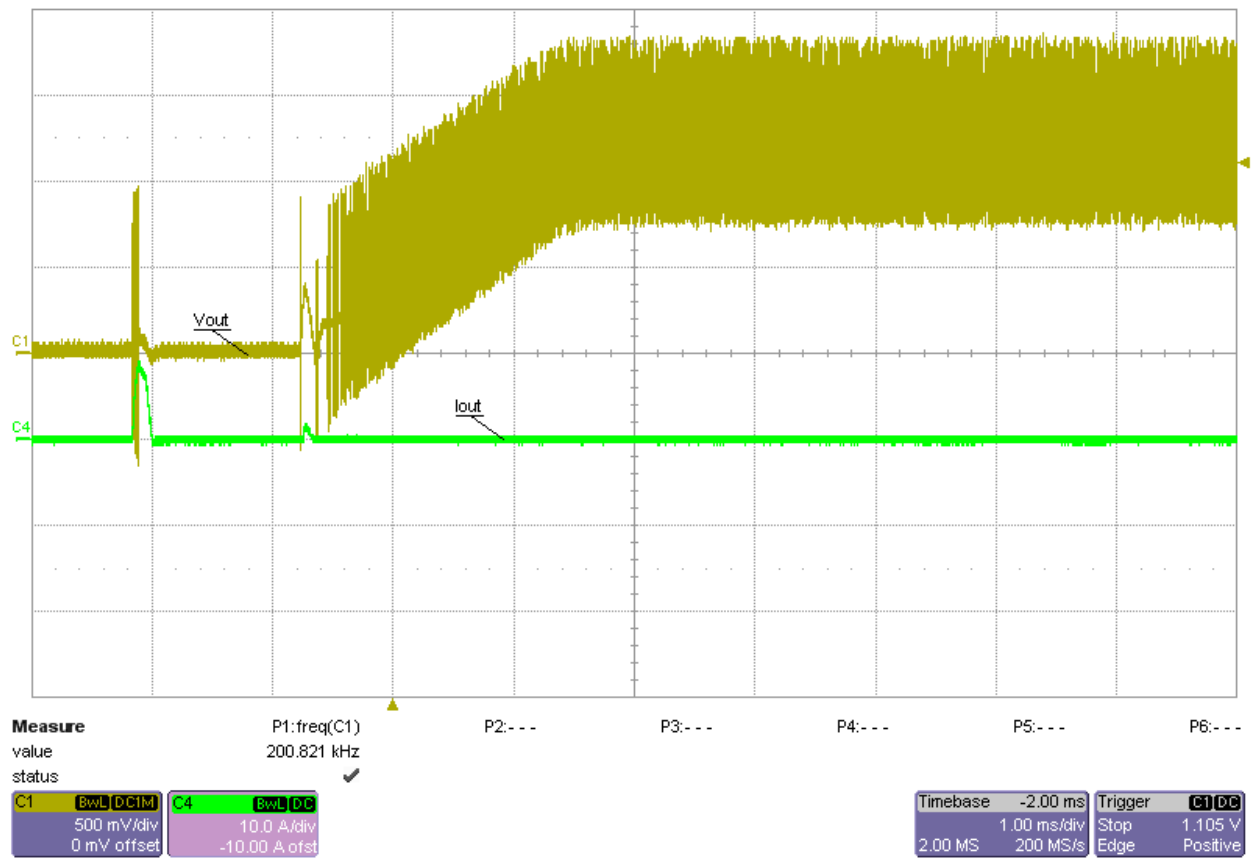




**Short Circuit Test (zoomed out) at 12Vin**

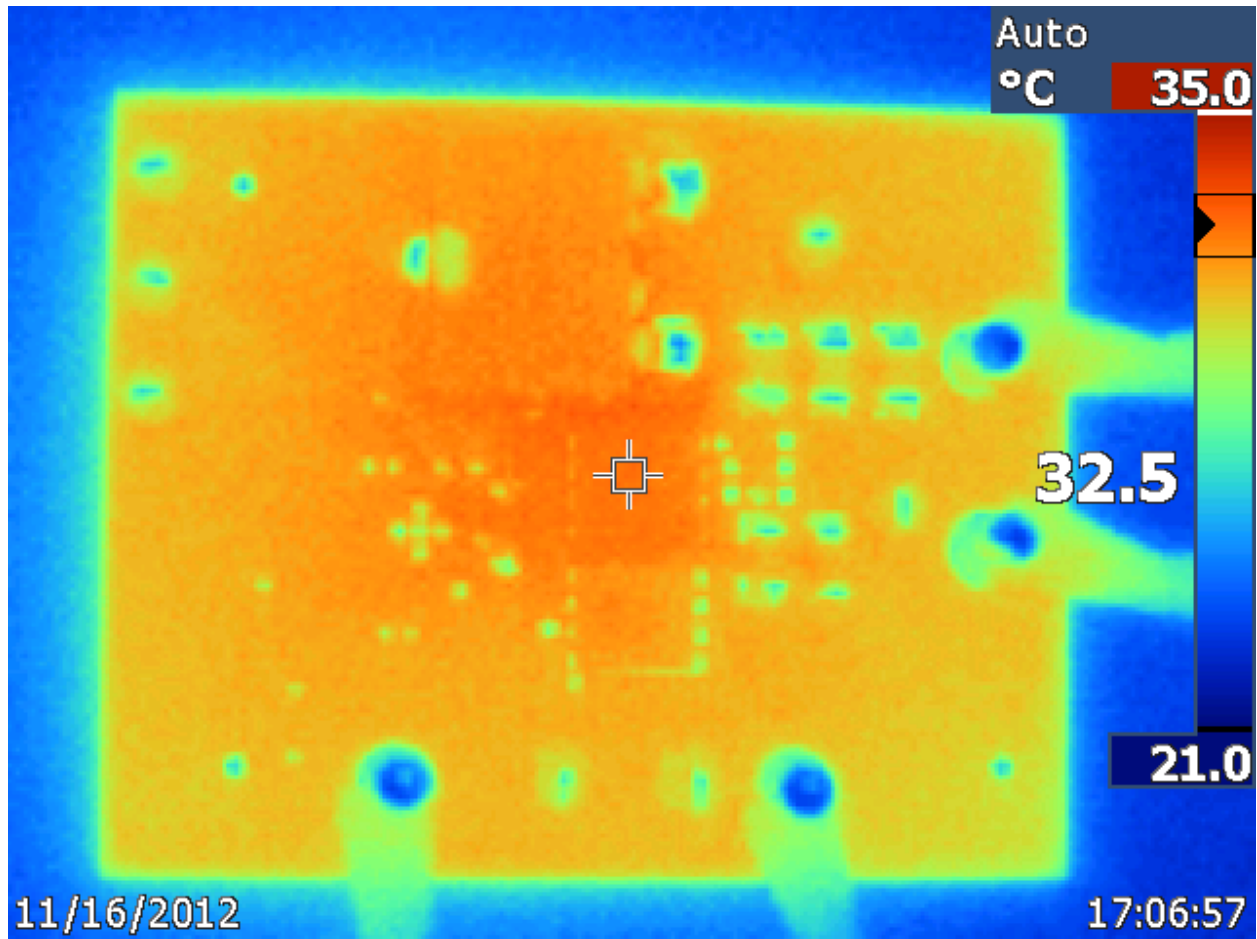


**Short Circuit Test (zoomed in) at 12Vin**



**Short Circuit Recovery at 12Vin**

**Thermal Data**

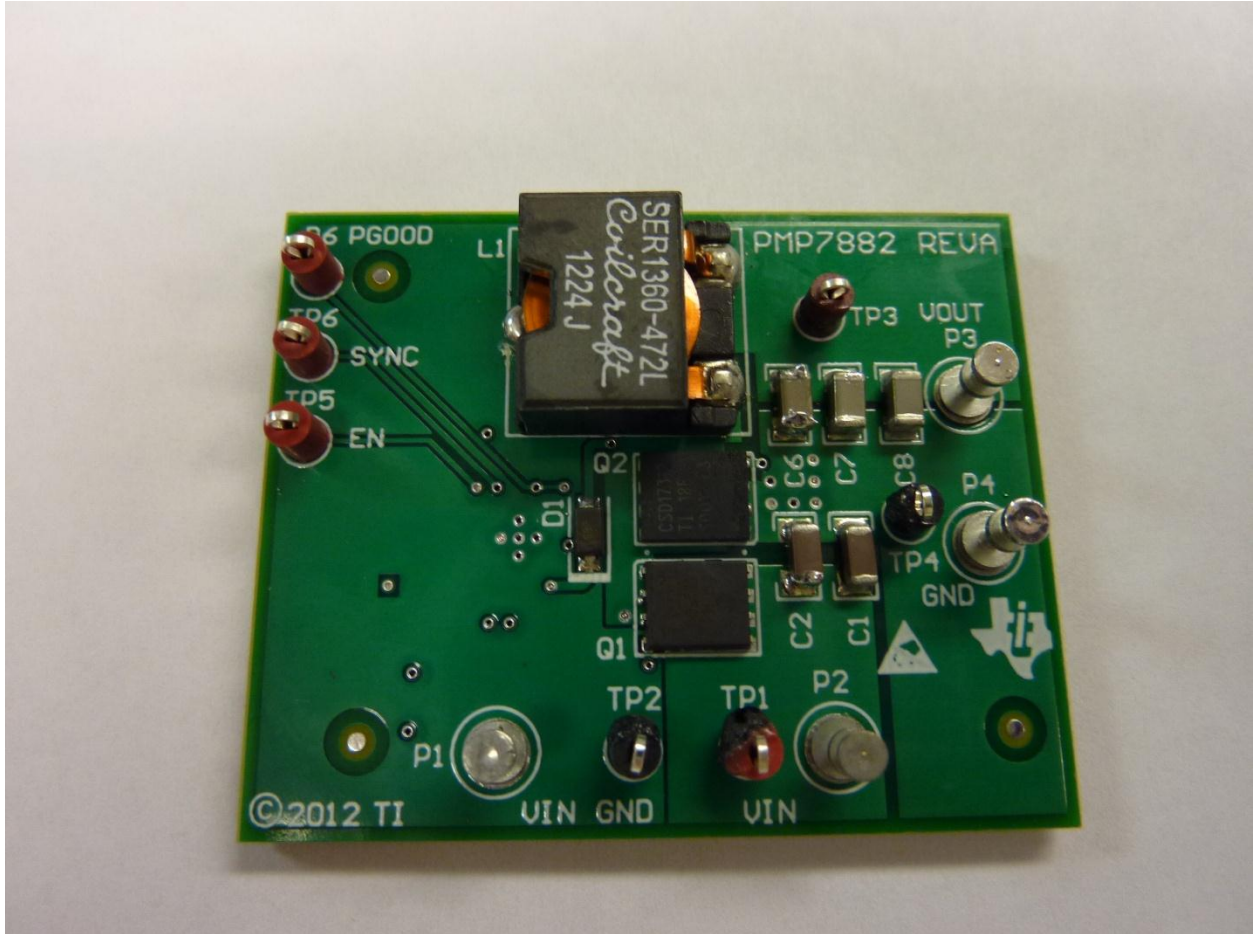


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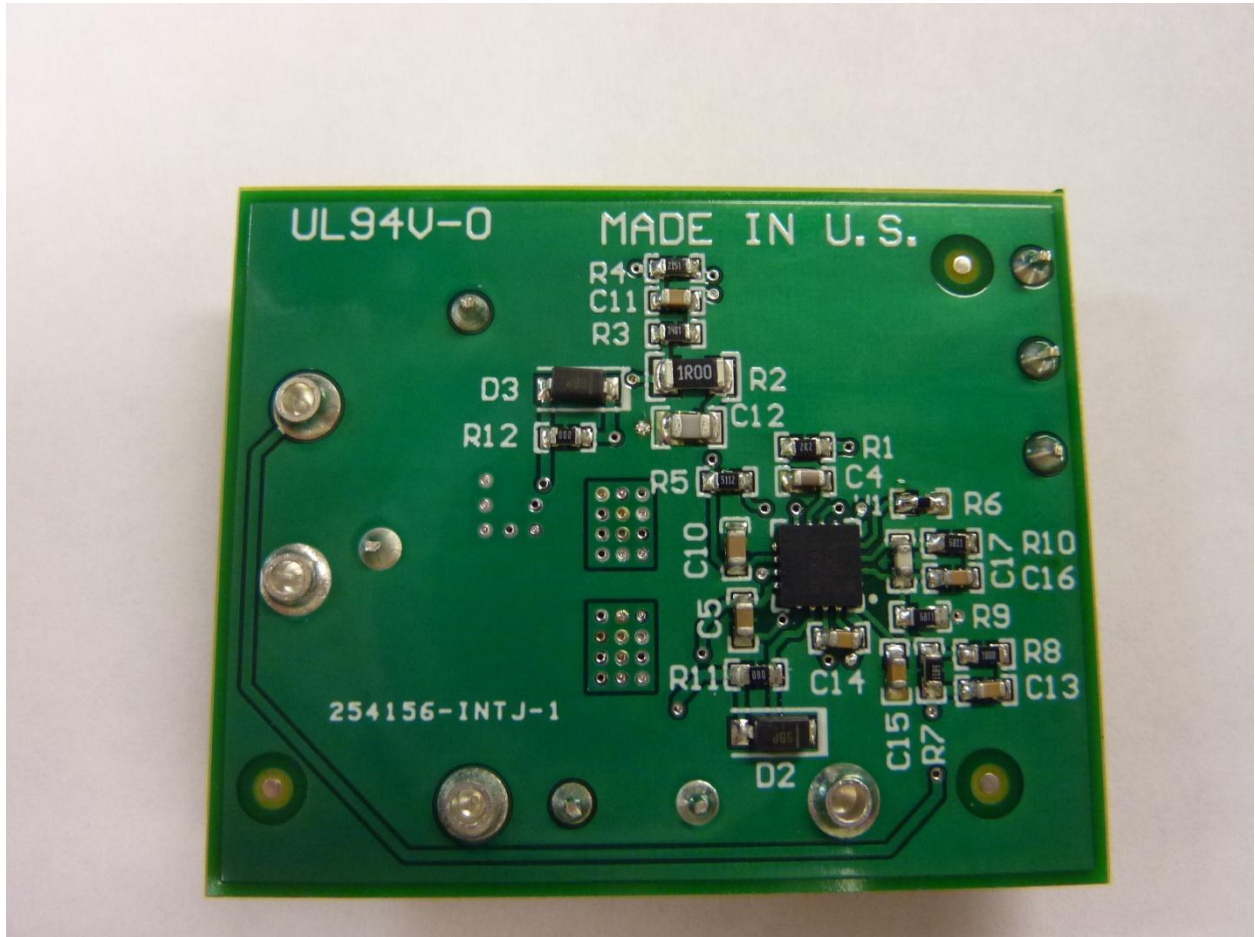
## FABRICATION

Board size: 1.75" x 1.42"

### Top Side



Bottom Side



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|                               |  |
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