

**Test Data  
For PMP20808  
March 14, 2017**



## Table of Contents

|   |    |
|---|----|
| 1. Design Specifications .....                          | 3  |
| 2. Circuit Description.....                             | 3  |
| 3. PMP20808 Board Photos .....                          | 4  |
| 4. Thermal Data.....                                    | 5  |
| 5. Efficiency .....                                     | 6  |
| 5.1 Efficiency Chart .....                              | 6  |
| 5.2 Efficiency Data.....                                | 6  |
| 6 Waveforms.....  | 7  |
| 6.1 Load Transient Response .....                       | 7  |
| 6.2 Startup .....                                       | 8  |
| 6.3 Output Voltage Ripple and Switch Node Voltage ..... | 10 |
| 6.4 Short Circuit .....                                 | 11 |

## 1. Design Specifications

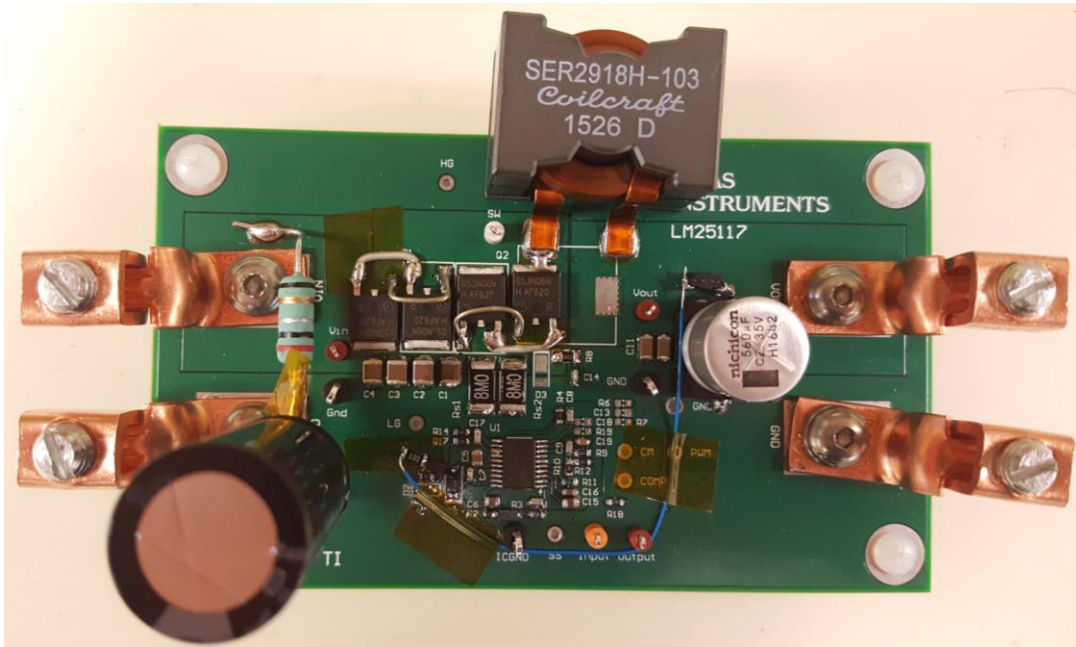
|                            |                 |
|----------------------------|-----------------|
| <b>Vin Minimum</b>         | <b>36VDC</b>    |
| <b>Vin Nominal</b>         | <b>38VDC</b>    |
| <b>Vin Maximum</b>         | <b>40VDC</b>    |
| <b>Vout</b>                | <b>25VDC</b>    |
| <b>Iout</b>                | <b>18A Max.</b> |
| <b>Switching Frequency</b> | <b>225KHz</b>   |

## 2. Circuit Description

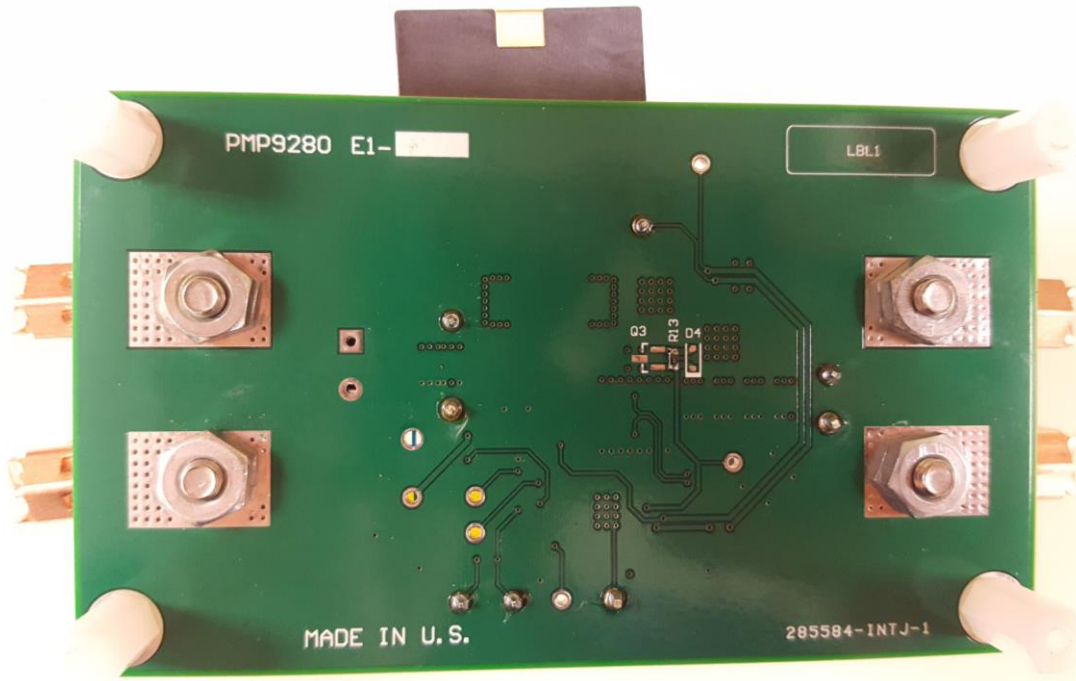
PMP20808 is a Single-Phase Synchronous Buck Converter using the LM5117 controller IC. The design accepts an input voltage of 36Vin to 40Vin (38Vin Nominal) and provides an output of 25Vout capable of supplying 18A of continuous current to the load. The design was built on the PMP9280 PCB, which was modified to the PMP20808 design configuration and requirements. The PCB is a 4-layer board with 1oz Copper on all layers.

### 3. PMP20808 Board Photos

Board Dimensions: 2.36" x 4.05"

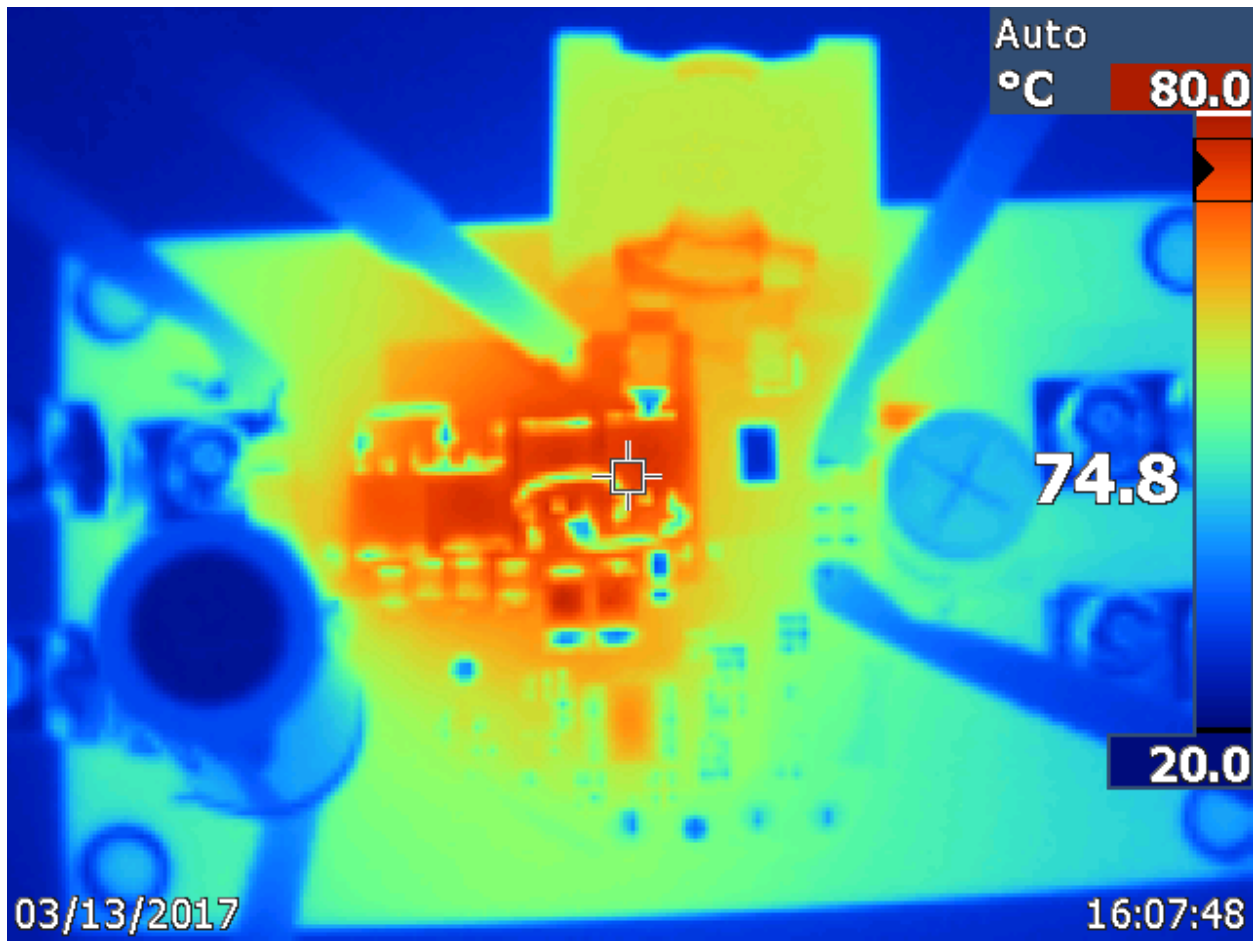


Board Photo (Top)



Board Photo (Bottom)

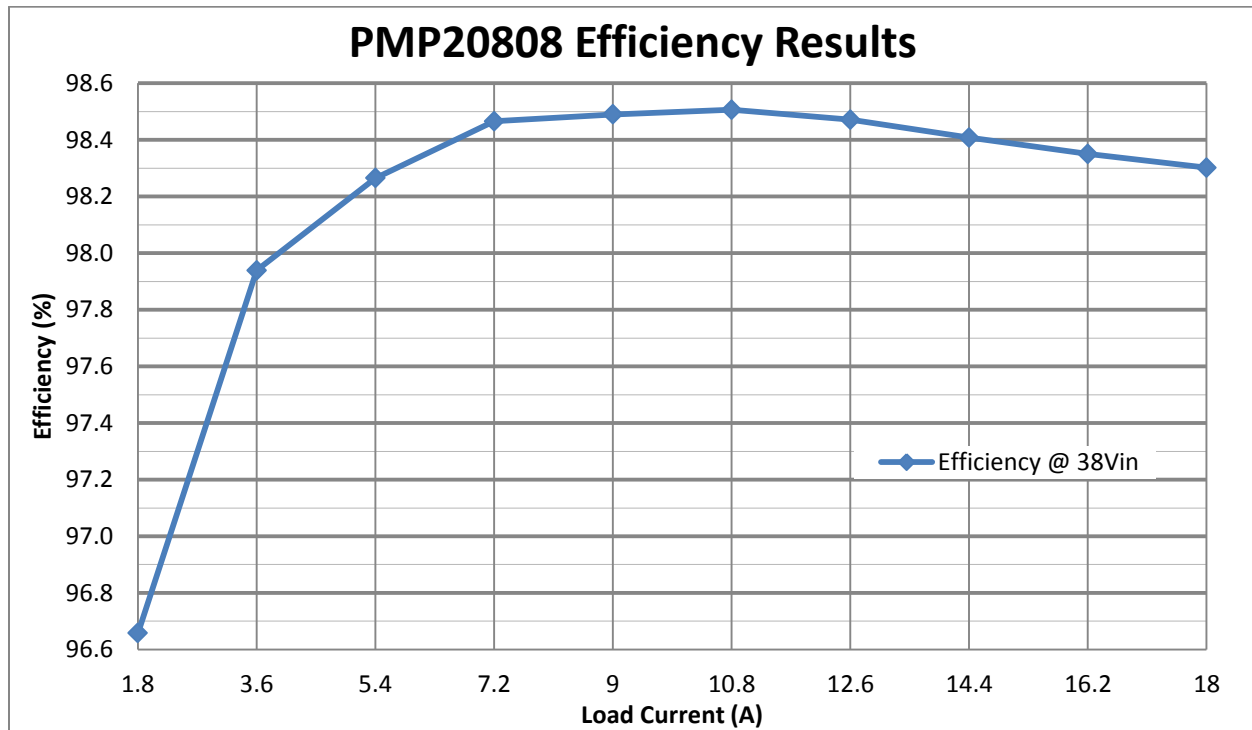
#### 4. Thermal Data



IR thermal image taken at steady state with 38Vin and 18A load (no airflow; ambient at room temp.)

## 5. Efficiency

### 5.1 Efficiency Chart

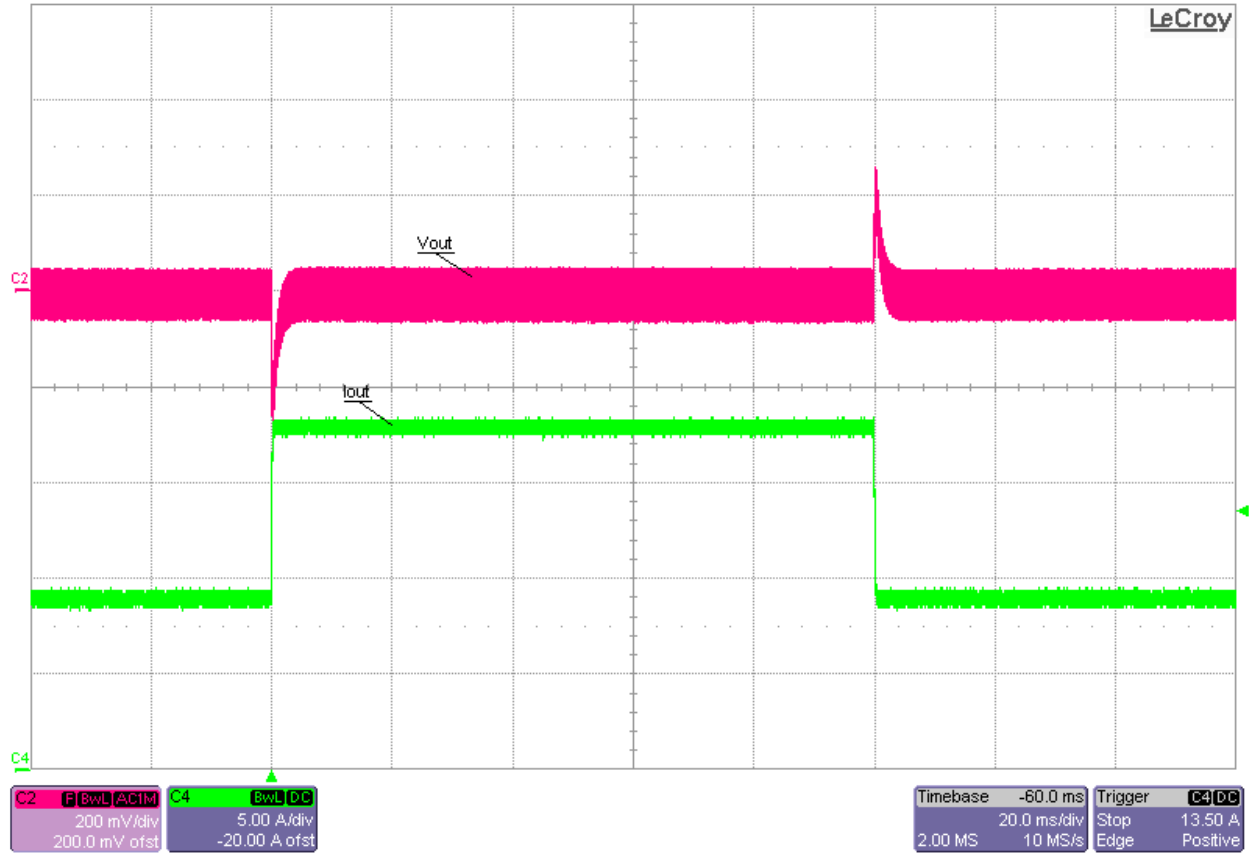


### 5.2 Efficiency Data

| Vin (V) | Iin (A) | Vout (V) | Iout (A) | Pin (W) | Pout (W) | Ploss (W) | Efficiency (%) |
|---------|---------|----------|----------|---------|----------|-----------|----------------|
| 38      | 1.223   | 24.956   | 1.8      | 46.47   | 44.92    | 1.55      | 96.7           |
| 38      | 2.414   | 24.956   | 3.6      | 91.73   | 89.84    | 1.89      | 97.9           |
| 38      | 3.609   | 24.956   | 5.4      | 137.14  | 134.76   | 2.38      | 98.3           |
| 38      | 4.802   | 24.955   | 7.2      | 182.48  | 179.68   | 2.80      | 98.5           |
| 38      | 6.001   | 24.955   | 9        | 228.04  | 224.60   | 3.44      | 98.5           |
| 38      | 7.2     | 24.955   | 10.8     | 273.60  | 269.51   | 4.09      | 98.5           |
| 38      | 8.403   | 24.955   | 12.6     | 319.31  | 314.43   | 4.88      | 98.5           |
| 38      | 9.61    | 24.956   | 14.4     | 365.18  | 359.37   | 5.81      | 98.4           |
| 38      | 10.818  | 24.957   | 16.2     | 411.08  | 404.30   | 6.78      | 98.4           |
| 38      | 12.026  | 24.957   | 18       | 456.99  | 449.23   | 7.76      | 98.3           |

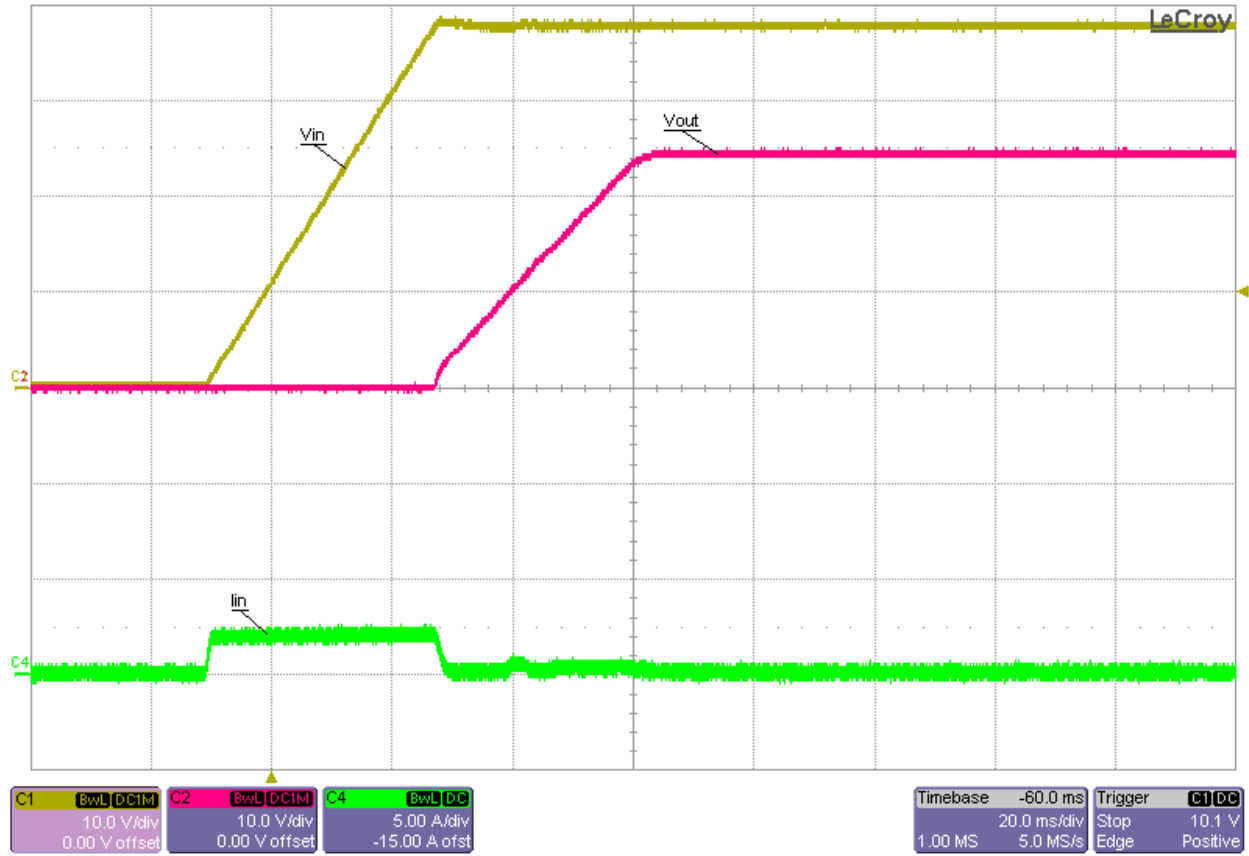
## 6 Waveforms

### 6.1 Load Transient Response



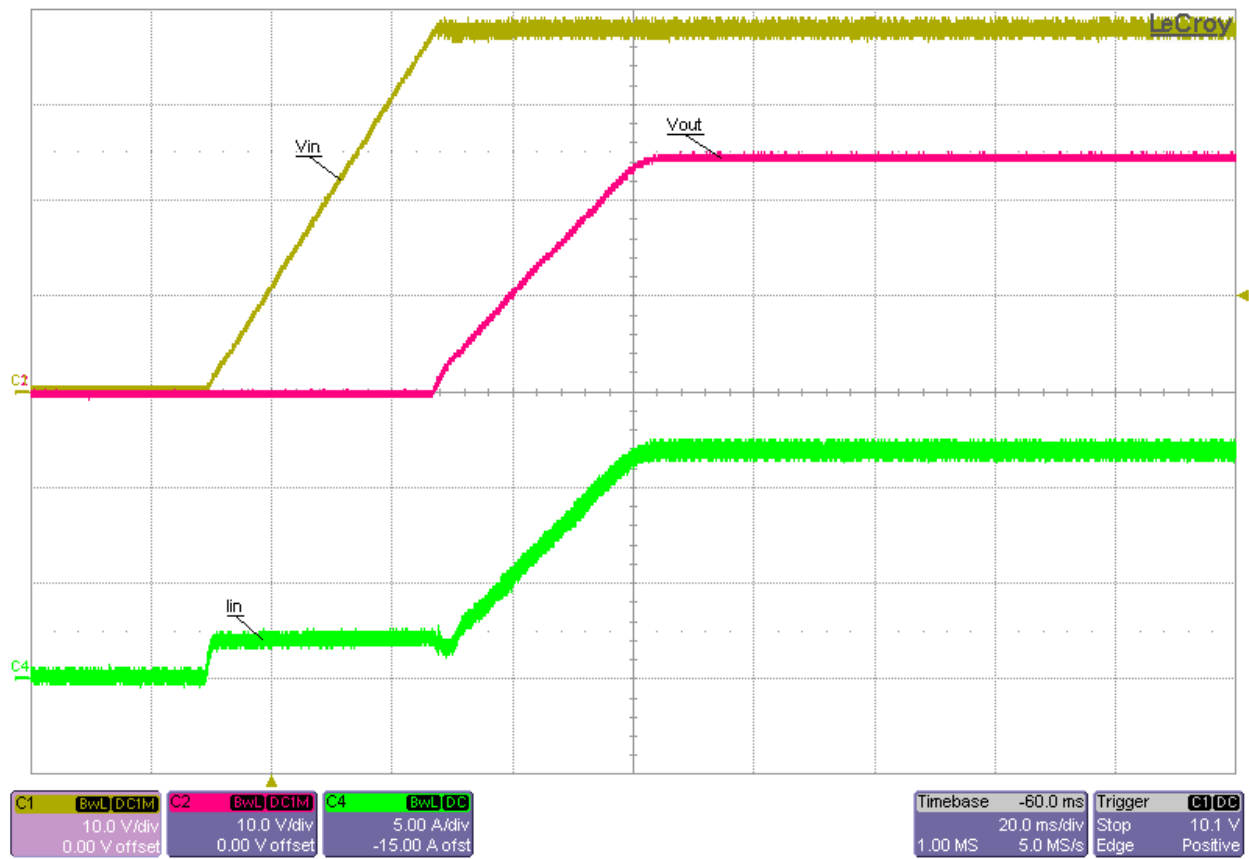
**Load Transient Response at 38Vin and 9A-to-18A (50%-to-100%) Load Step**

## 6.2 Startup



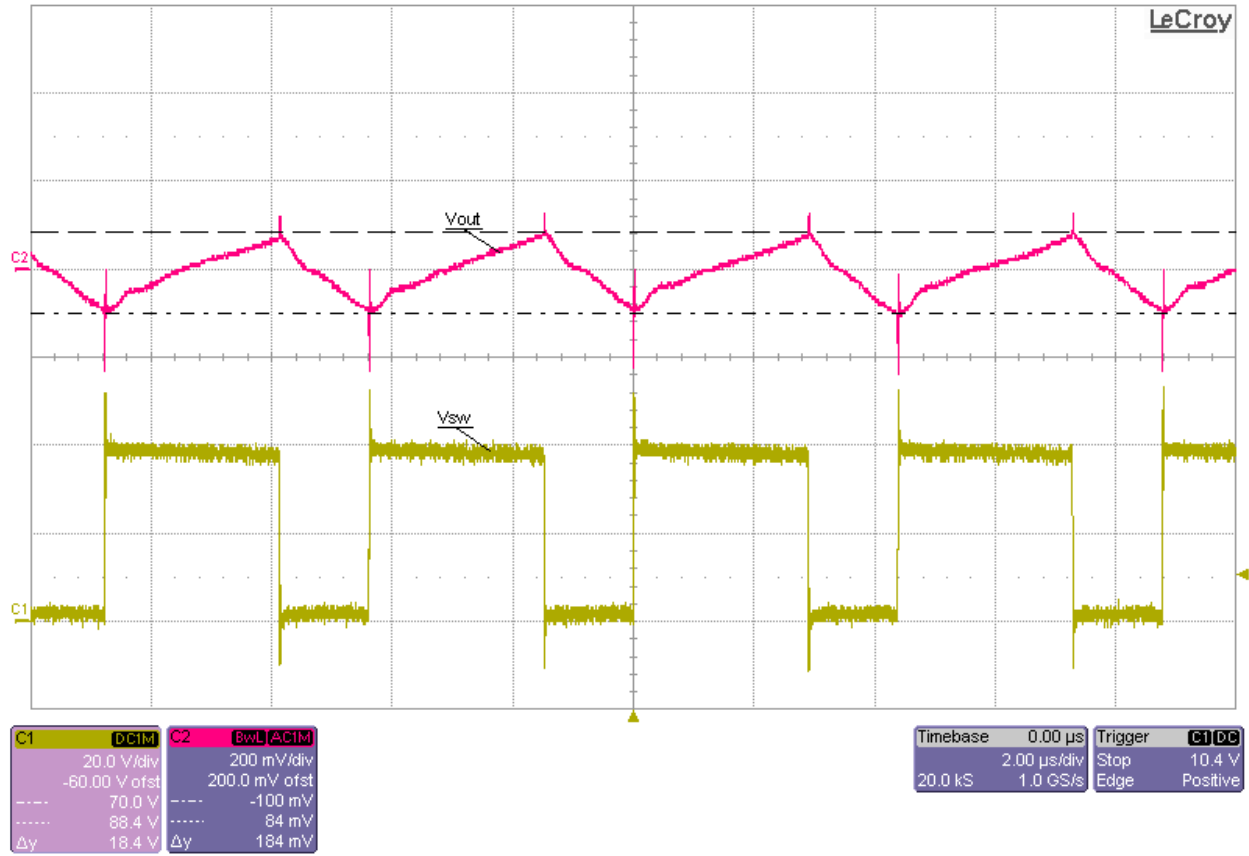
Startup into No Load at 38Vin





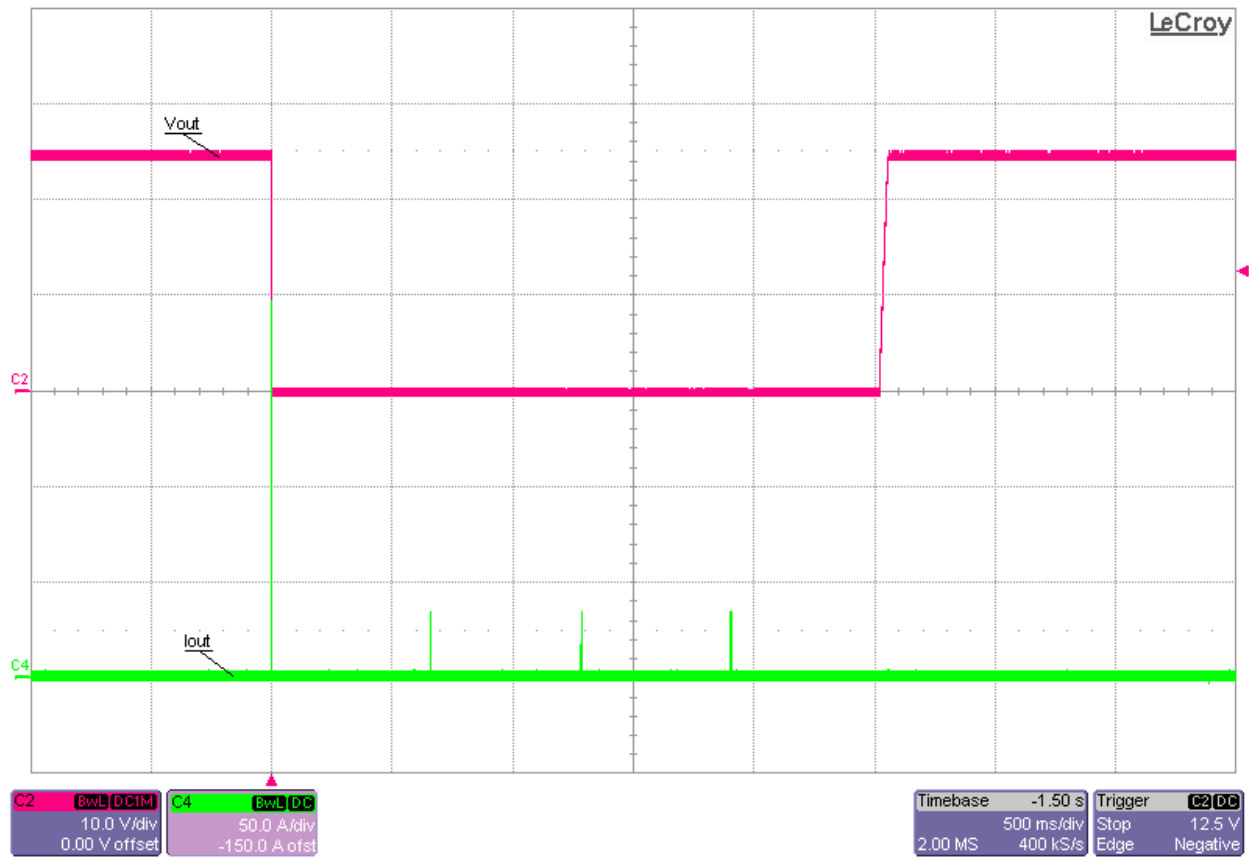
**Startup into 18A Constant-Current Load at 38Vin**

### 6.3 Output Voltage Ripple and Switch Node Voltage



Switch Node Voltage and Output Voltage Ripple at 38Vin and 18A Load ( $V_{ripple} \approx 185mVp-p$ )

### 6.4 Short Circuit



**Short Circuit Application and Recovery at 38Vin from/into No Load**

## IMPORTANT NOTICE FOR TI DESIGN INFORMATION AND RESOURCES

Texas Instruments Incorporated ("TI") technical, application or other design advice, services or information, including, but not limited to, reference designs and materials relating to evaluation modules, (collectively, "TI Resources") are intended to assist designers who are developing applications that incorporate TI products; by downloading, accessing or using any particular TI Resource in any way, you (individually or, if you are acting on behalf of a company, your company) agree to use it solely for this purpose and subject to the terms of this Notice.

TI's provision of TI Resources does not expand or otherwise alter TI's applicable published warranties or warranty disclaimers for TI products, and no additional obligations or liabilities arise from TI providing such TI Resources. TI reserves the right to make corrections, enhancements, improvements and other changes to its TI Resources.

You understand and agree that you remain responsible for using your independent analysis, evaluation and judgment in designing your applications and that you have full and exclusive responsibility to assure the safety of your applications and compliance of your applications (and of all TI products used in or for your applications) with all applicable regulations, laws and other applicable requirements. You represent that, with respect to your applications, you have all the necessary expertise to create and implement safeguards that (1) anticipate dangerous consequences of failures, (2) monitor failures and their consequences, and (3) lessen the likelihood of failures that might cause harm and take appropriate actions. You agree that prior to using or distributing any applications that include TI products, you will thoroughly test such applications and the functionality of such TI products as used in such applications. TI has not conducted any testing other than that specifically described in the published documentation for a particular TI Resource.

You are authorized to use, copy and modify any individual TI Resource only in connection with the development of applications that include the TI product(s) identified in such TI Resource. NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT OF TI OR ANY THIRD PARTY IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information regarding or referencing third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of TI Resources may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI RESOURCES ARE PROVIDED "AS IS" AND WITH ALL FAULTS. TI DISCLAIMS ALL OTHER WARRANTIES OR REPRESENTATIONS, EXPRESS OR IMPLIED, REGARDING TI RESOURCES OR USE THEREOF, INCLUDING BUT NOT LIMITED TO ACCURACY OR COMPLETENESS, TITLE, ANY EPIDEMIC FAILURE WARRANTY AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY YOU AGAINST ANY CLAIM, INCLUDING BUT NOT LIMITED TO ANY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON ANY COMBINATION OF PRODUCTS EVEN IF DESCRIBED IN TI RESOURCES OR OTHERWISE. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, DIRECT, SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF TI RESOURCES OR USE THEREOF, AND REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

You agree to fully indemnify TI and its representatives against any damages, costs, losses, and/or liabilities arising out of your non-compliance with the terms and provisions of this Notice.

This Notice applies to TI Resources. Additional terms apply to the use and purchase of certain types of materials, TI products and services. These include; without limitation, TI's standard terms for semiconductor products (<http://www.ti.com/sc/docs/stdterms.htm>), [evaluation modules](#), and [samples](http://www.ti.com/sc/docs/sampterm.htm) (<http://www.ti.com/sc/docs/sampterm.htm>).

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2017, Texas Instruments Incorporated