


NOTES, UNLESS OTHERWISE SPECIFIED:

1. The netname "DMD_P3P3V" represents connection to the +3.3V digital power plane.
2. The symbol  represents connection to the digital ground plane.
3. A "Z" suffix on a signal name indicates an active low signal.
4. All components with designators "U", "D", "Y" and "Q" are electrostatic discharge sensitive.
5. All resistor values are in ohms, 1/16W and 5% unless otherwise specified.
6. 470 Ohm resistors R1, R2, R3, R4, R6, and R9 protect the system when/If wrong DMD is installed on-board.

HIGHEST REFERENCE DESIGNATORS USED						
C111	D3	DN2	J2	L4	P1	R45
TP38	U1000	Q9				



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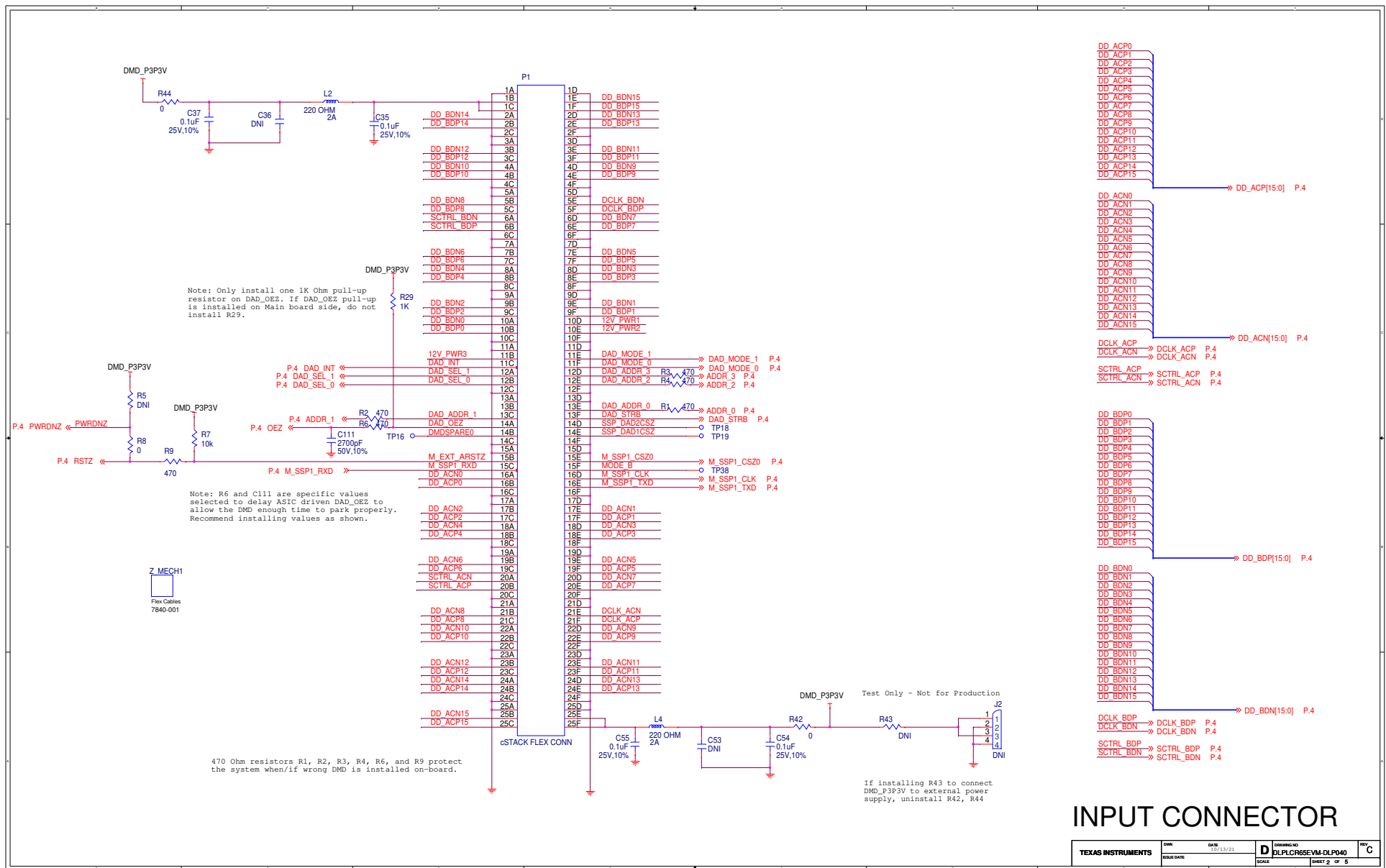
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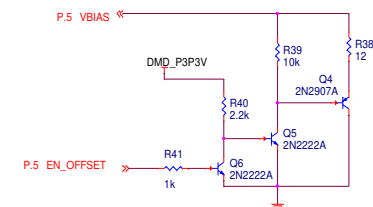
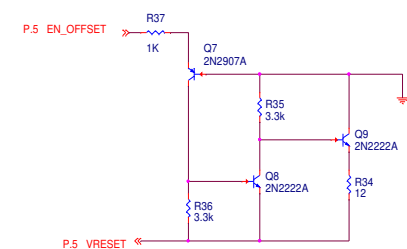
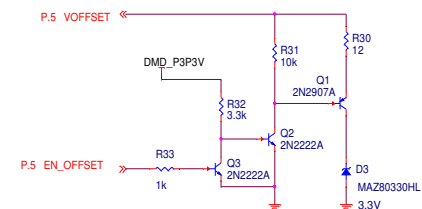
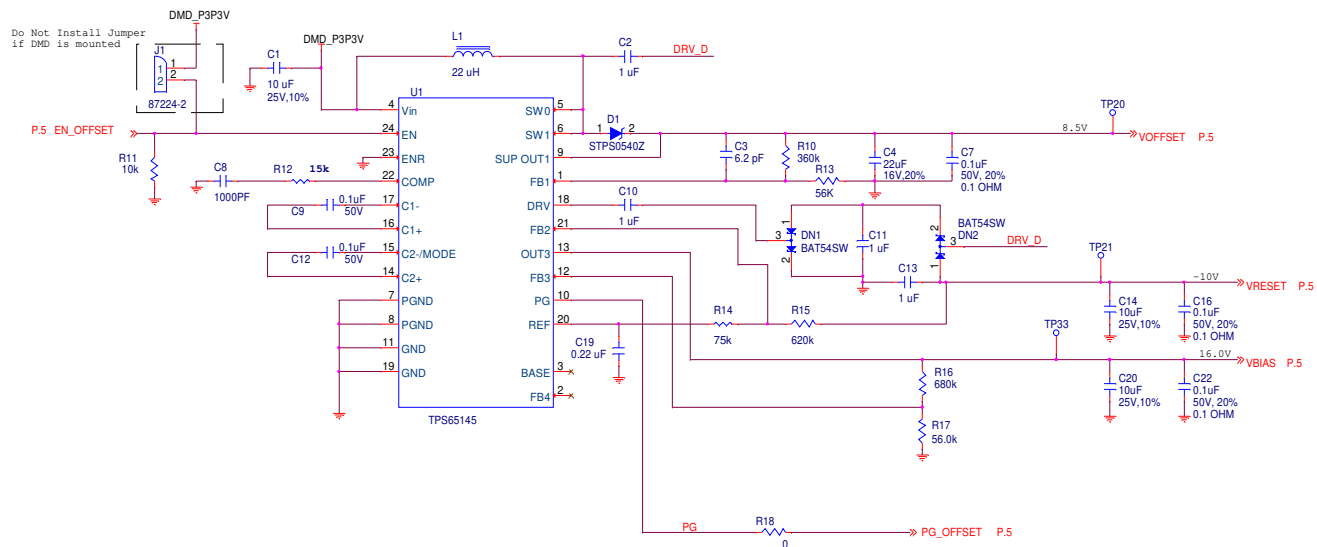
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REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	ECO 2117139: Initial Release	9/13/2011	
B	ECO 2123014: Added DAD_OEZ Delay Circuit Page 2 - R29 changed from 10K Ohm to 1K Ohm Page 2 - Added C111 2700pF	3/07/2012	
C	Changed U1000 from DLP6500FYE to DLP6500BFYE	10/13/2021	

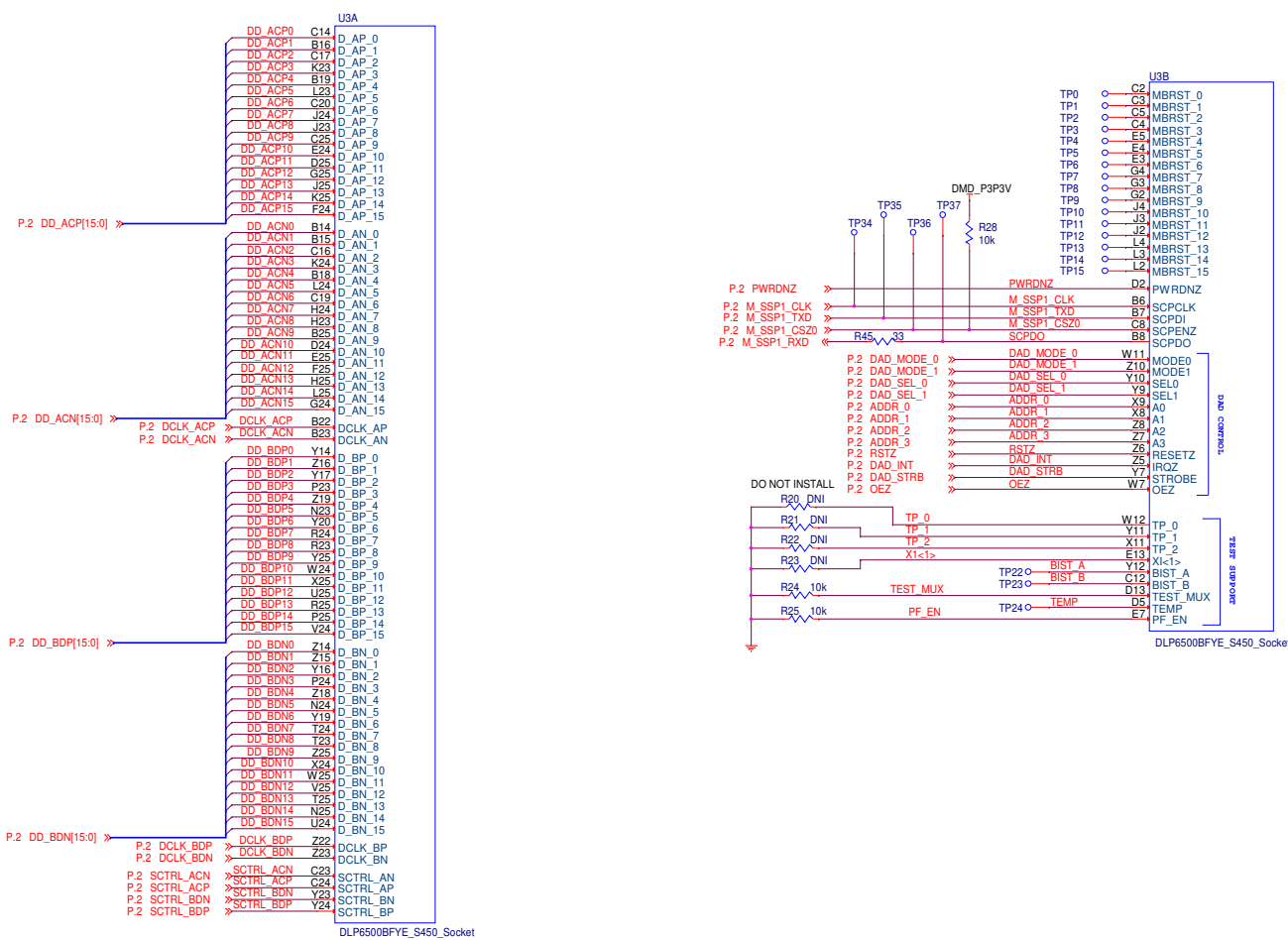
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		ENGR					
		APVD					
		MFG					
		QA			TITLE		
2511981	0314SS				DLP6500		
NEXT ASSY	USED ON				DRAWING NO		
					DLP040		
APPLICATION		SW	Calcedas 16.0		SCALE	SHEET 1 of 5	REV
							C





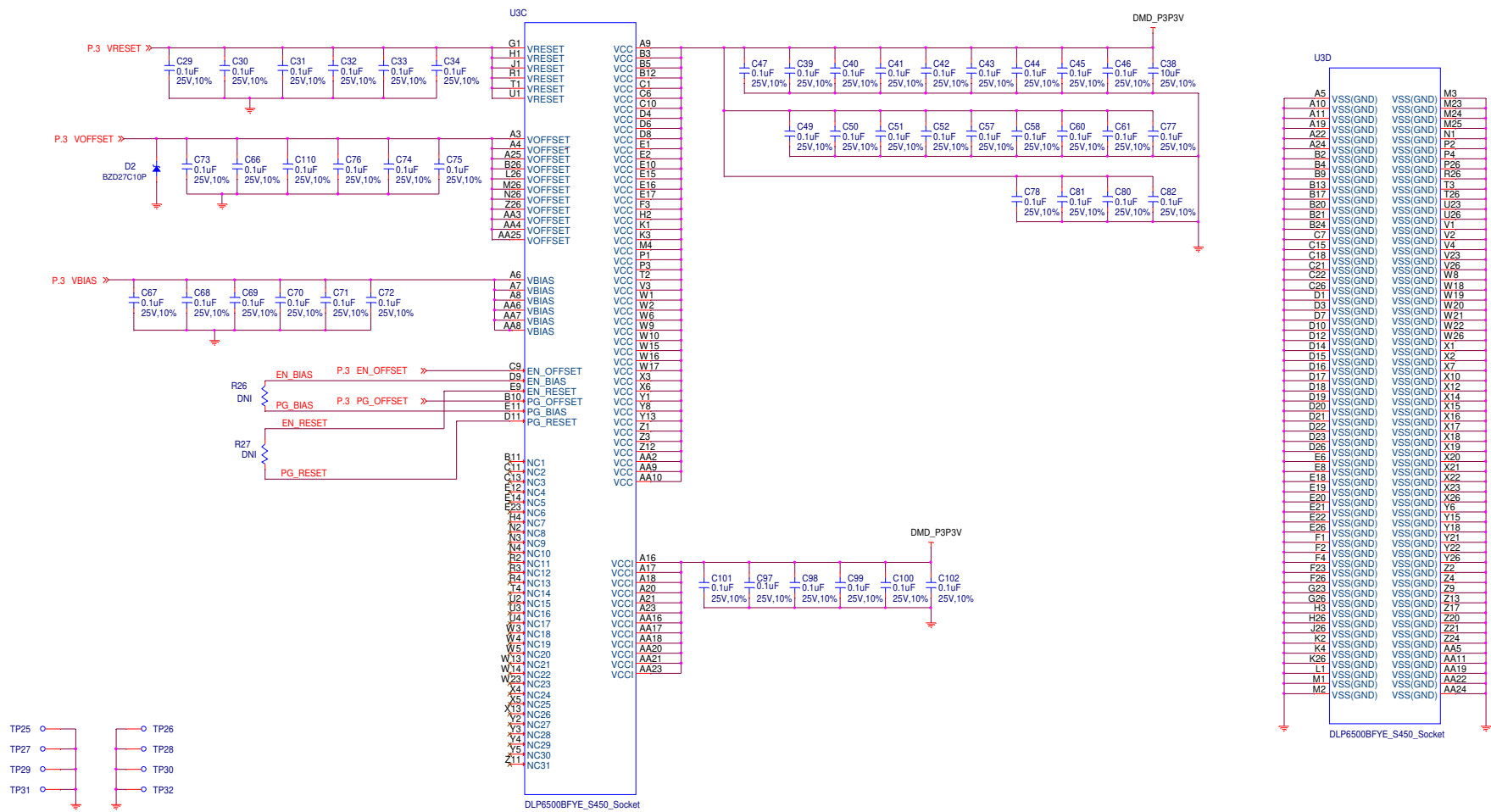
DMD POWER SUPPLY

TEXAS INSTRUMENTS	OWN	DATE	DRAWING NO	REV
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			2044	Sheet 2 of 6



DMD DATA INPUT

TEXAS INSTRUMENTS	OWN	DATE	ISSUE NO	REV
	ISSUE DATE	20/13/21	DLP6500BFEVM-DLP040	C



DMD POWER

TEXAS INSTRUMENTS	OWN	DATE	D	DRAWING NO	REV
	ISSUE DATE	25/13/25			
				DLPLCR65EVM-DLP640	C

Sheet 2 of 5