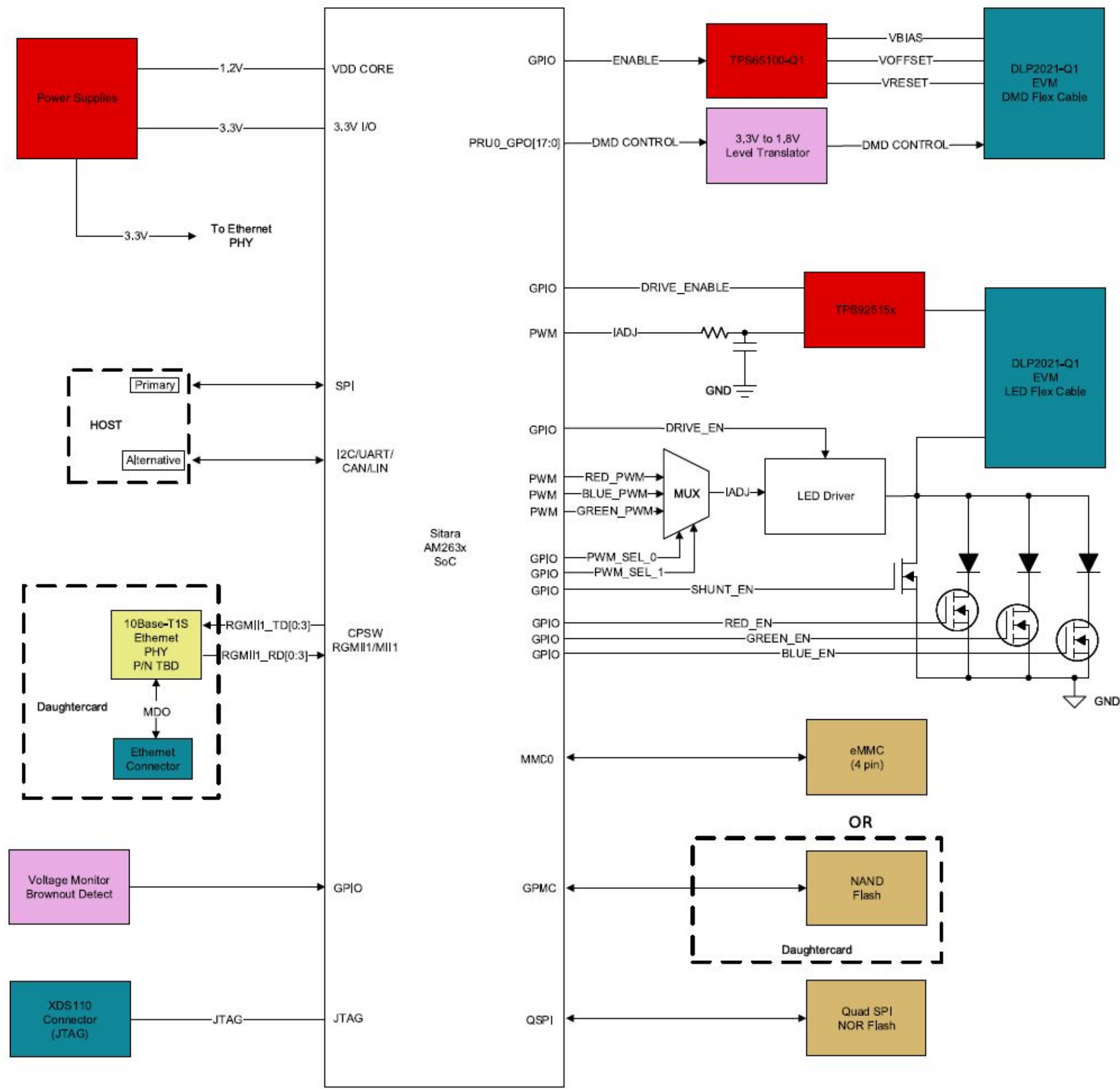


A

B

C

D



A

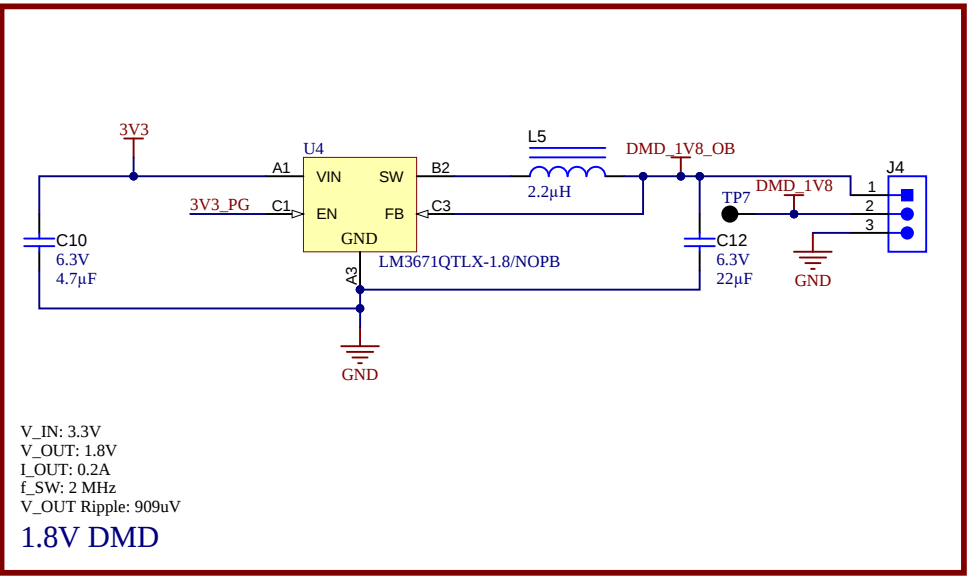
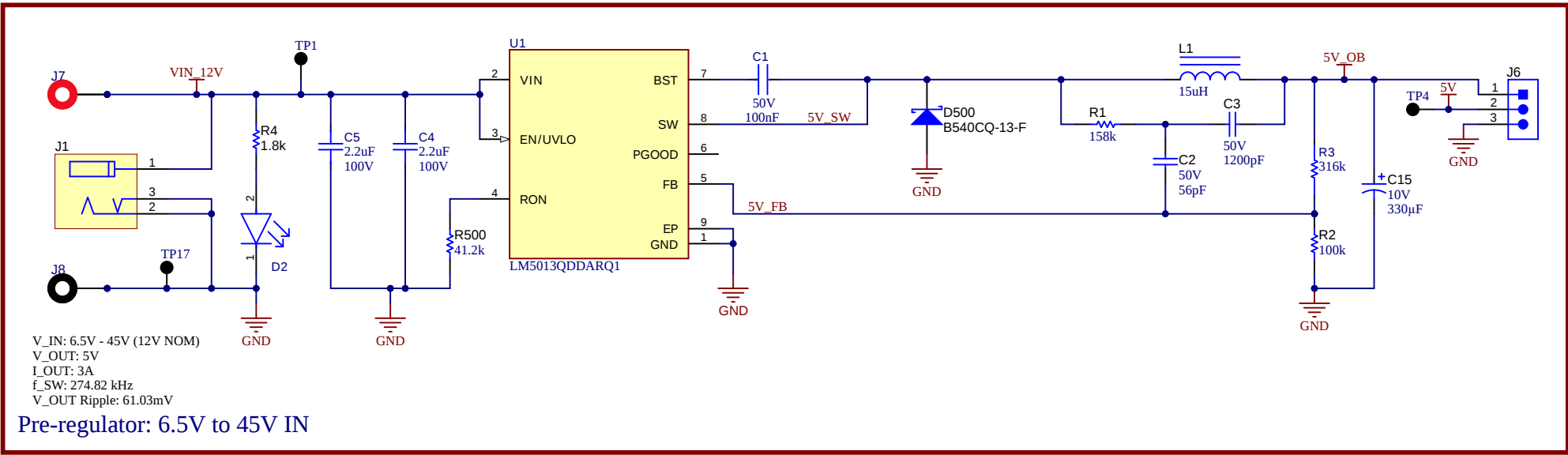
B

C

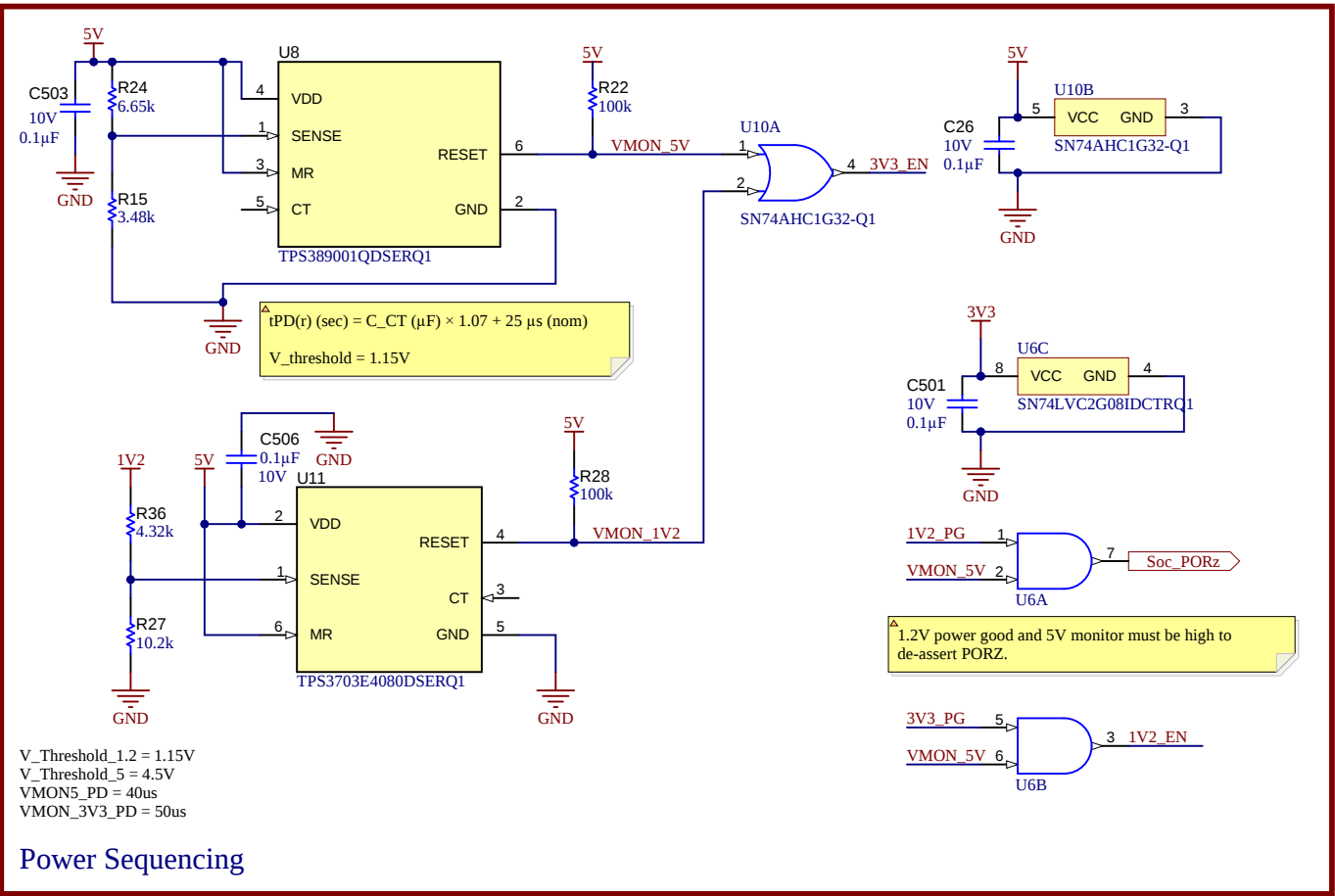
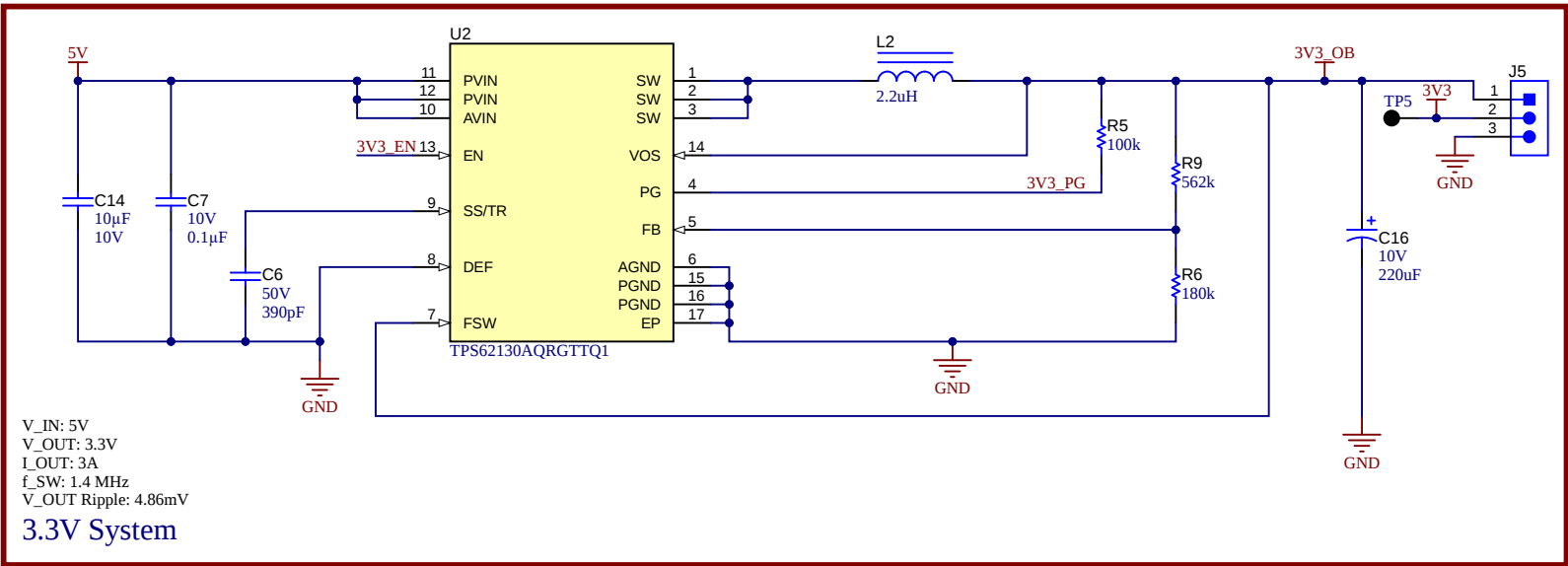
D

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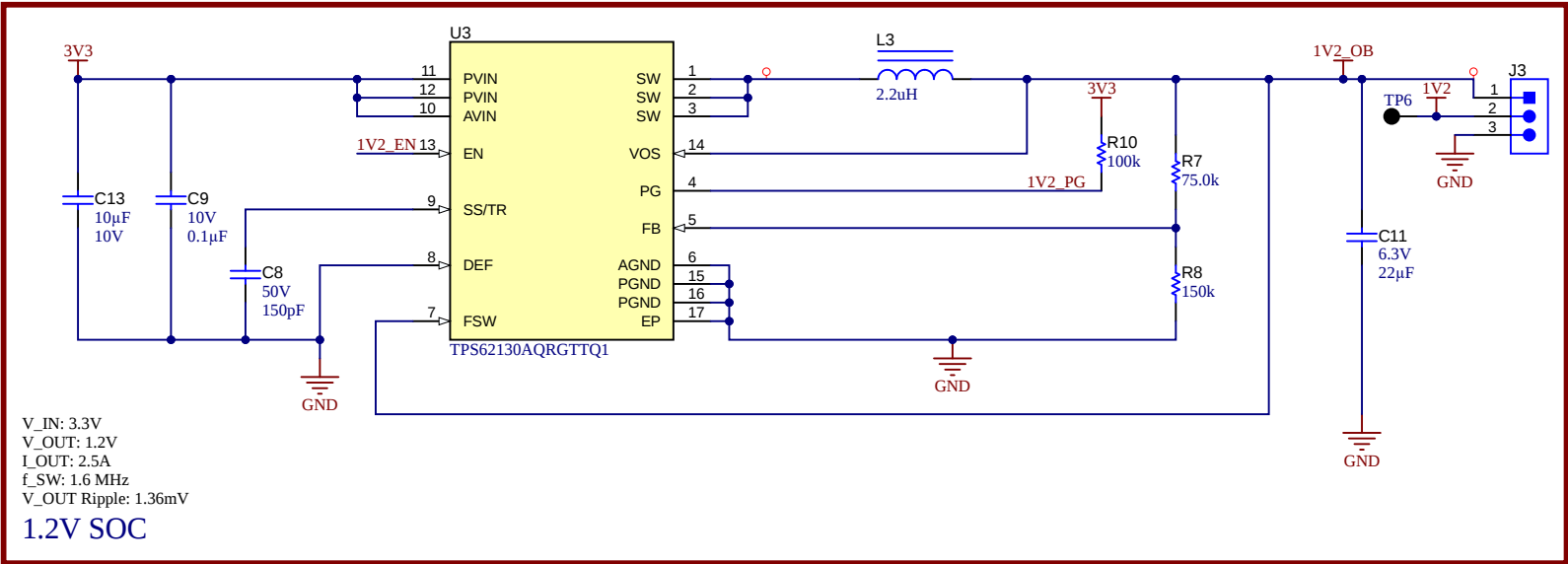
A



B



C



D

Orderable: DLP2021AM263Q1EVM	Designed for: Public Release	Mod. Date: 8/10/2026
TID #: N/A	Project Title: DLP2021AM263Q1EVM	
Number: DLP096	Rev: D	Sheet Title: System_Power
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 10
Drawn By: A. Whitehead	File: DLP096D_System_Power.SchDoc	Size: B
Engineer: A. Whitehead	Contact: http://www.ti.com/support	

A

B

C

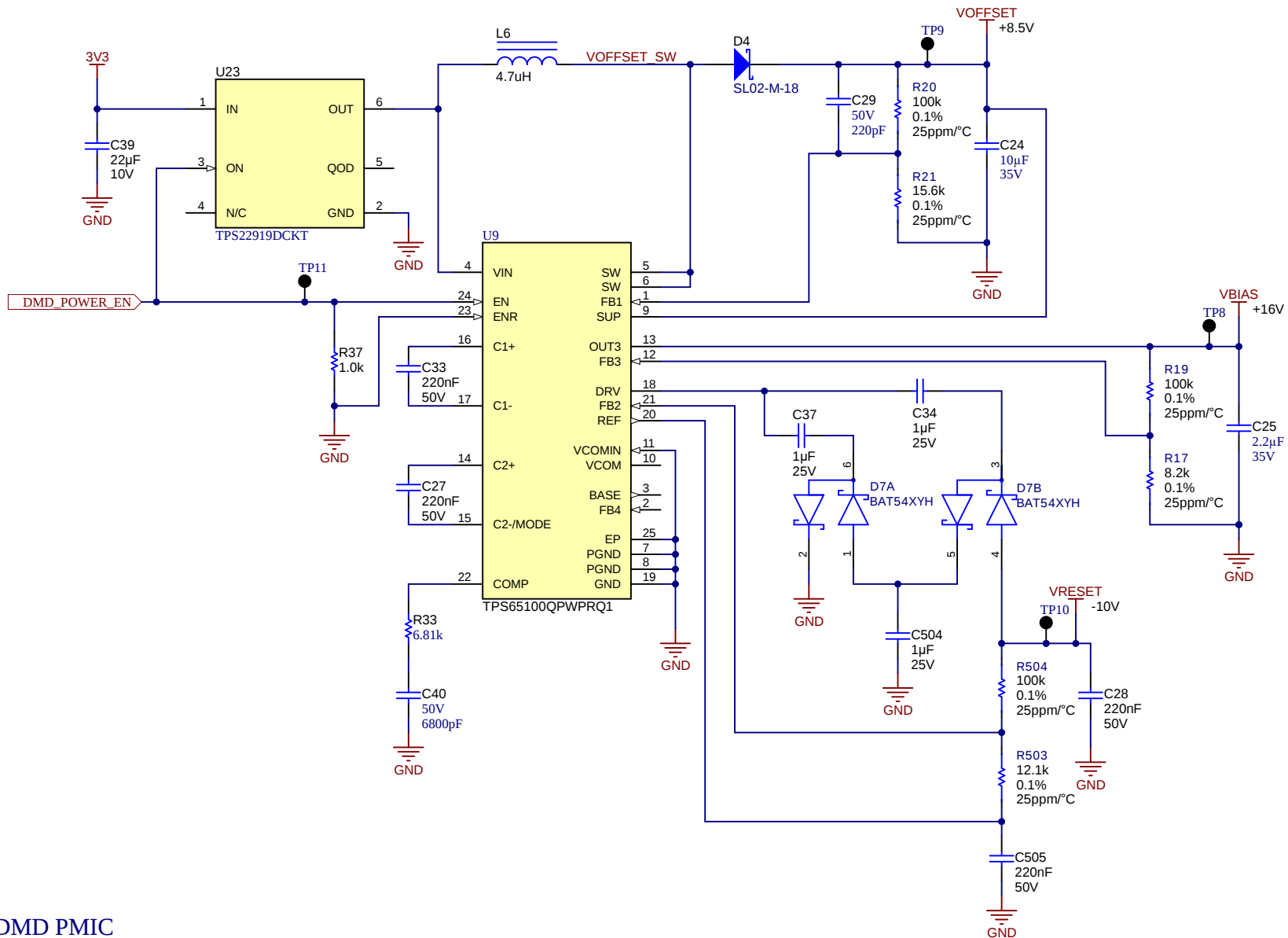
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A

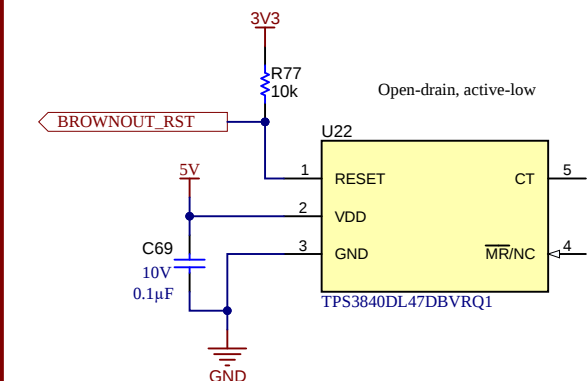
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C

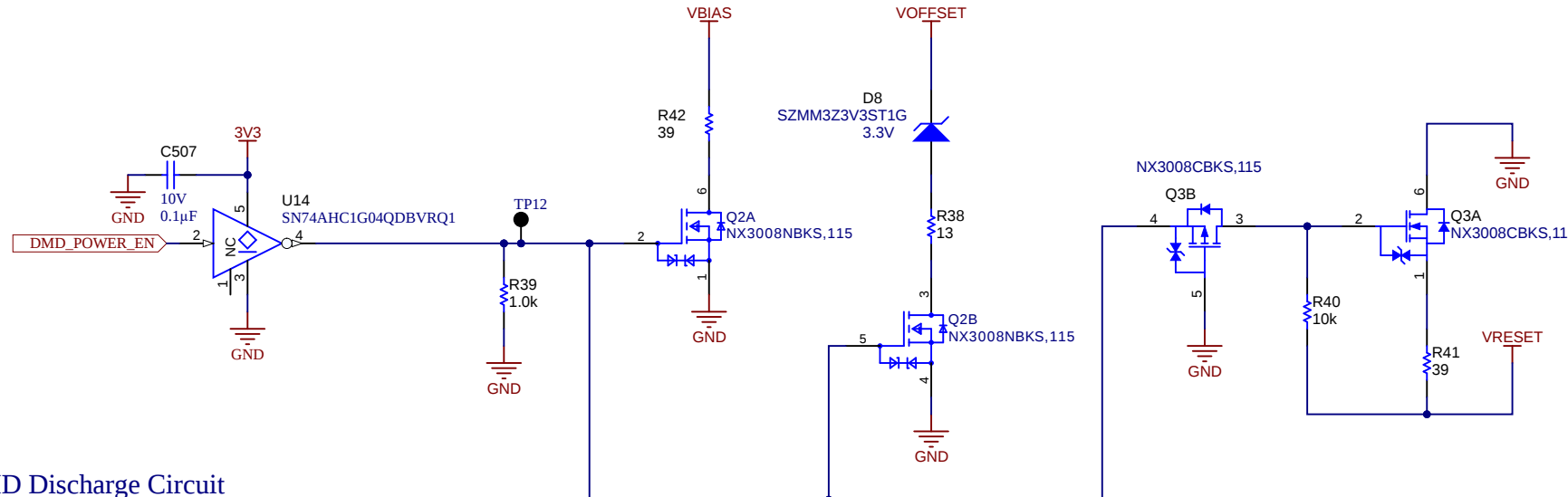
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DMD PMIC



5V Supervisor for DMD Park

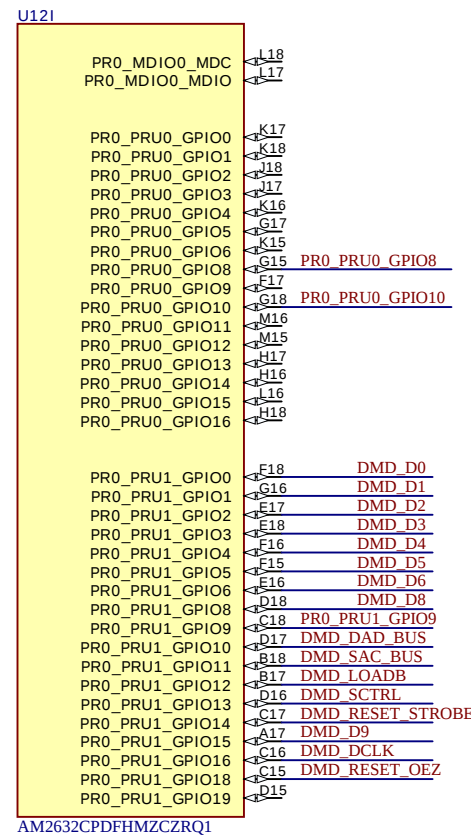
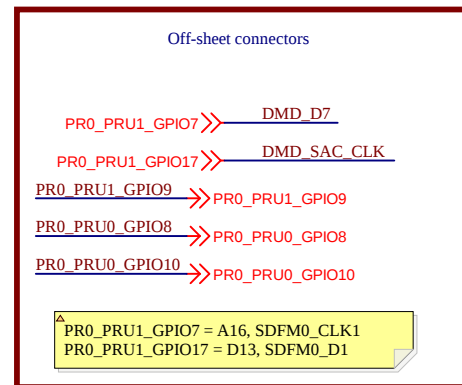


DMD Discharge Circuit

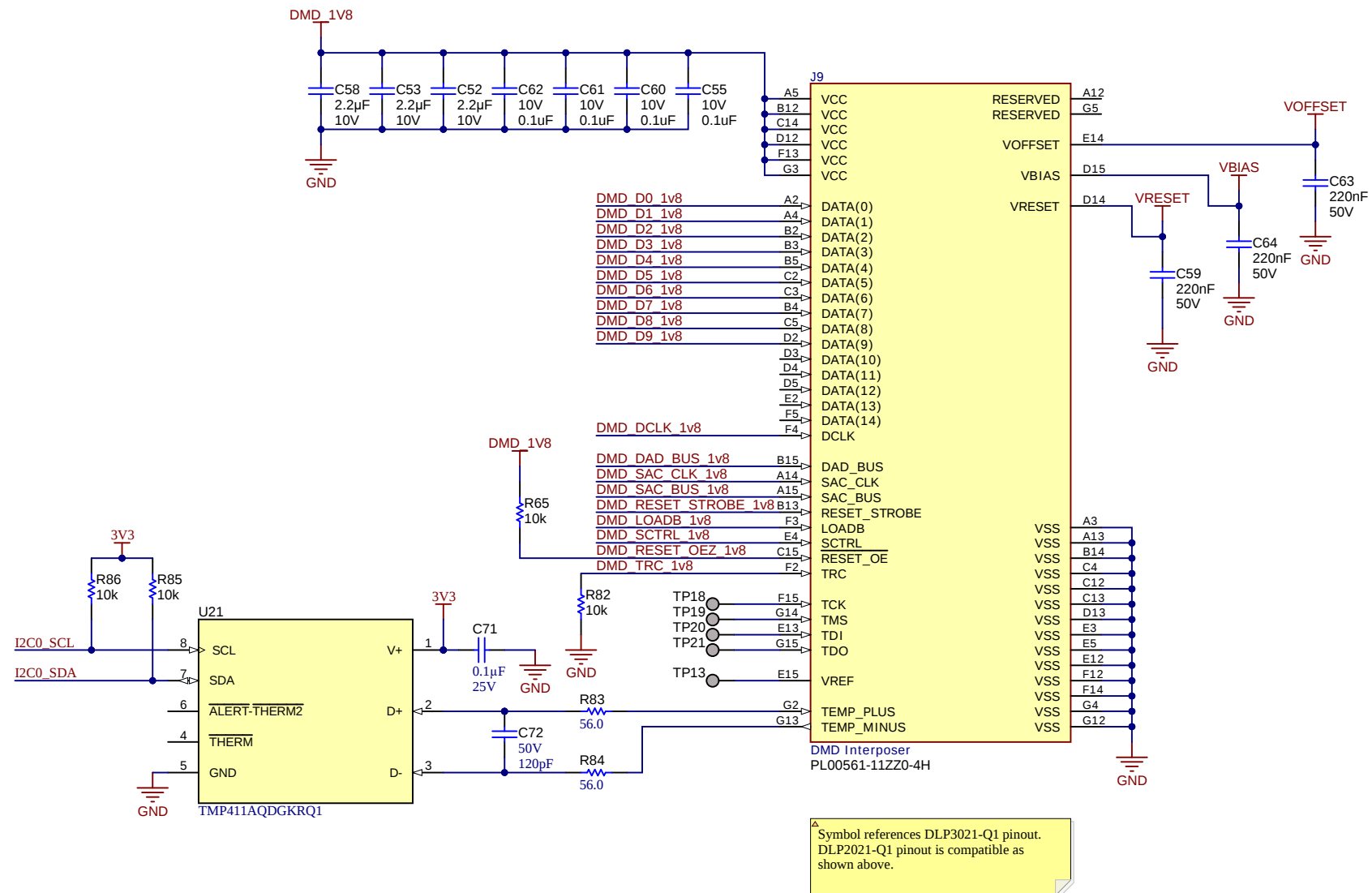
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Orderable: DLP096	Designed for: Public Release	Mod. Date: 8/10/2026
TID #: N/A	Project Title: DLP096D_DMD_Power.SchDoc	
Number: DLP096	Rev: D	Sheet Title: DMD Power
SVN Rev: Not in version control	Assembly Variant: 001	Sheet 5 of 10
Drawn By: A. Whitehead	File: DLP096D_DMD_Power.SchDoc	Size: B
Engineer: A. Whitehead	Contact: http://www.ti.com/support	

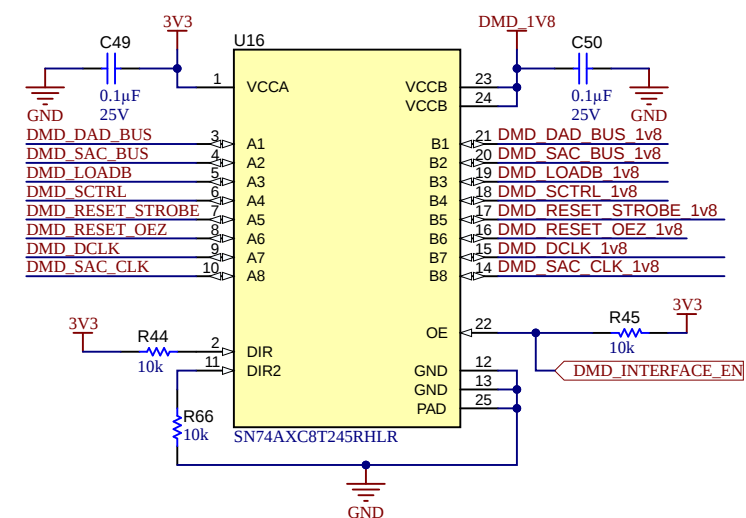
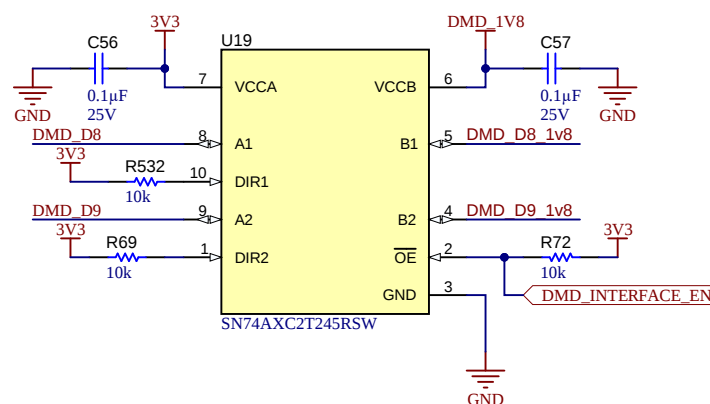
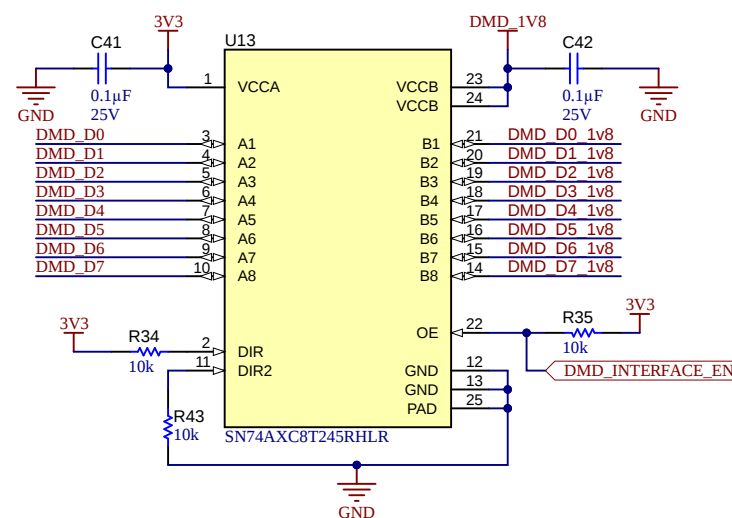




SQC PRU



DMD Interposer



PRU-to-DMD Level Shifters

A

B

C

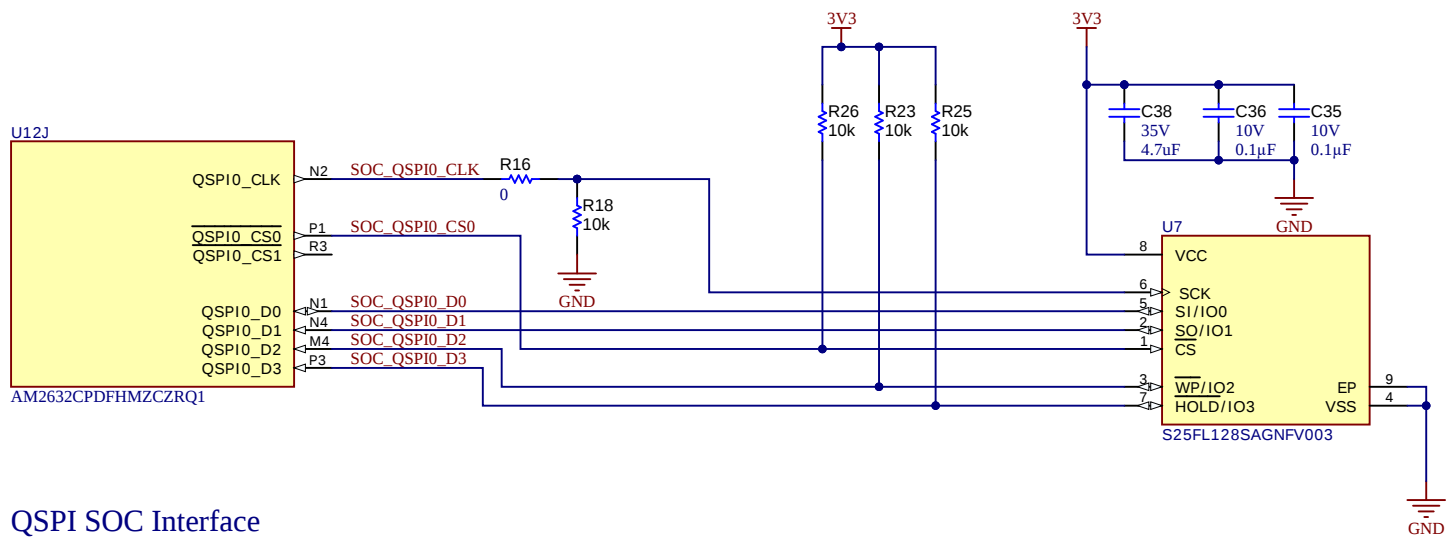
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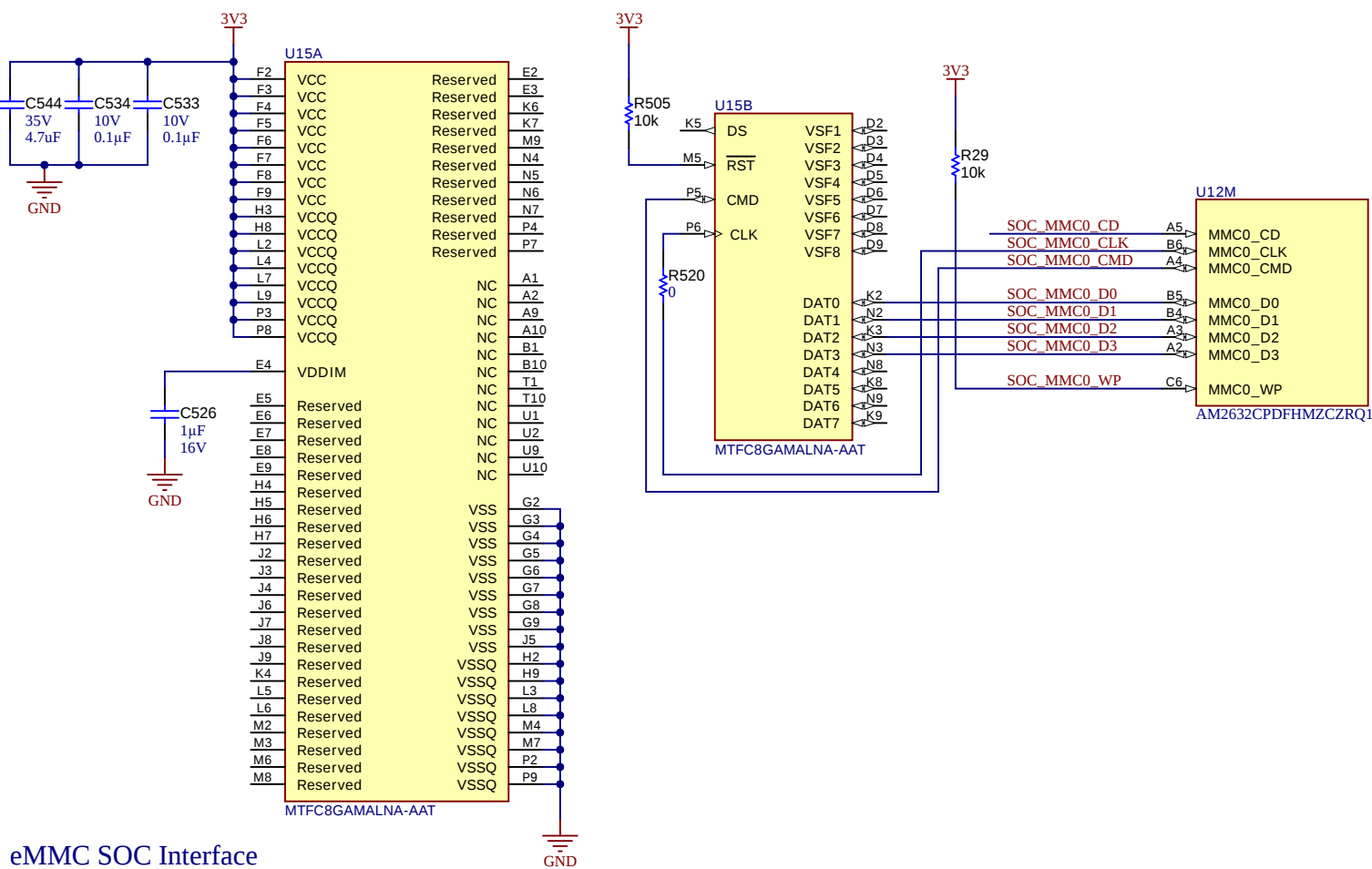
B

C

D



QSPI SOC Interface



eMMC SOC Interface

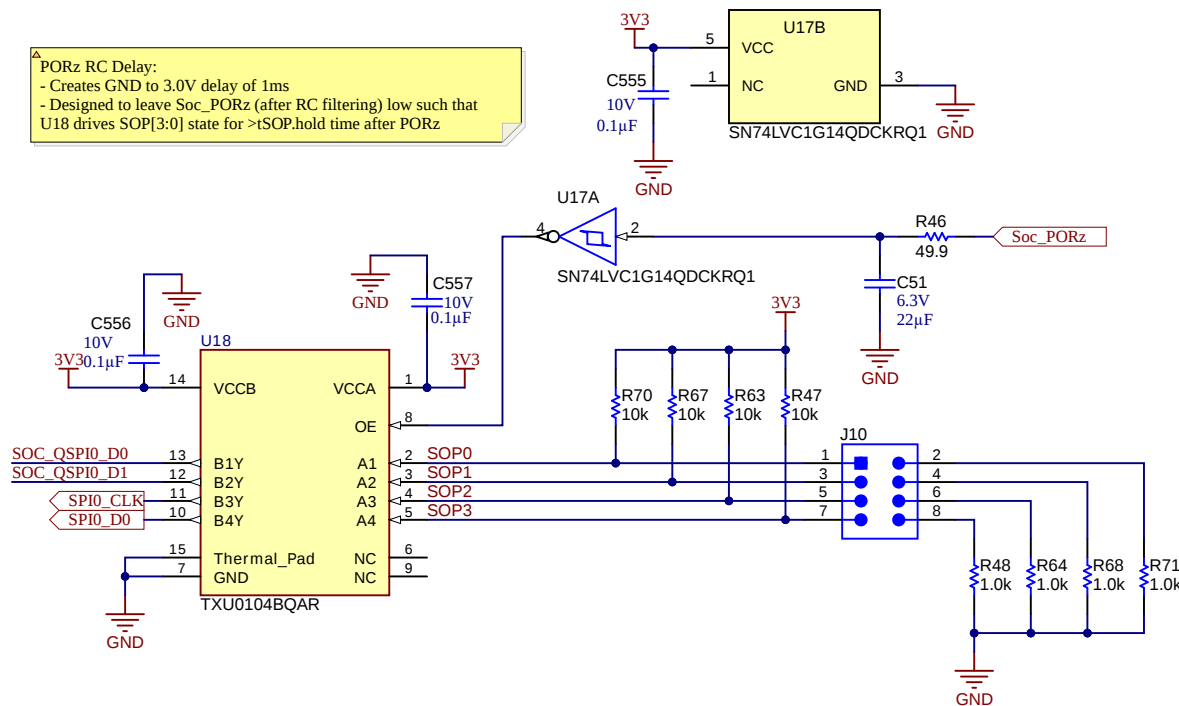


Table 5-2. BOOTMODE Pin Mapping

Boot Mode	SPI0_D0_pad (SOP3)	SPI0_CLK_pad (SOP2)	QSPI_D1 (SOP1)	QSPI_D0 (SOP0)
QSPI (4S) - Quad Read Mode	0	0	0	0
UART	0	0	0	1
QSPI (1S) - Single Read Mode	0	0	1	0
QSPI (4S) - Quad Read UART Fallback Mode	0	1	0	0
QSPI (1S) - Single Read UART Fallback Mode	0	1	0	1
DevBoot	1	0	1	1
Unsupported Boot Mode	All other combinations not defined above			

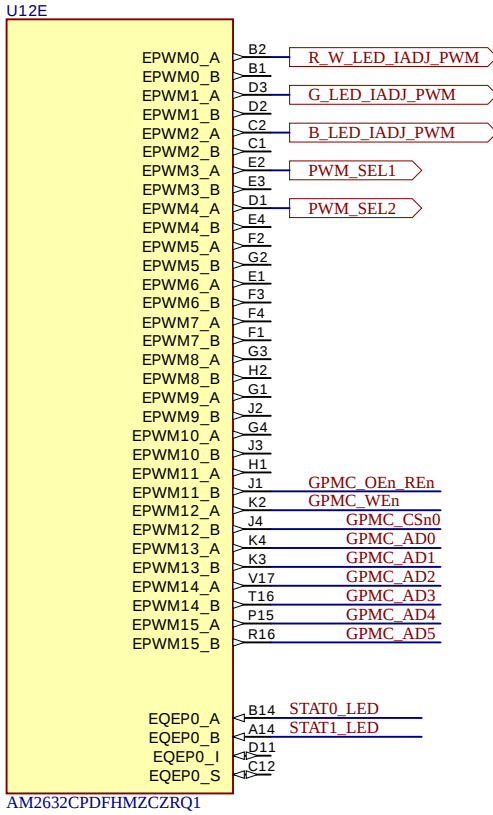
SOC BOOT MODE

A

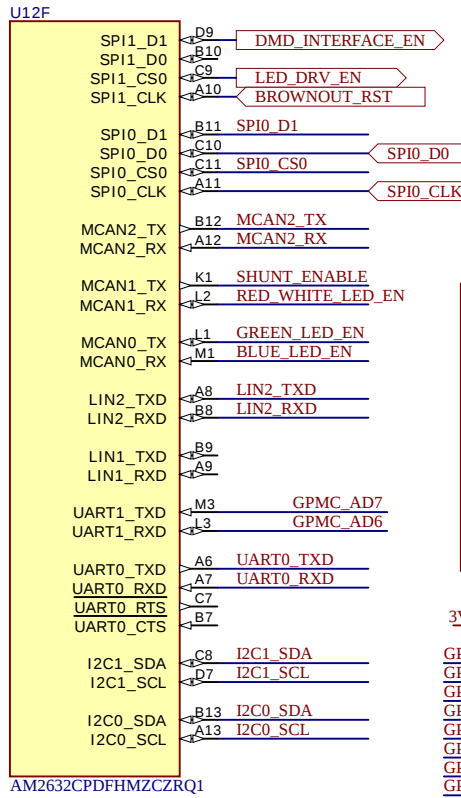
B

A

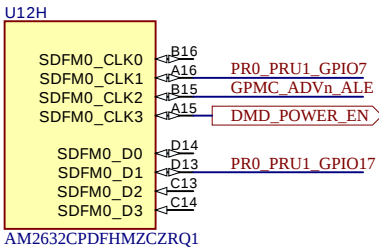
B



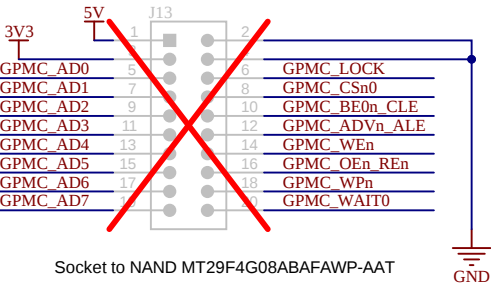
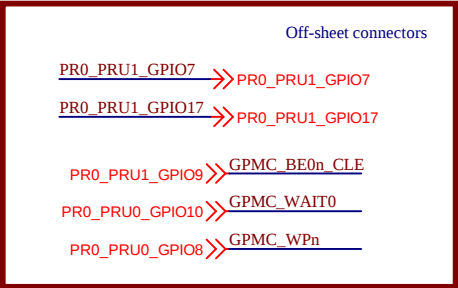
AM2632CPDFHMZCZRQ1



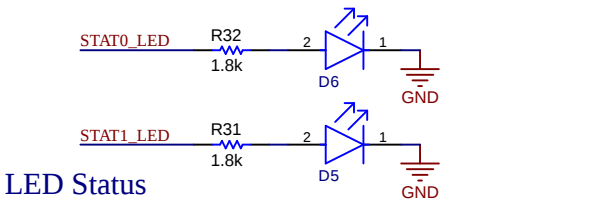
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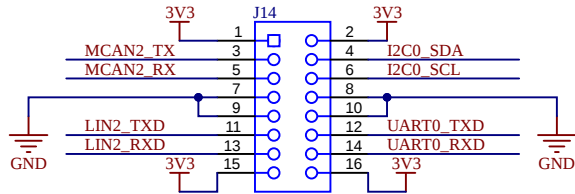
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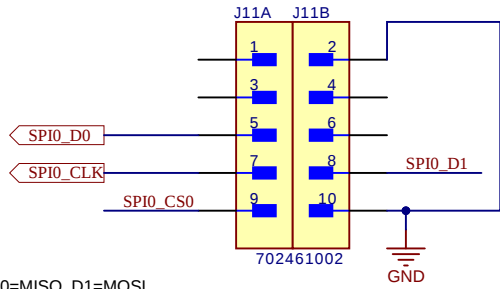
Raw ONFI NAND Daughtercard Socket



LED Status

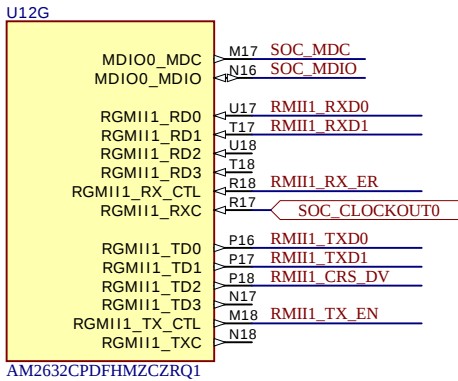


CAN / LIN / I2C / UART Daughtercard Socket



D0=MISO, D1=MOSI

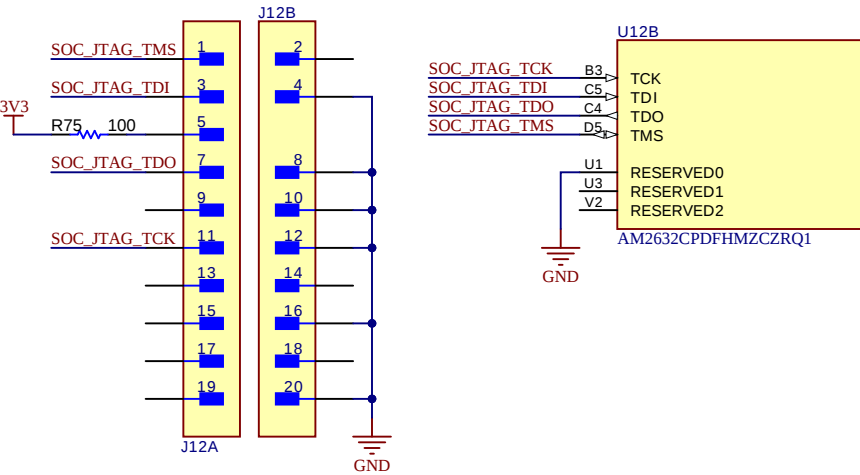
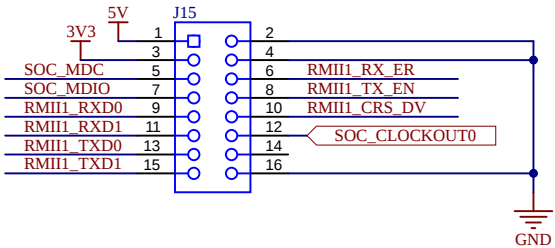
TotalPhase Cheetah USB-to-SPI Adapter



AM2632CPDFHMZCZRQ1

Socket for Ethernet RMII e.g. DP83TD510E (10BASE-T1L / 10BASE-T1S PHY shown only as example of AM263x expected connections)

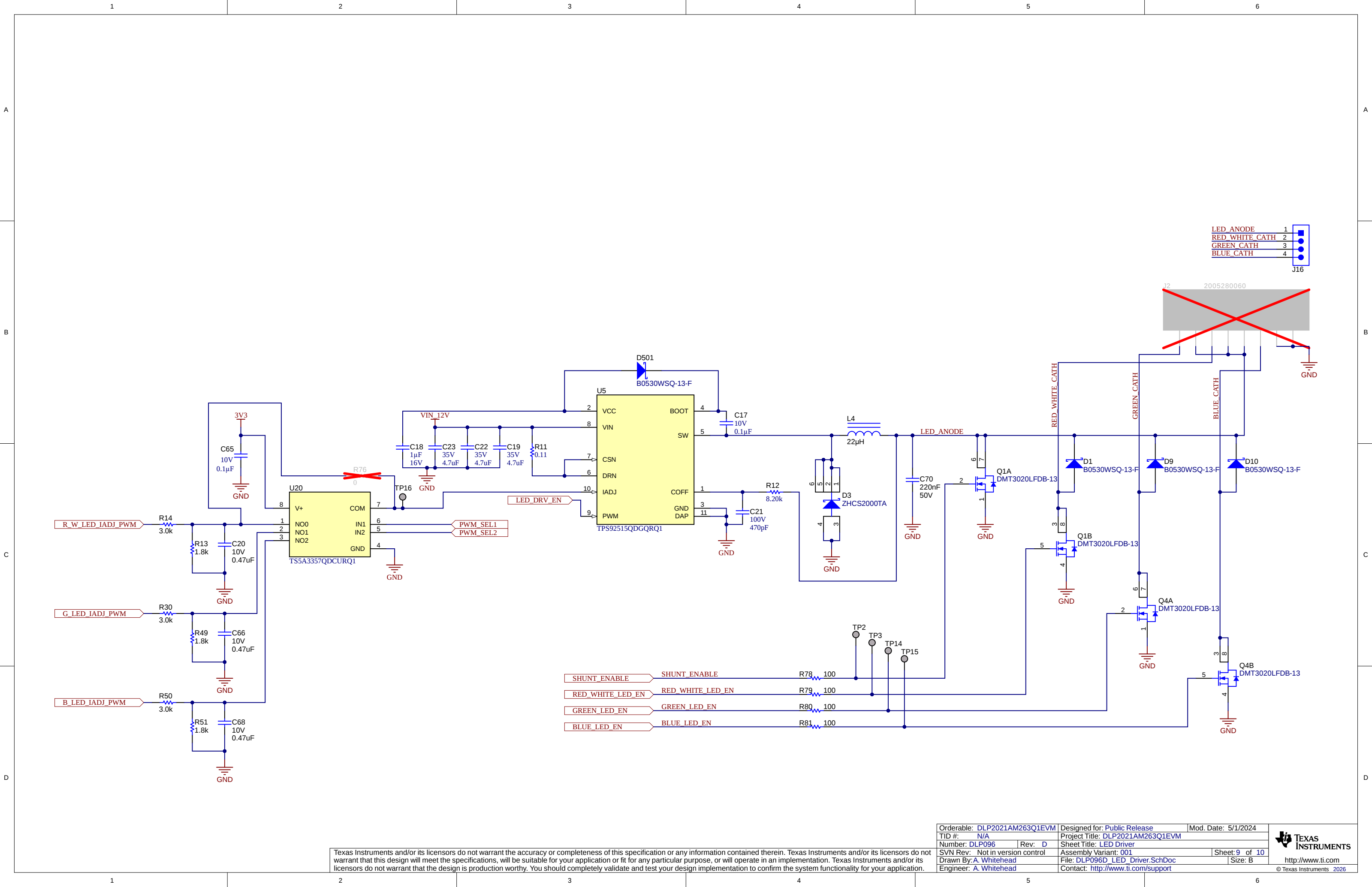
Ethernet RMII Daughtercard Socket



JTAG Programming / Debug

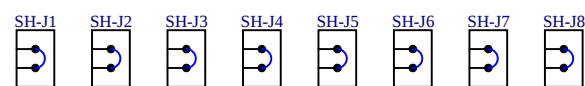
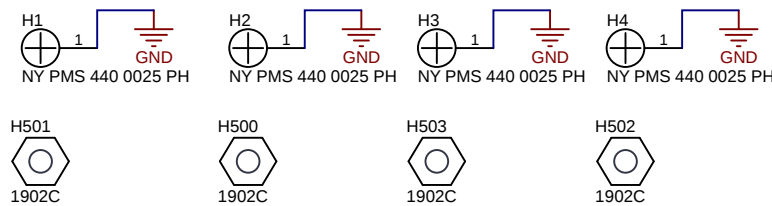
D

D



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Orderable: DLP2021AM263Q1EVM		Designed for: Public Release	Mod. Date: 5/1/2024
TID #: N/A		Project Title: DLP2021AM263Q1EVM	
Number: DLP096	Rev: D	Sheet Title: LED Driver	
SVN Rev: Not in version control		Assembly Variant: 001	
Drawn By: A. Whitehead		File: DLP096D_LED_Driver.SchDoc	Sheet: 9 of 10
Engineer: A. Whitehead		Contact: http://www.ti.com/support	Size: B



PCB Number: DLP096
PCB Rev: D

PCB
LOGO
Texas Instruments



PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo

[illegible]

LBL1

PCB Label

THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ5

Assembly Note

Indication provided for the D* components at their cathode side