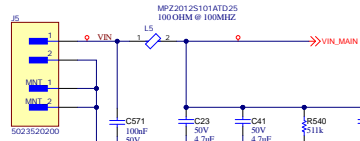
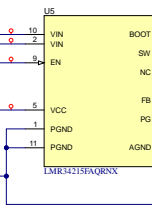


INPUT VOLTAGE FROM DRIVER

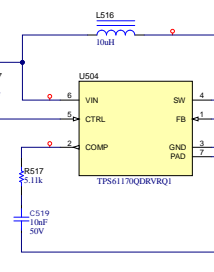


VIN_MAIN to TPS99000 to detect voltage dropping to park DMD. Assumes there is at least 400nF capacitance on driver board.

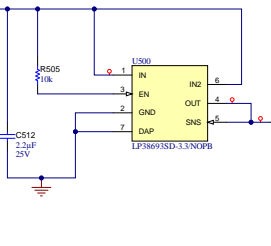
3.3V REGULATOR



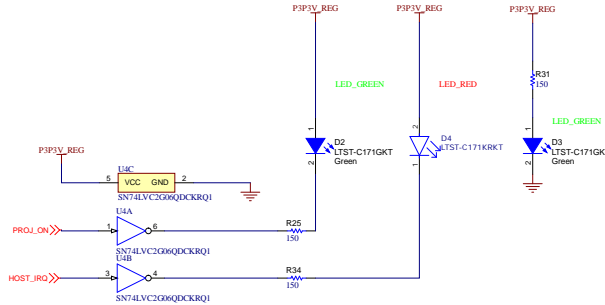
TPS99000 6.5V REGULATOR



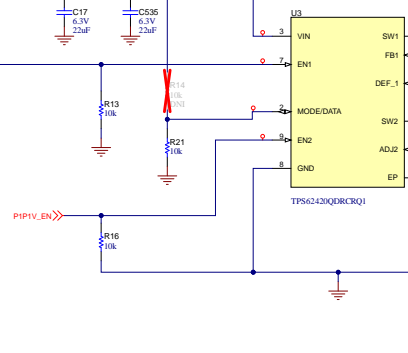
TPS99000 ANALOG 3.3V REGULATOR

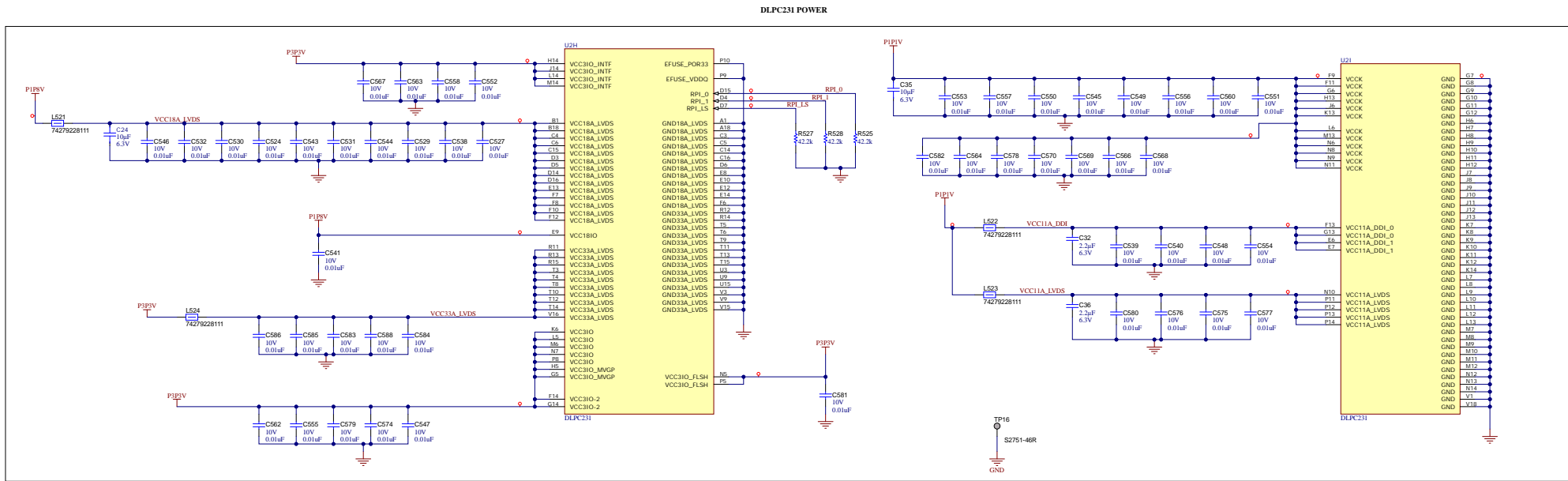


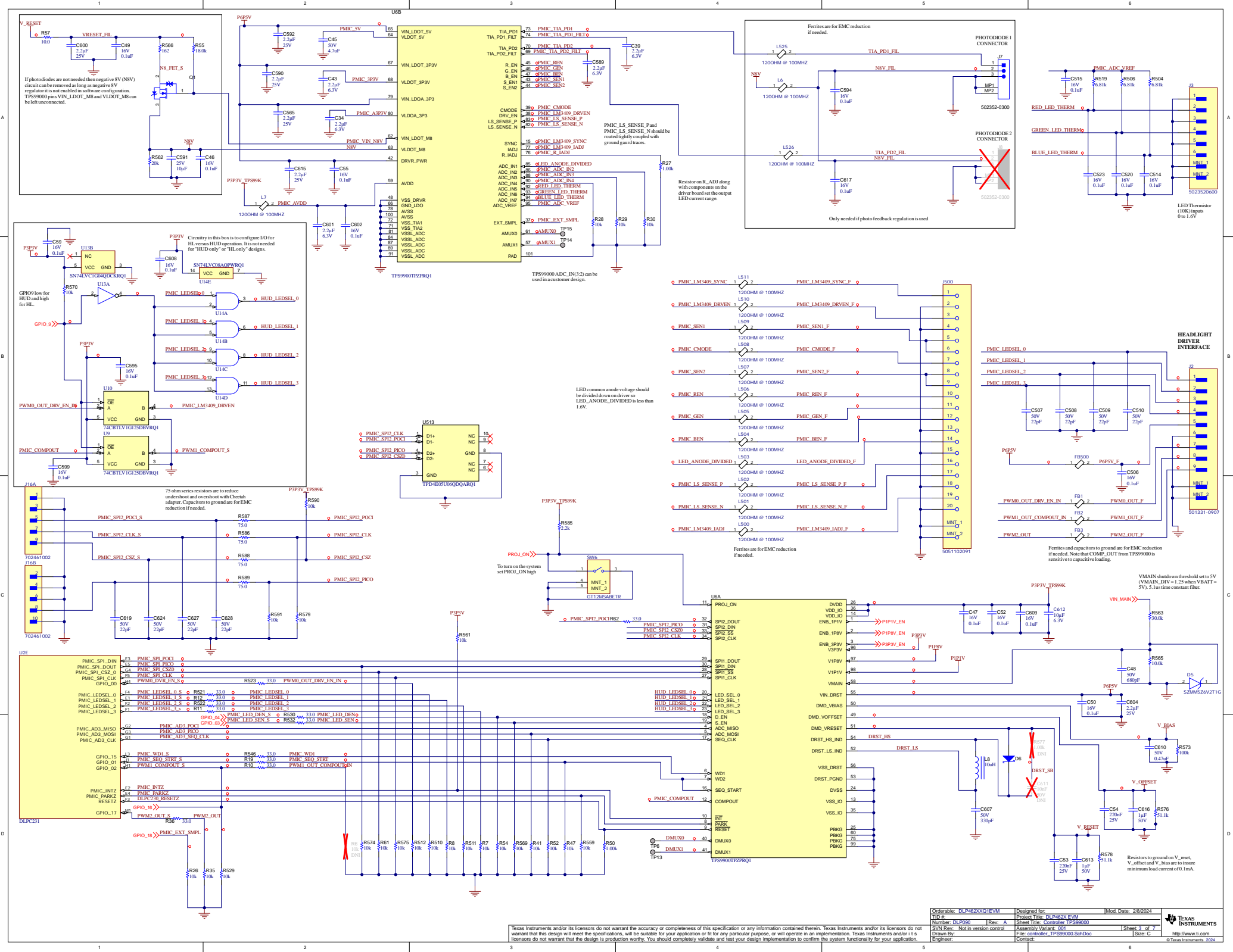
STATUS LEDS

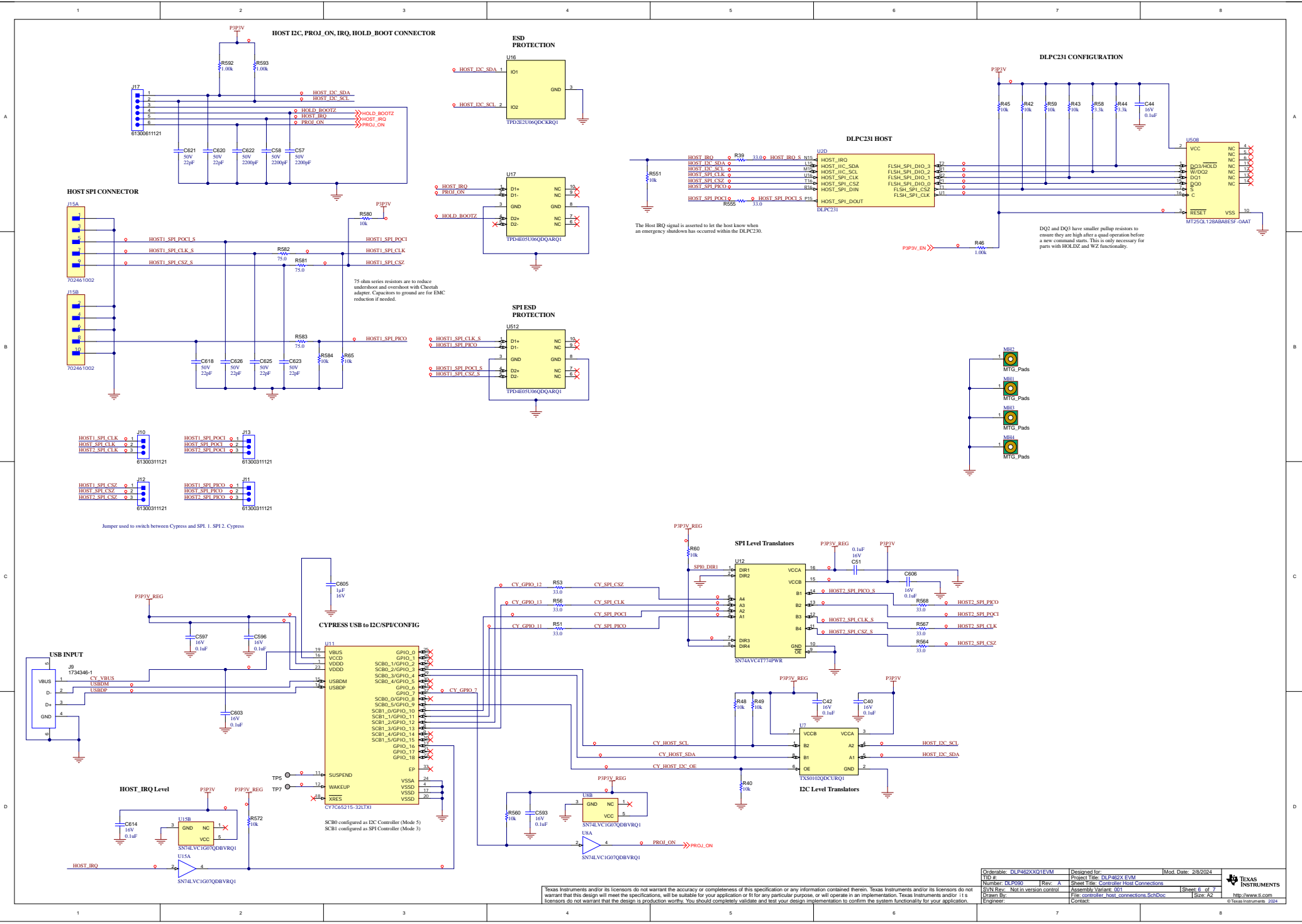


LSV and L1V REGULATORS











Assembly Note
Indication provided for the D* components at their cathode side

To optimize PCB routing the DLPC231 can optionally be configured to swap HS0 and HS1 buses and to reverse the bit order 7:0 <-> 0:7 for either or both buses. For this design the bit order is reversed on both buses, but HS0 and HS1 are not swarpped.

