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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/1/2023
TID #: N/A	Project Title: ADC36xxEVMCVAL	
Number: ADC36xxEVMCVAL	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 002	Sheet: 1 of 8
Drawn By:	File: ADC36xx-CVAL_Power_RevA.SchDoc	Size: B
Engineer: Miguel Lomeli	Contact: http://www.ti.com/support	

A

B

C

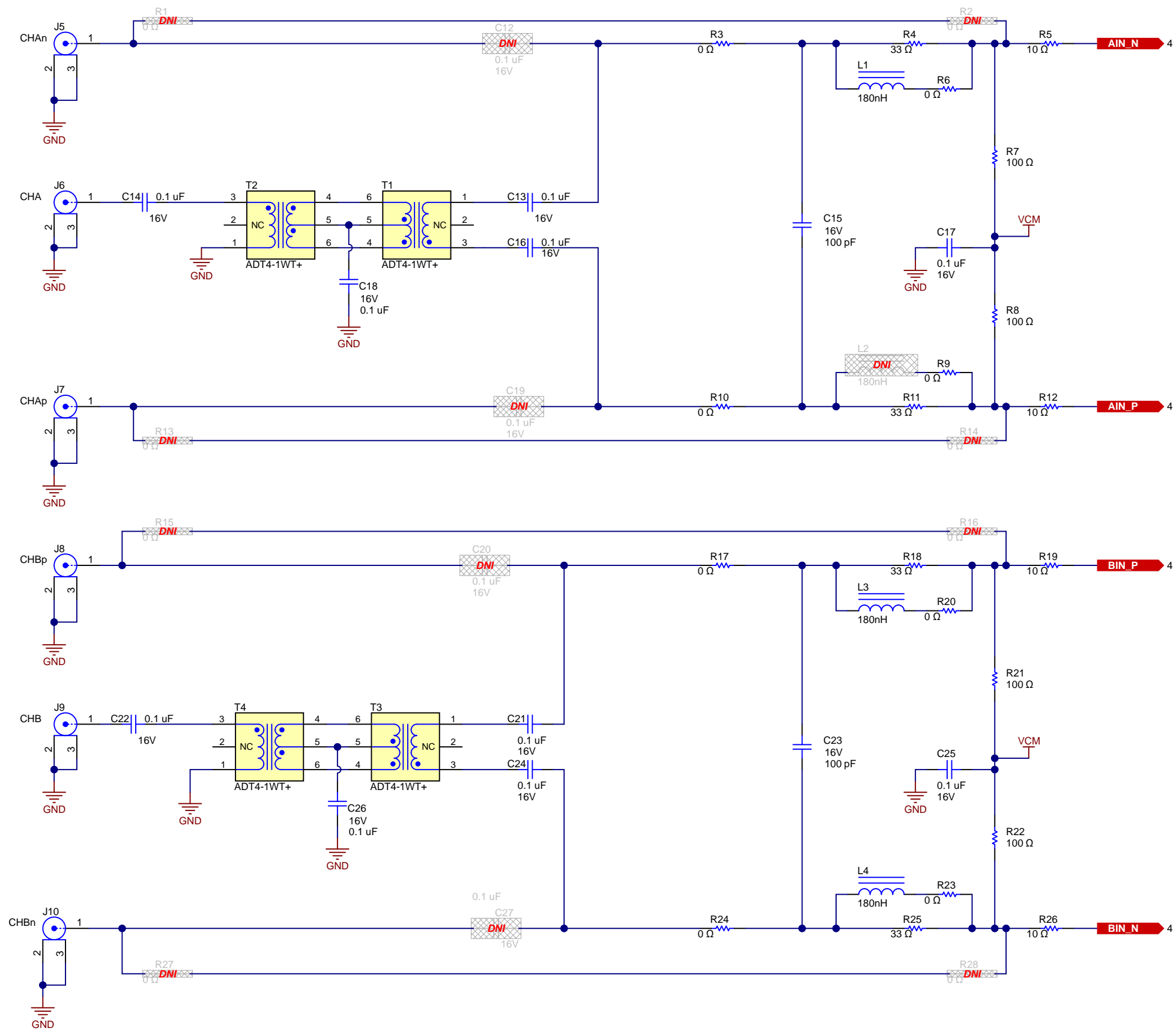
D

A

B


C

D



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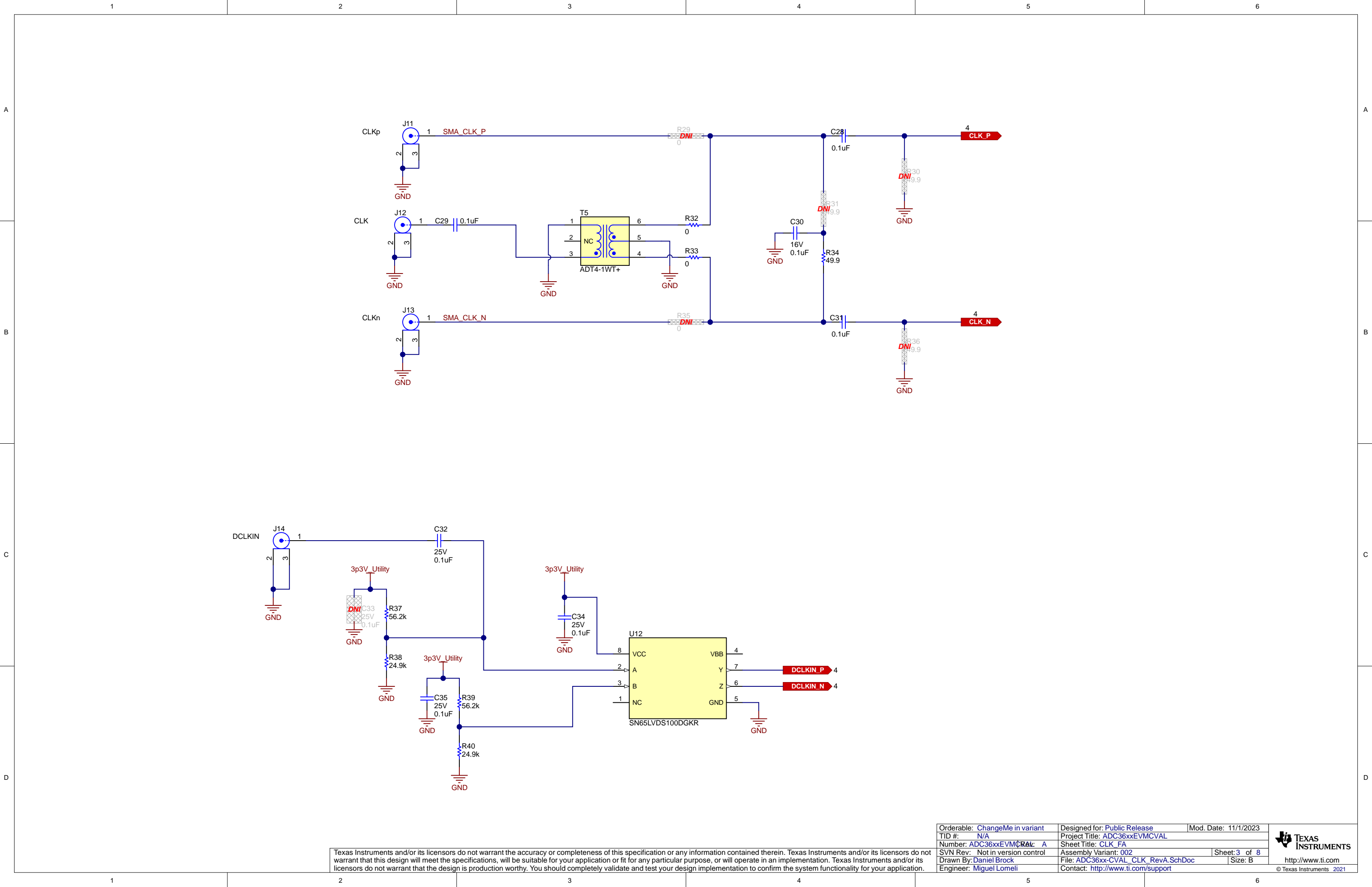
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TID #: N/A	Project Title: ADC36xxEVMCVAL	
Number: ADC36xxEVMCVAL	Sheet Title: AIN_Dual_FA	
SVN Rev: Not in version control	Assembly Variant: 002	Sheet: 2 of 8
Drawn By: Daniel Brock	File: ADC36xx-CVAL_AIN_Dual_RevA.SchDoc	Size: B
Engineer: Miguel Lomeli	Contact: http://www.ti.com/support	



TEXAS
INSTRUMENTS

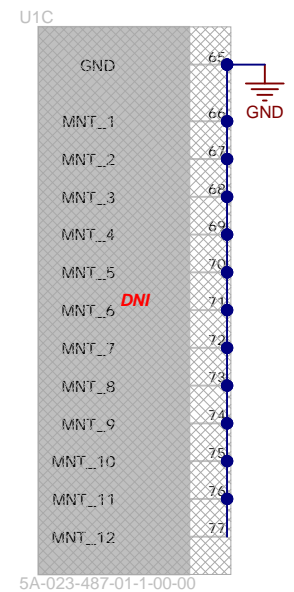
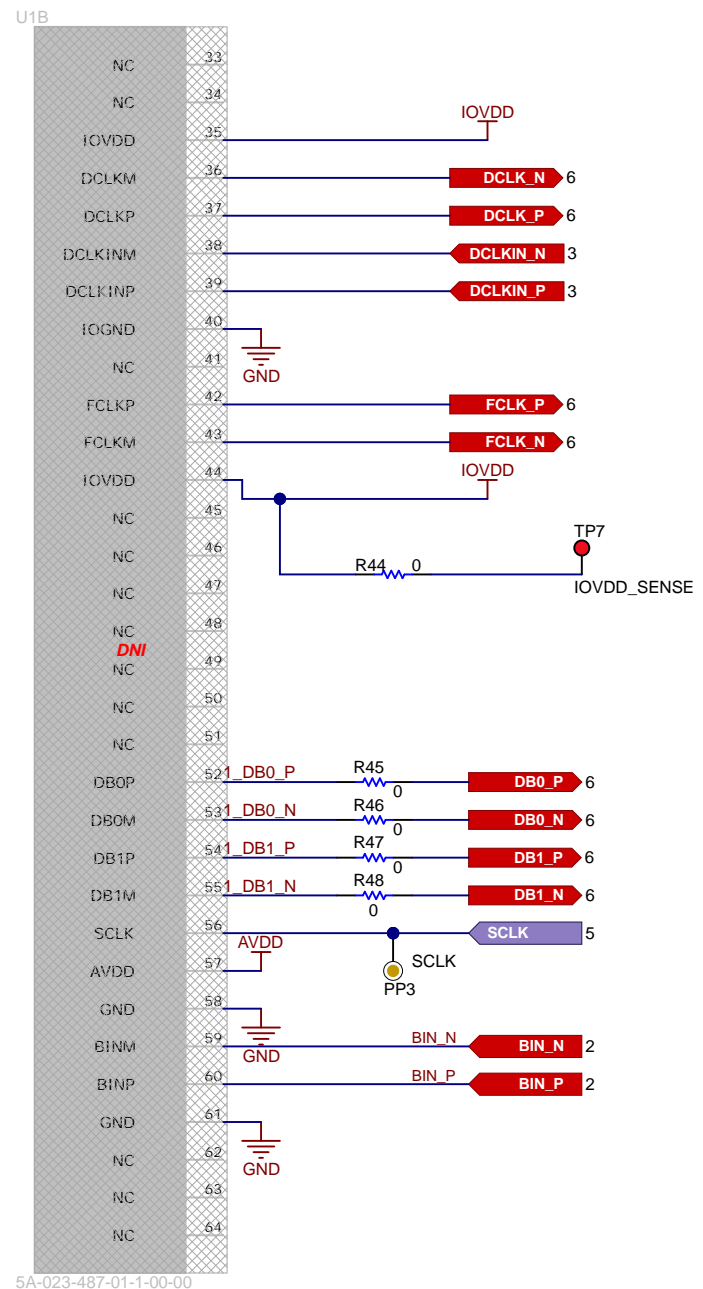
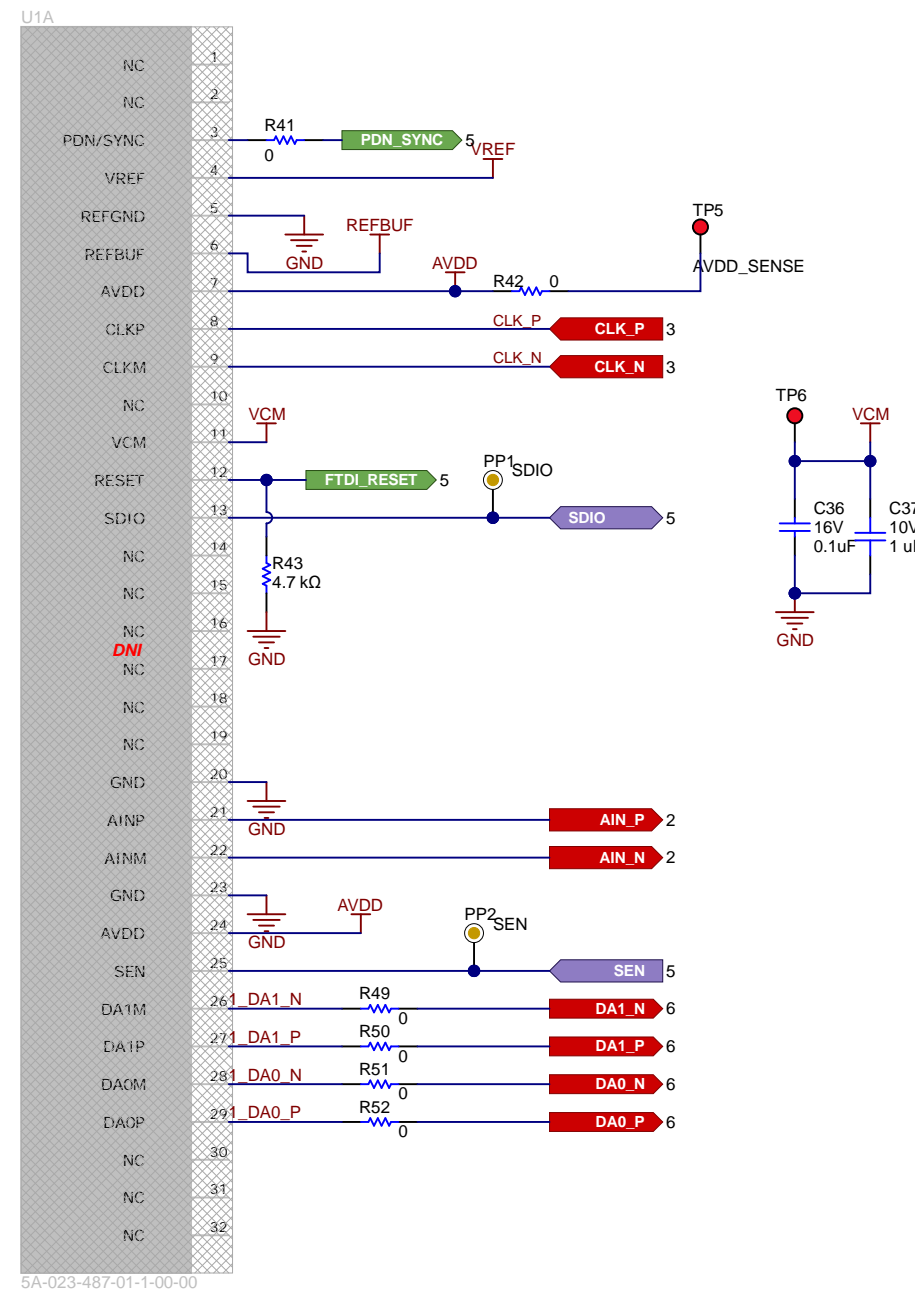
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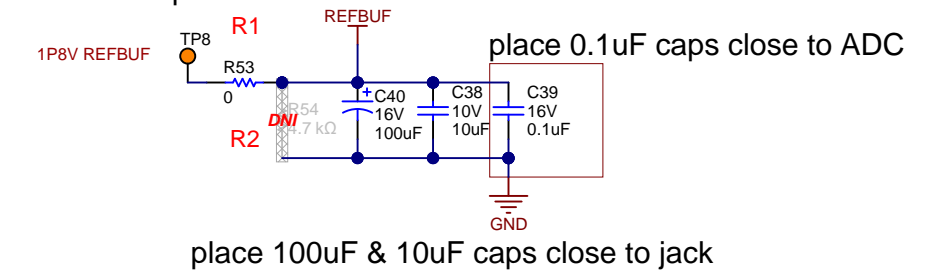
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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/1/2023
TID #: N/A	Project Title: ADC36xxEVMCVAL	
Number: ADC36xxEVMCVAL	Sheet Title: CLK_FA	
SVN Rev: Not in version control	Assembly Variant: 002	Sheet: 3 of 8
Drawn By: Daniel Brock	File: ADC36xx-CVAL_CLK_RevA.SchDoc	Size: B
Engineer: Miguel Lomeli	Contact: http://www.ti.com/support	

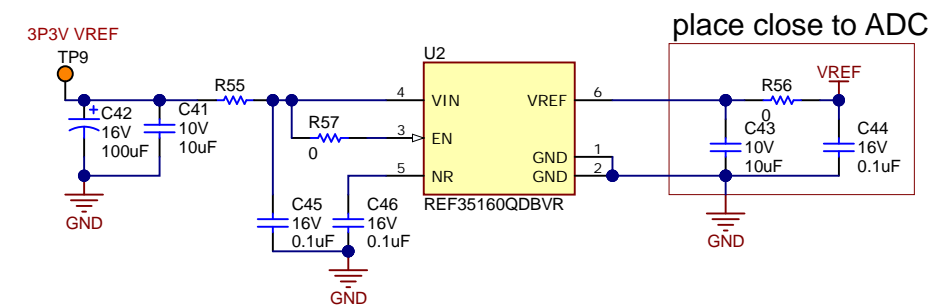


REFBUF VOLTAGE	VOLTAGE REFERENCE OPTION	CLOCKING OPTION
>1.7 V (Default)	External reference	Differential clock input
1.2 V (1.15-1.25V)	External 1.2V input on REFBUF pin using internal gain buffer	Differential clock input
0.5 - 0.7V	Internal reference	Differential clock input
< 0.1V	Internal reference	Single ended clock input

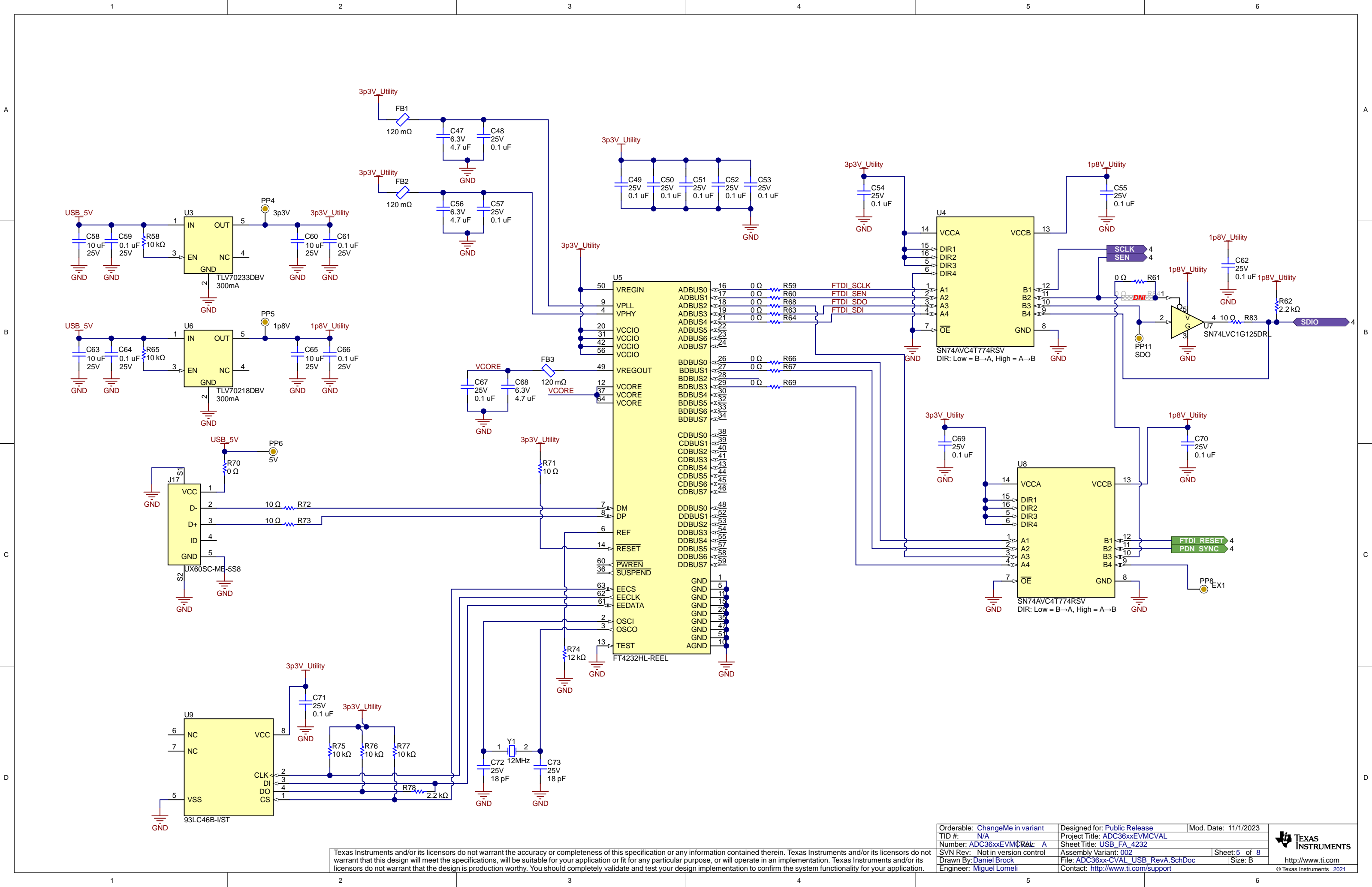
Use R1 & R2 for voltage divider
as alternate option



Reference Voltage

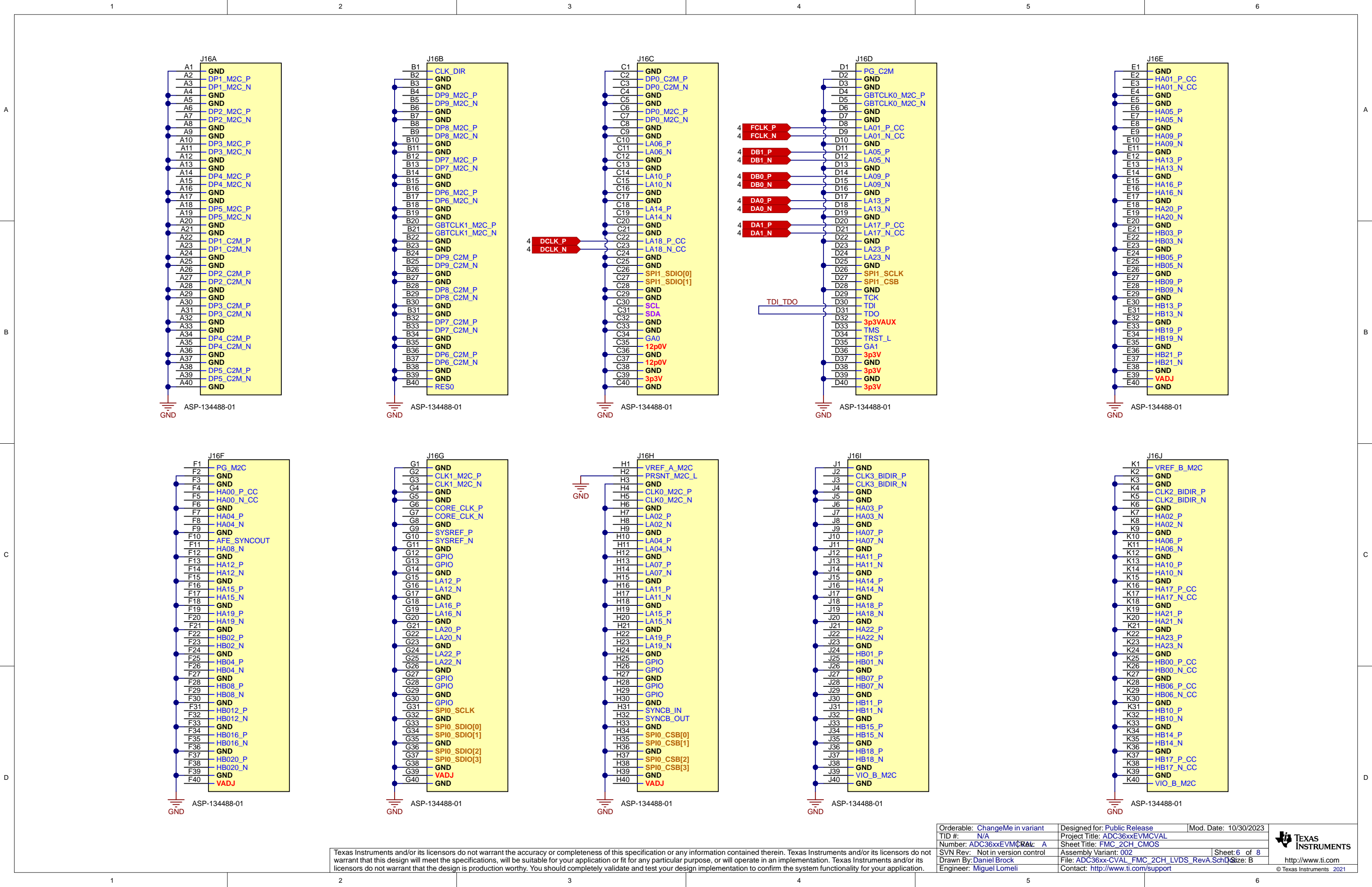


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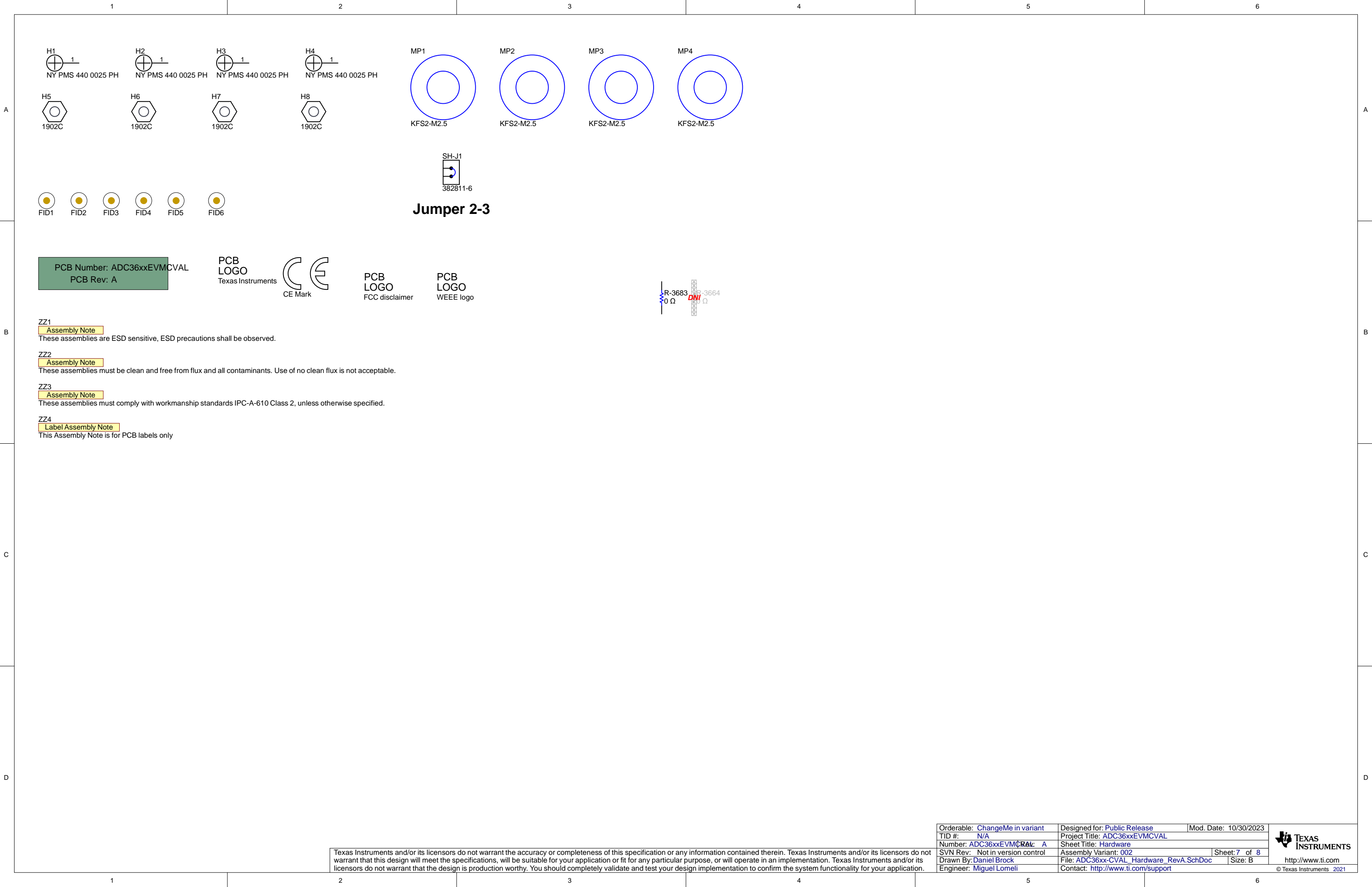
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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/1/2023
TID #: N/A	Project Title: ADC36xxEVMCVAL	
Number: ADC36xxEVMCVAL	Sheet Title: USB_FA_4232	
SVN Rev: Not in version control	Assembly Variant: 002	Sheet: 5 of 8
Drawn By: Daniel Brock	File: ADC36xx-CVAL_USB_RevA.SchDoc	Size: B
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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 10/30/2023
TID #: N/A	Project Title: ADC36xxEVMCVAL	
Number: ADC36xxEVMCVAL	Sheet Title: FMC_2CH_CMOS	
SVN Rev: Not in version control	Assembly Variant: 002	Sheet 6 of 8
Drawn By: Daniel Brock	File: ADC36xx-CVAL_FMC_2CH_LVDS_RevA.SchDoc	Size: B
Engineer: Miguel Lomeli	Contact: http://www.ti.com/support	




Revision History

Rev	Release Date	Notes
A	Dec 2023	Initial Release

Variant(s)

Variant	Notes
001	ADC3664CVAL
002	ADC3683CVAL

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TID #: N/A	Project Title: ADC36xxEVMCVAL		
Number: ADC36xxEVMCVAL	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: 002	Sheet: 8 of 8	
Drawn By:	File: ADC36xx-CVAL_Revision_History_RevA.SchDoc	Size: B	
Engineer: Miguel Lomeli	Contact: http://www.ti.com/support		