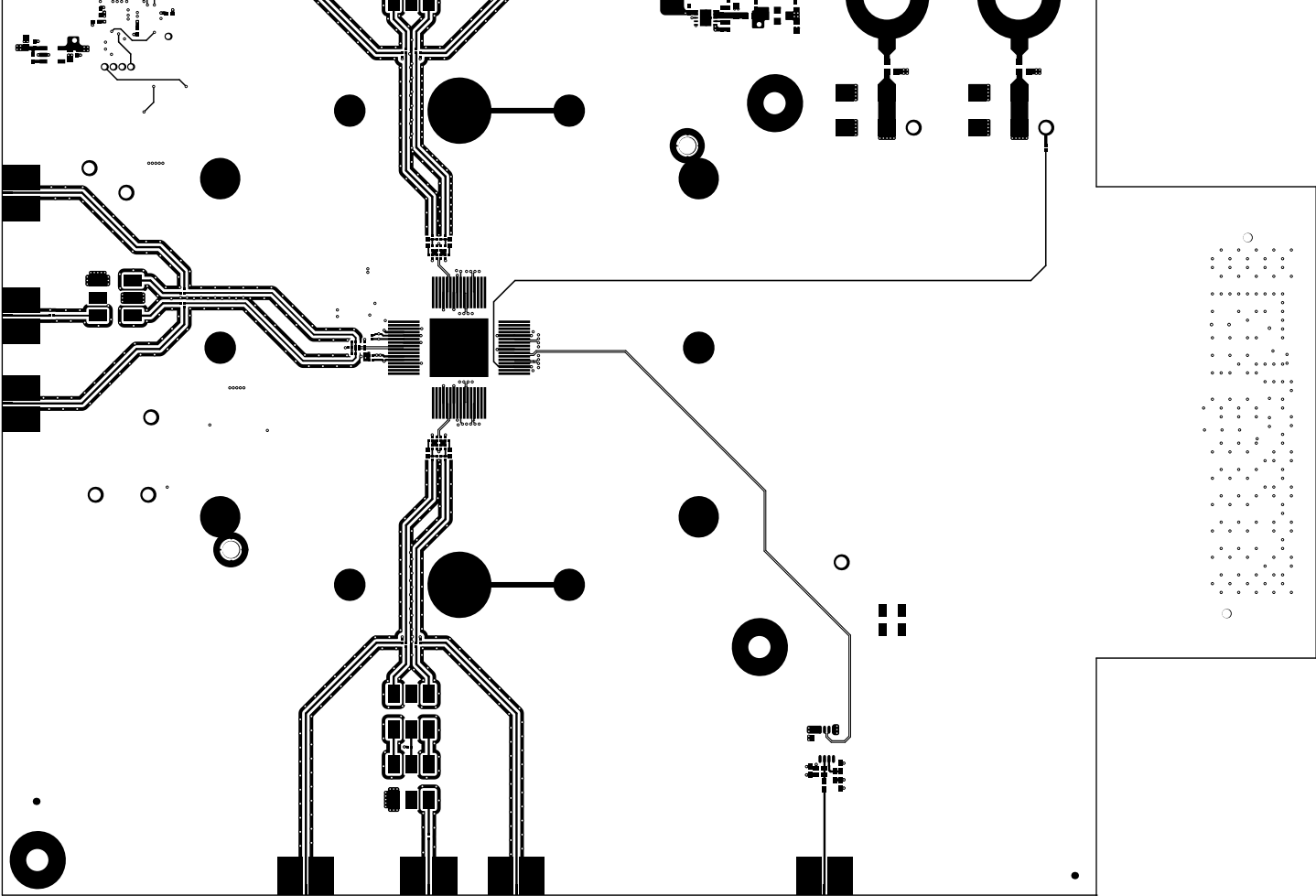
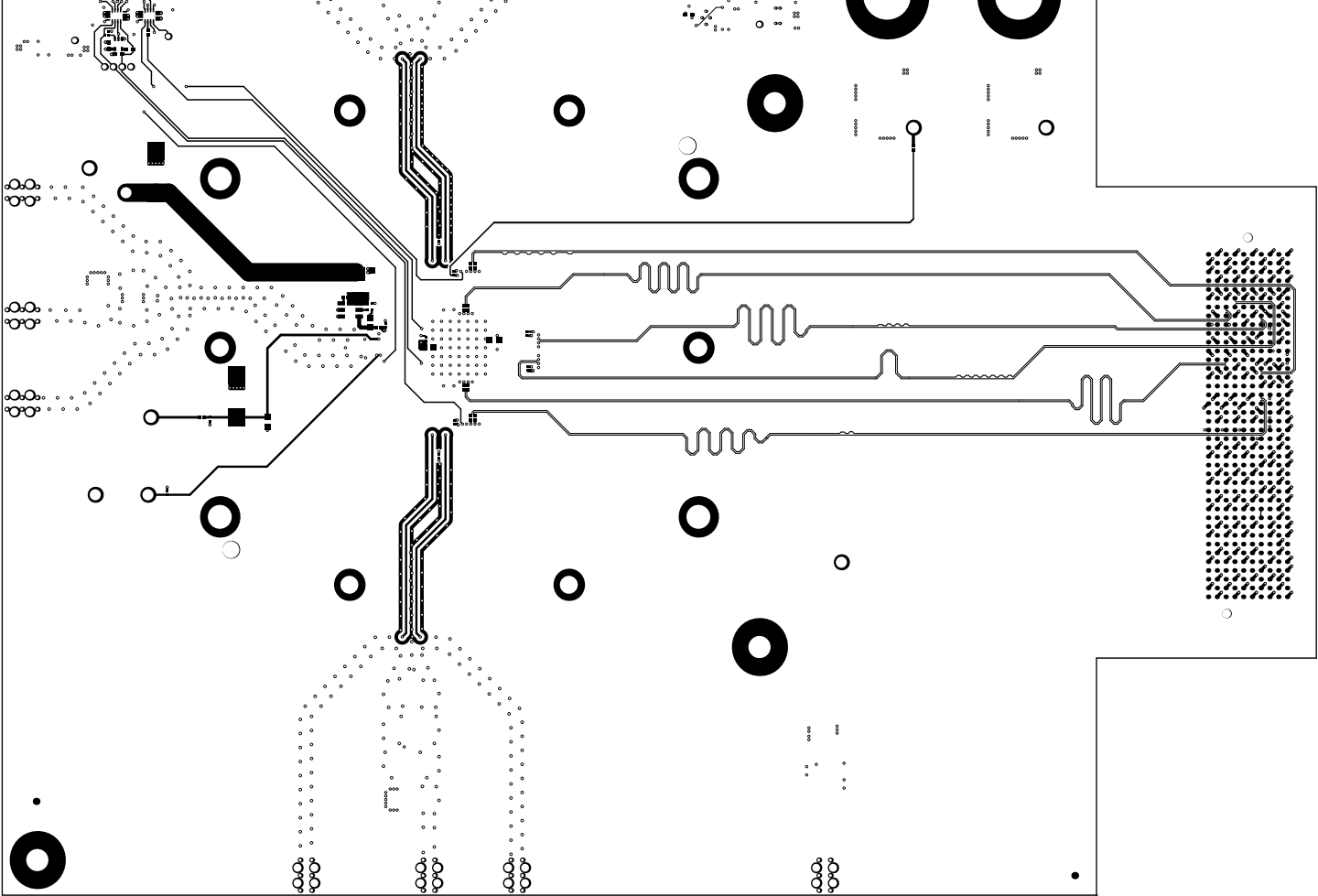


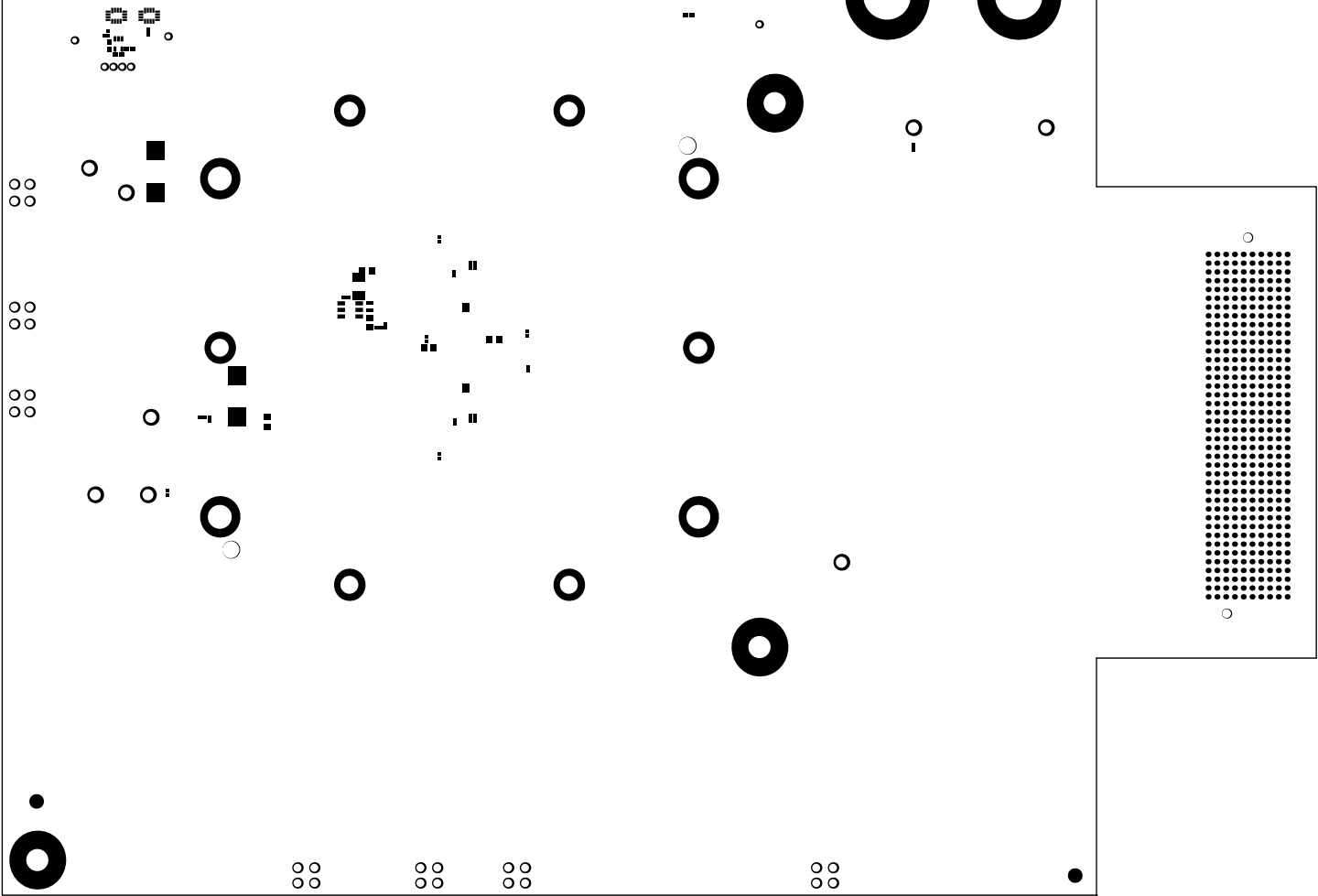
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	ADC36xxEVMQUALREV: A1	SUN REV: Not in version control
LAYER NAME = Top Solder	TID #:	N/A	
PLOT NAME = ADC36xxEVMQUAL	GENERATED	: 12/1/2023 2:36:18 PM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	ADC36xxEVMCVALREV: A1	SUN REV: Not in version control
LAYER NAME = L1_Top	TID #:	N/A	
PLOT NAME = ADC36xxEVMCVAL	GENERATED	: 12/1/2023 2:36:39 PM	TEXAS INSTRUMENTS

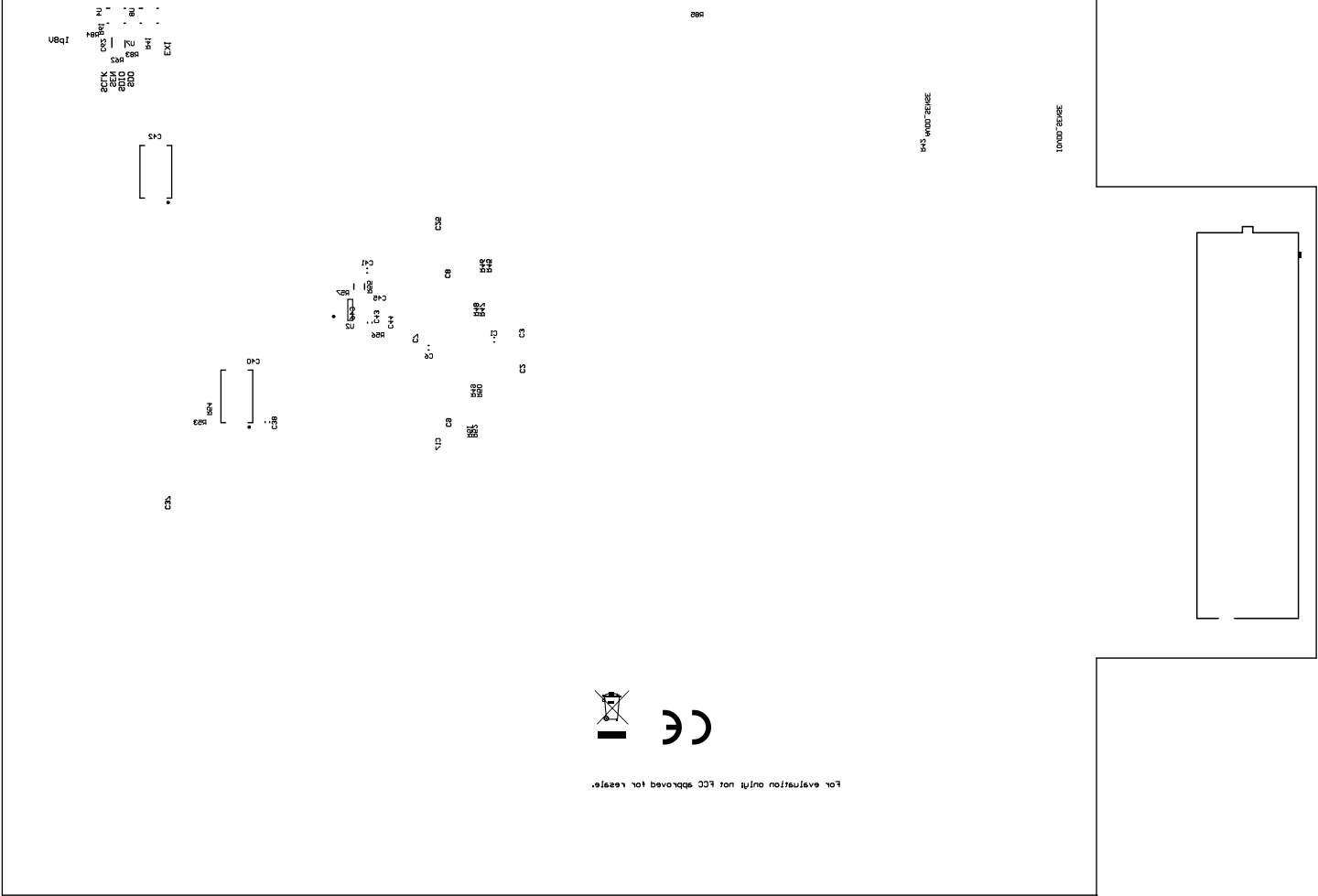


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	ADC36xxEVMCVALREV: A1	SUN REV: Not in version control
LAYER NAME = L6_Bottom	TID #:	N/A	
PLOT NAME = ADC36xxEVMCVAL	GENERATED	: 12/1/2023 2:37:01 PM	TEXAS INSTRUMENTS



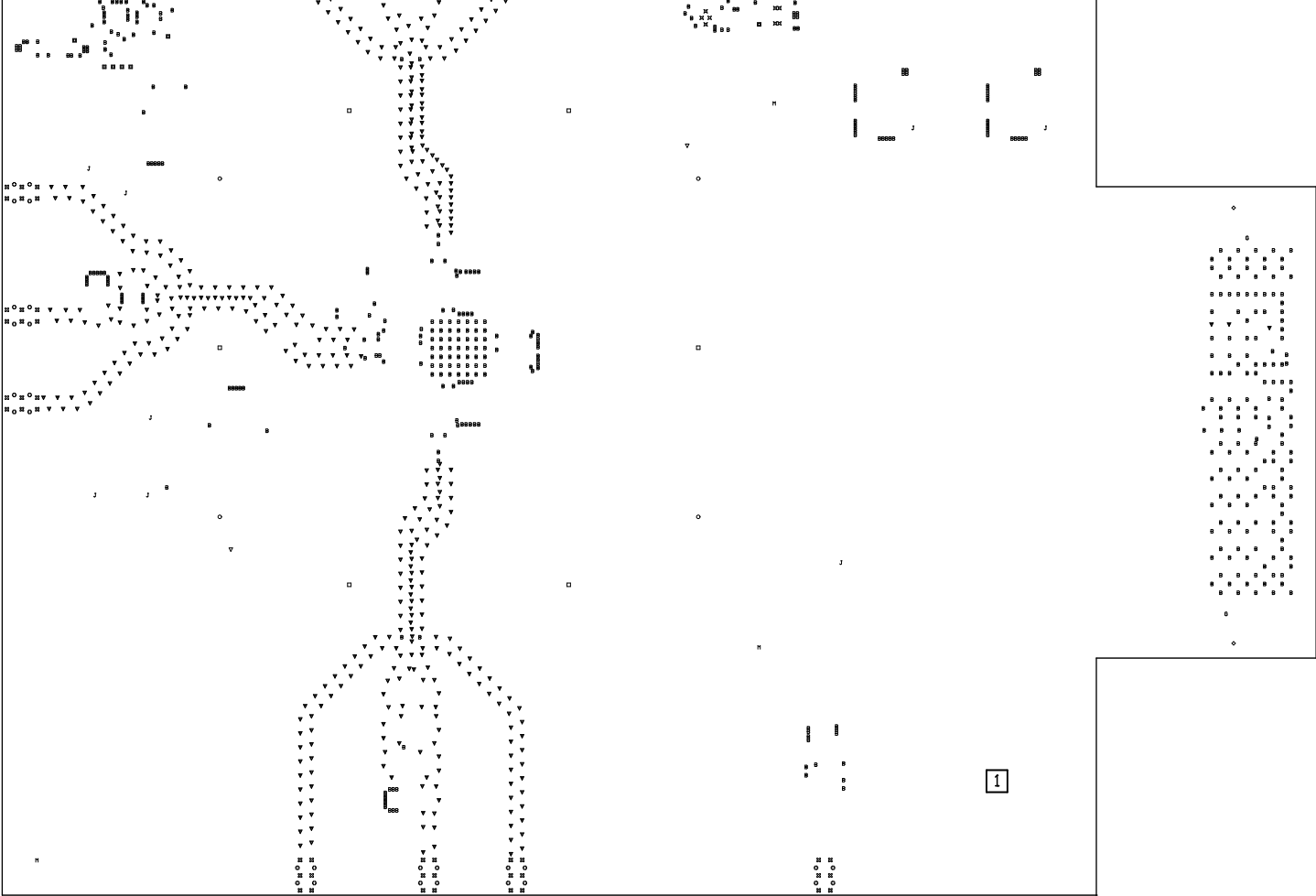
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: ADC36xxEVMCUALREV: A1	SUN REV: Not in version control
LAYER NAME = Bottom Solder	TID #: N/A	
PLOT NAME = ADC36xxEVMCUAL	GENERATED : 12/1/2023 2:37:21 PM	TEXAS INSTRUMENTS

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	ADC36xxEVMQUALREV: A1	SUN REV: Not in version control
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = ADC36xxEVMQUAL	GENERATED : 12/1/2023 2:37:41 PM		TEXAS INSTRUMENTS

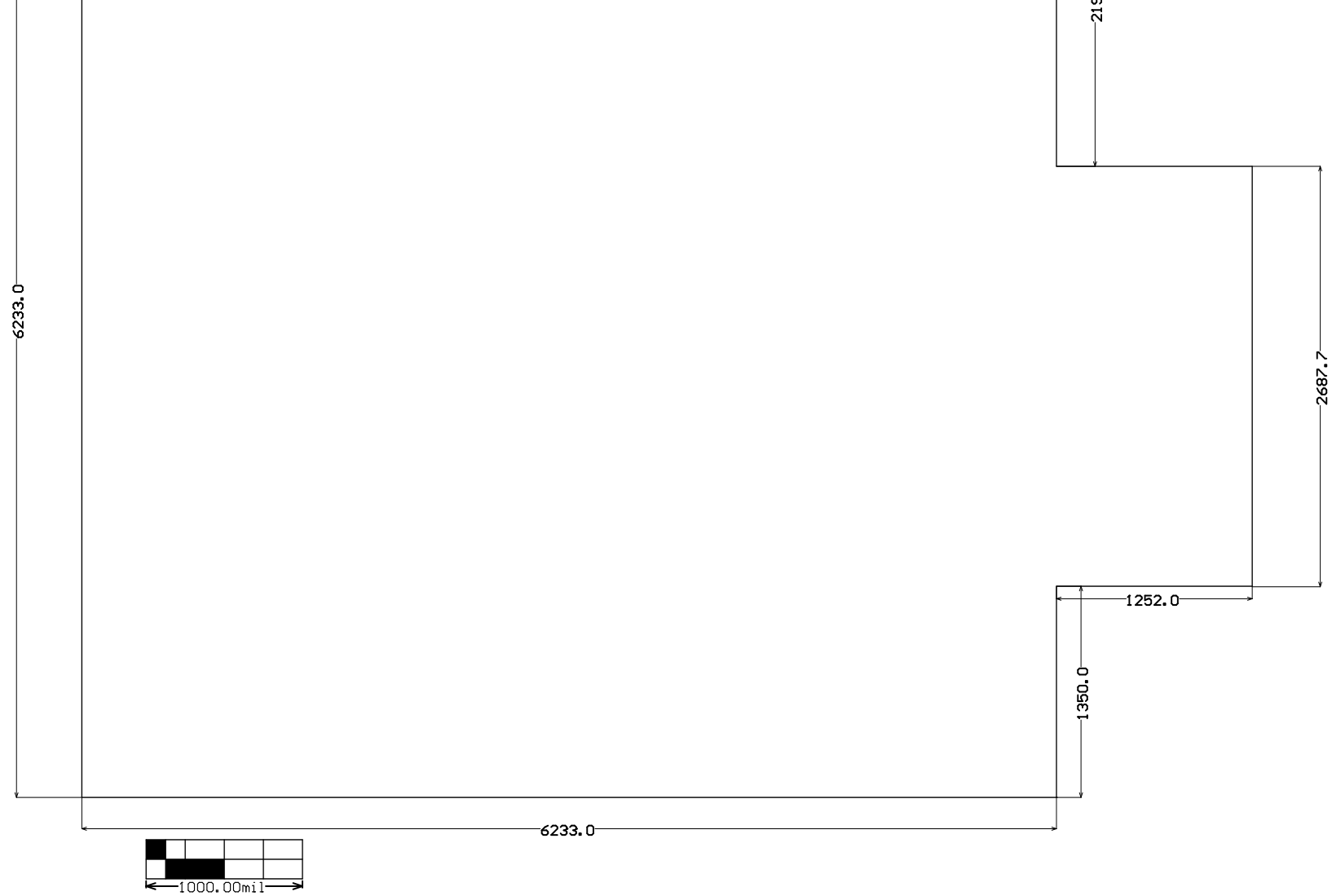


Symbol	Count	Hole Size	Plated	Hole Type	Via/Pad	Routed Path Length
⌘	16	7.87mil <0.200mm>	PTH	Round	Pad	-
B	552	8.00mil <0.203mm>	PTH	Round	Via	-
▼	484	10.00mil <0.254mm>	PTH	Round	Via	-
⊗	60	16.00mil <0.406mm>	PTH	Round	Pad	-
⊙	2	27.56mil <0.700mm>	PTH	Slot	Pad	98.43mil <2.500mm>
▣	10	28.00mil <0.711mm>	PTH	Round	Pad	-
⊕	40	45.00mil <1.143mm>	PTH	Round	Pad	-
G	2	50.00mil <1.270mm>	NPTH	Round	Pad	-
J	9	63.00mil <1.600mm>	PTH	Round	Pad	-
▽	2	96.50mil <2.451mm>	NPTH	Round	Pad	-
□	6	102.00mil <2.591mm>	PTH	Round	Pad	-
◇	2	105.91mil <2.690mm>	NPTH	Round	Pad	-
M	4	125.98mil <3.200mm>	PTH	Round	Pad	-
○	4	136.00mil <3.454mm>	PTH	Round	Pad	-
☆	5	270.00mil <6.858mm>	PTH	Round	Pad	-
1198 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	ADC36xxEVMQUALREV: A1	SUN REV: Not in version control
LAYER NAME =	FAB	TID #:	N/A
PLOT NAME =	ADC36xxEVMQUAL	GENERATED : 12/1/2023 2:38:02 PM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	ADC36xxEVMQUAL	REV:	A1	SUN REV:	Not in version control
LAYER NAME =	TID #:	N/A				
PLOT NAME =	ADC36xxEVMQUAL	GENERATED	:	12/1/2023	2:38:23 PM	TEXAS INSTRUMENTS