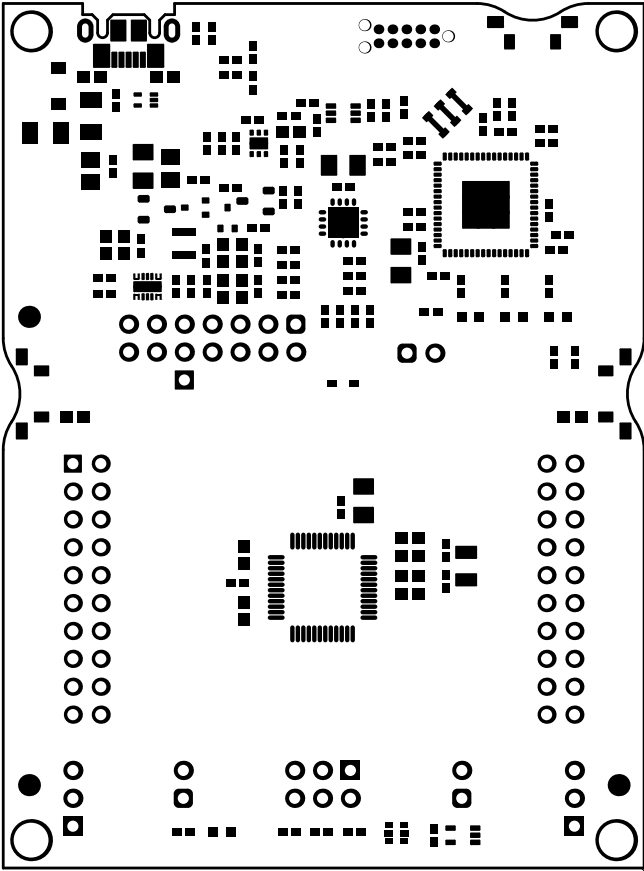
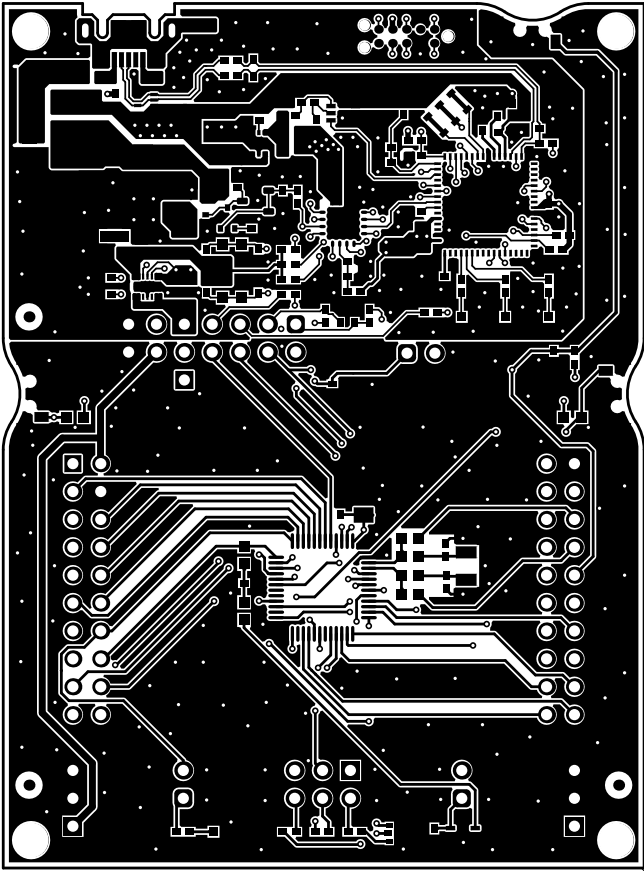


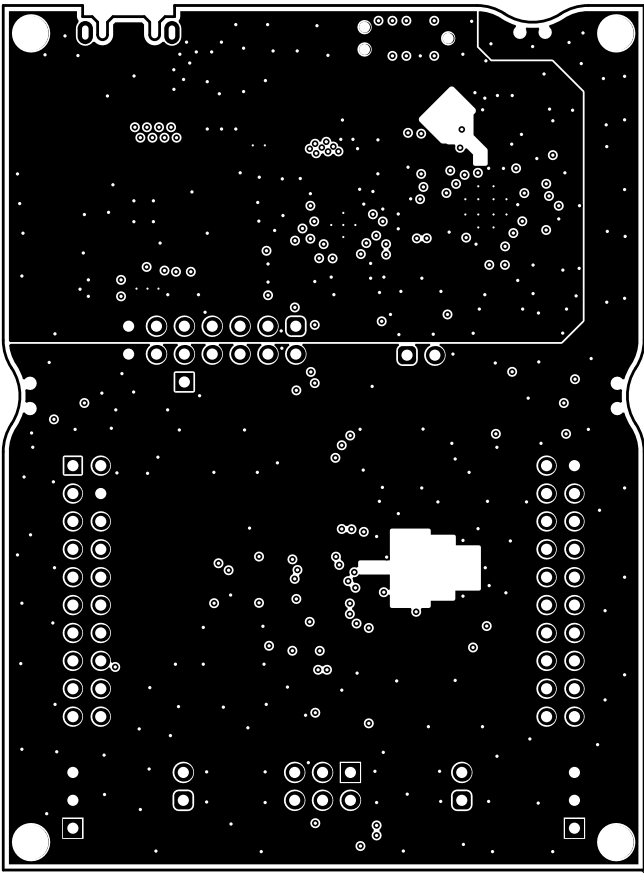
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU045	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 12/19/2018 2:16:25 PM		TEXAS INSTRUMENTS



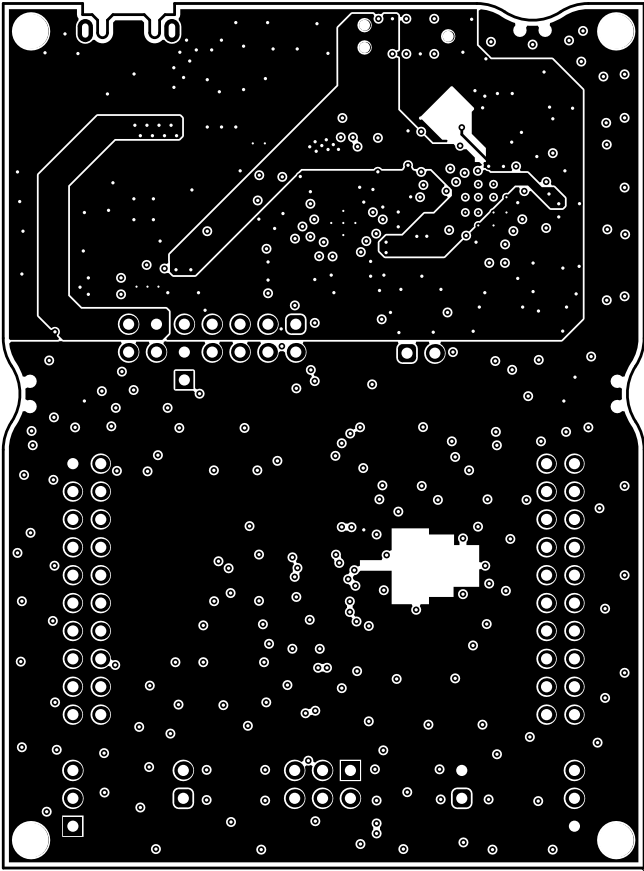
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU045	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Top Solder	TID #: N/A		
PLOT NAME = Top Solder Mask	GENERATED : 12/19/2018 2:16:27 PM		TEXAS INSTRUMENTS



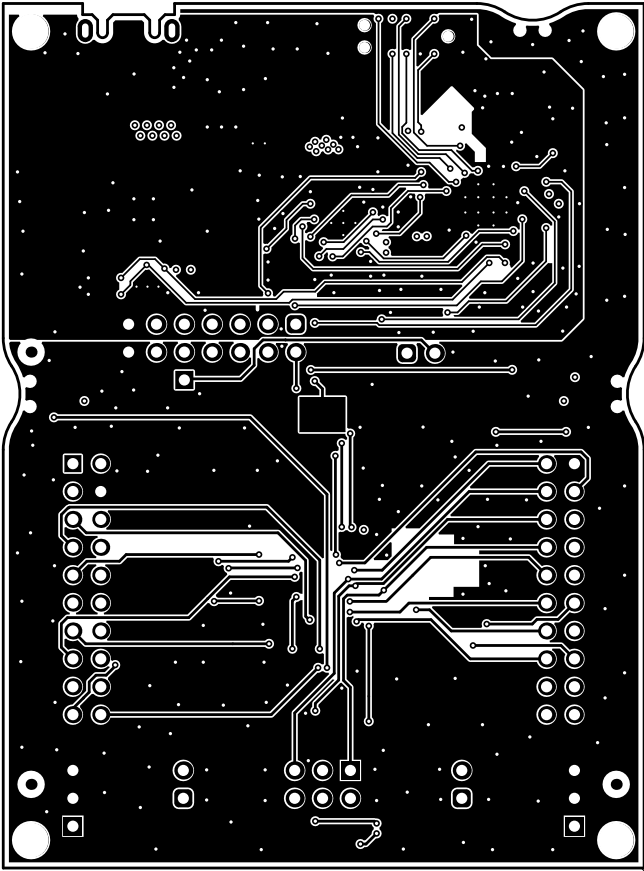
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU045	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Top Layer	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 12/19/2018 2:16:29 PM		TEXAS INSTRUMENTS



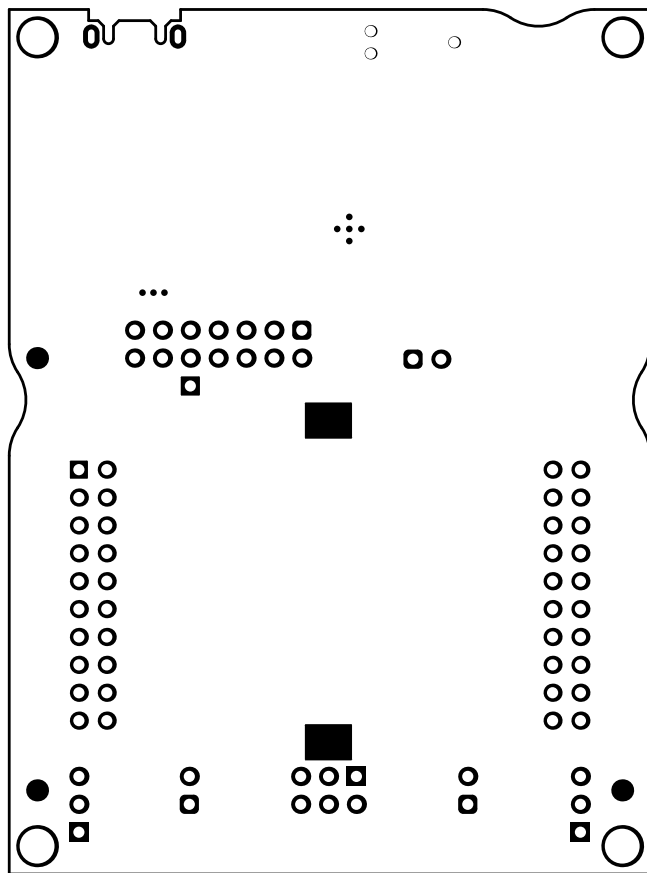
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU045	REV: A	SUN REV: Not In VersionControl
LAYER NAME = GND	TID #: N/A		
PLOT NAME = Signal Layer 1	GENERATED : 12/19/2018 2:16:30 PM		TEXAS INSTRUMENTS



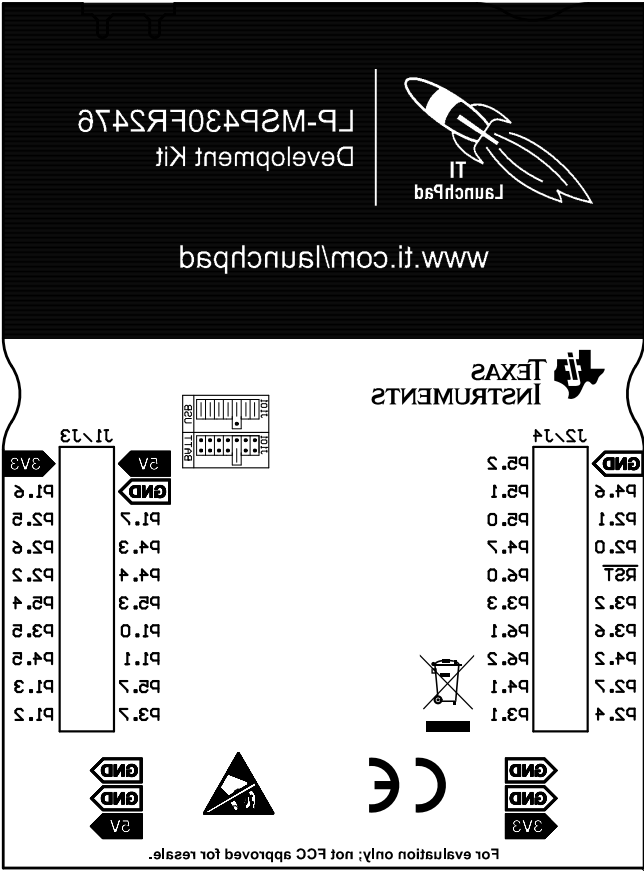
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU045	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Vcc	TID #: N/A		
PLOT NAME = Signal Layer 2	GENERATED : 12/19/2018 2:16:32 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU045	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Bottom Layer	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 12/19/2018 2:16:33 PM		TEXAS INSTRUMENTS



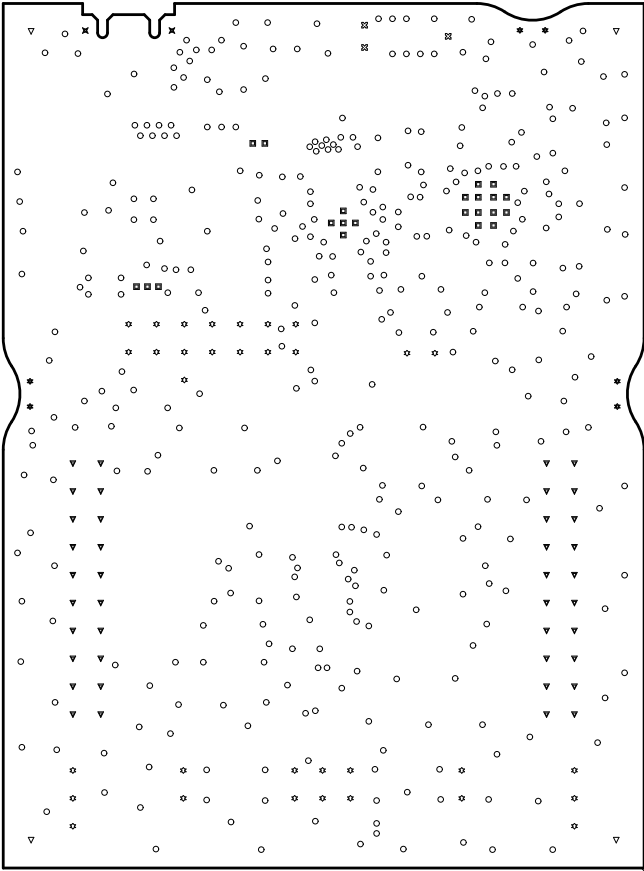
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU045	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 12/19/2018 2:16:35 PM		TEXAS INSTRUMENTS



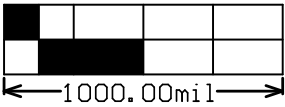
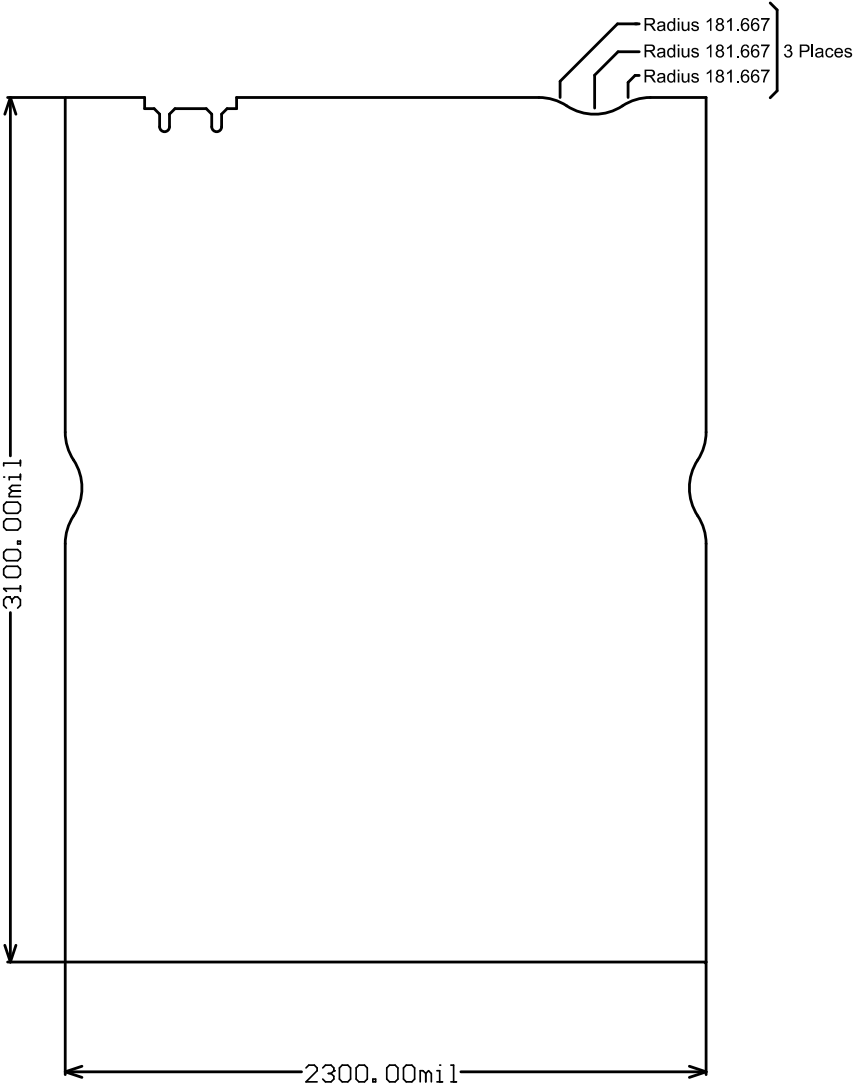
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU045	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 12/19/2018 2:16:37 PM	TEXAS INSTRUMENTS	

Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
✳	6	35.43mil (0.900mm)	NPTH	Round	Top Layer - Bottom Layer	
⊠	3	39.00mil (0.991mm)	NPTH	Round	Top Layer - Bottom Layer	
▽	4	125.00mil (3.175mm)	NPTH	Round	Top Layer - Bottom Layer	
◼	22	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
○	387	11.81mil (0.300mm)	PTH	Round	Top Layer - Bottom Layer	
✳	33	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer	
▼	40	40.16mil (1.020mm)	PTH	Round	Top Layer - Bottom Layer	
✕	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	497 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU045	REV: A	SUN REV: Not In VersionControl
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 12/19/2018 2:16:38 PM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU045	REV: A	SUN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED : 12/19/2018 2:16:41 PM		TEXAS INSTRUMENTS