

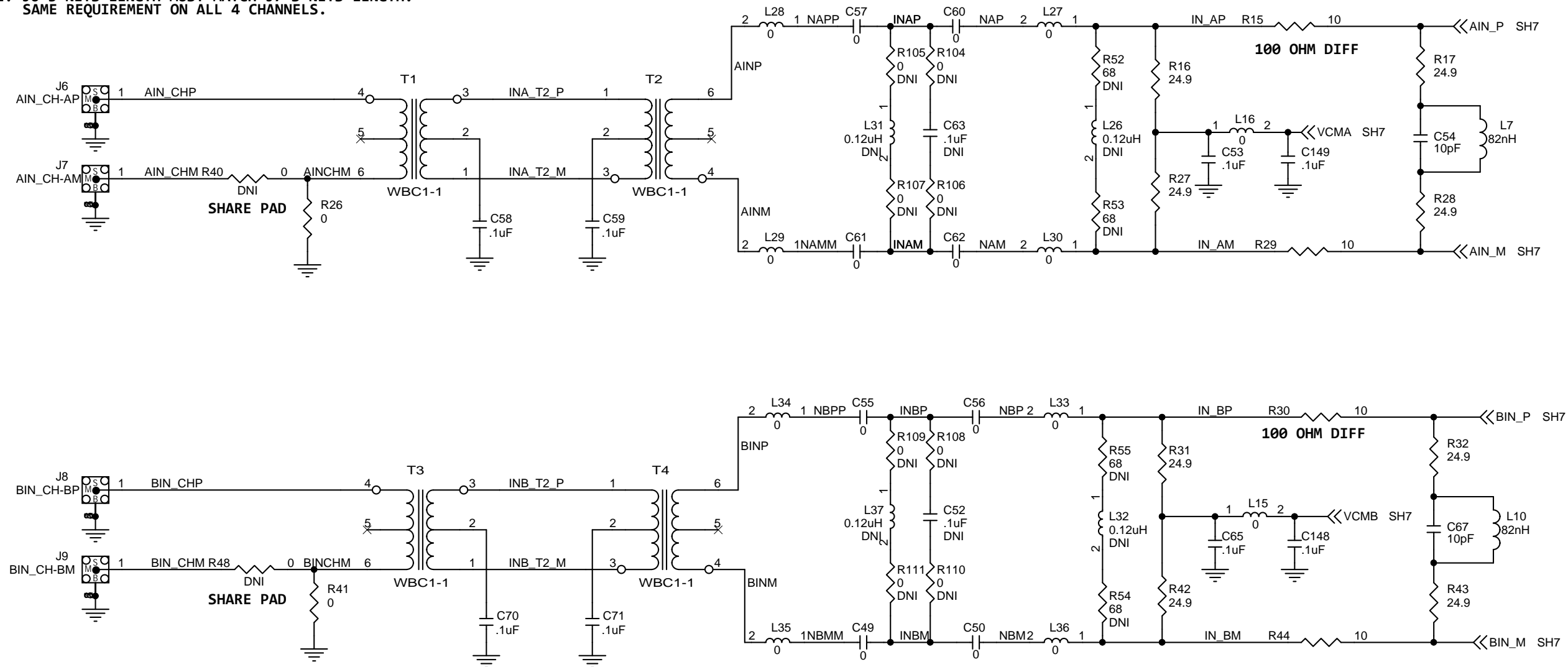
**NOTES:**  
1) DNI = DO NOT INSTALL



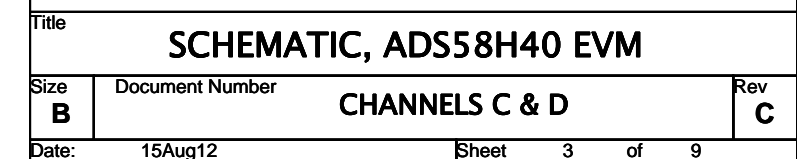
Title		
SCHEMATIC, ADS58H40 EVM		
Size	Document Number	Rev
B	MAIN POWER SUPPLY	C
Date:	16Jul12	Sheet 1 of 9

DRAWN BY: JV SMITH	7-13-2012
ENGINEER: K HSIA	7-13-2012

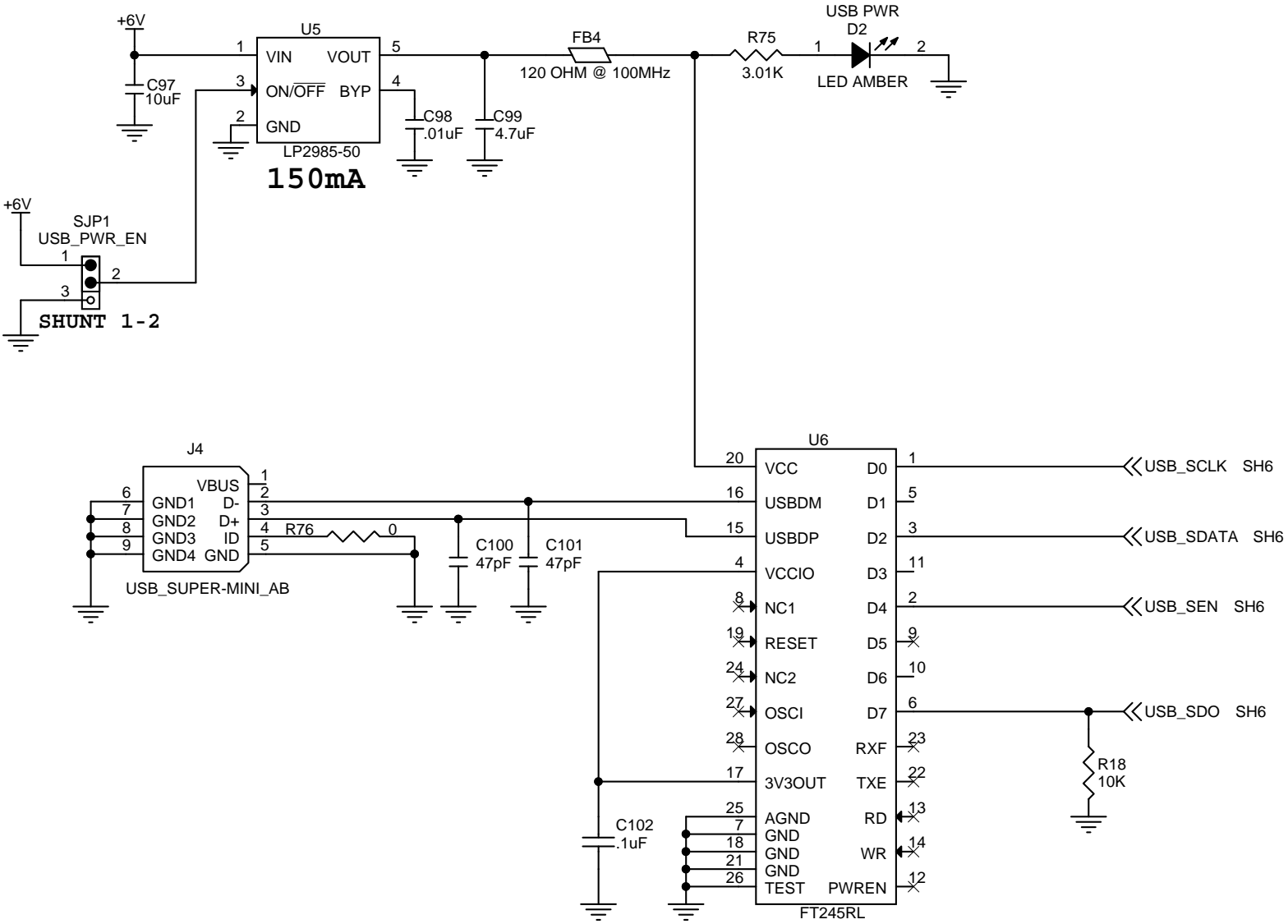
NOTE: J6's NETS LENGTH MUST MATCH J7'S NETS LENGTH.  
SAME REQUIREMENT ON ALL 4 CHANNELS.



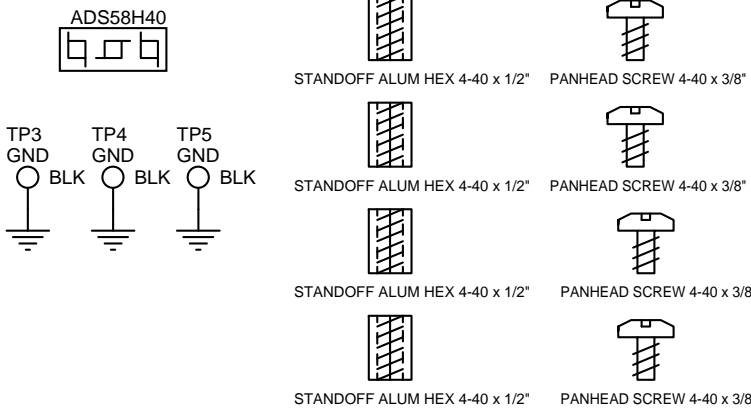
Title		
SCHEMATIC, ADS58H40 EVM		
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B	CHANNELS A & B	C
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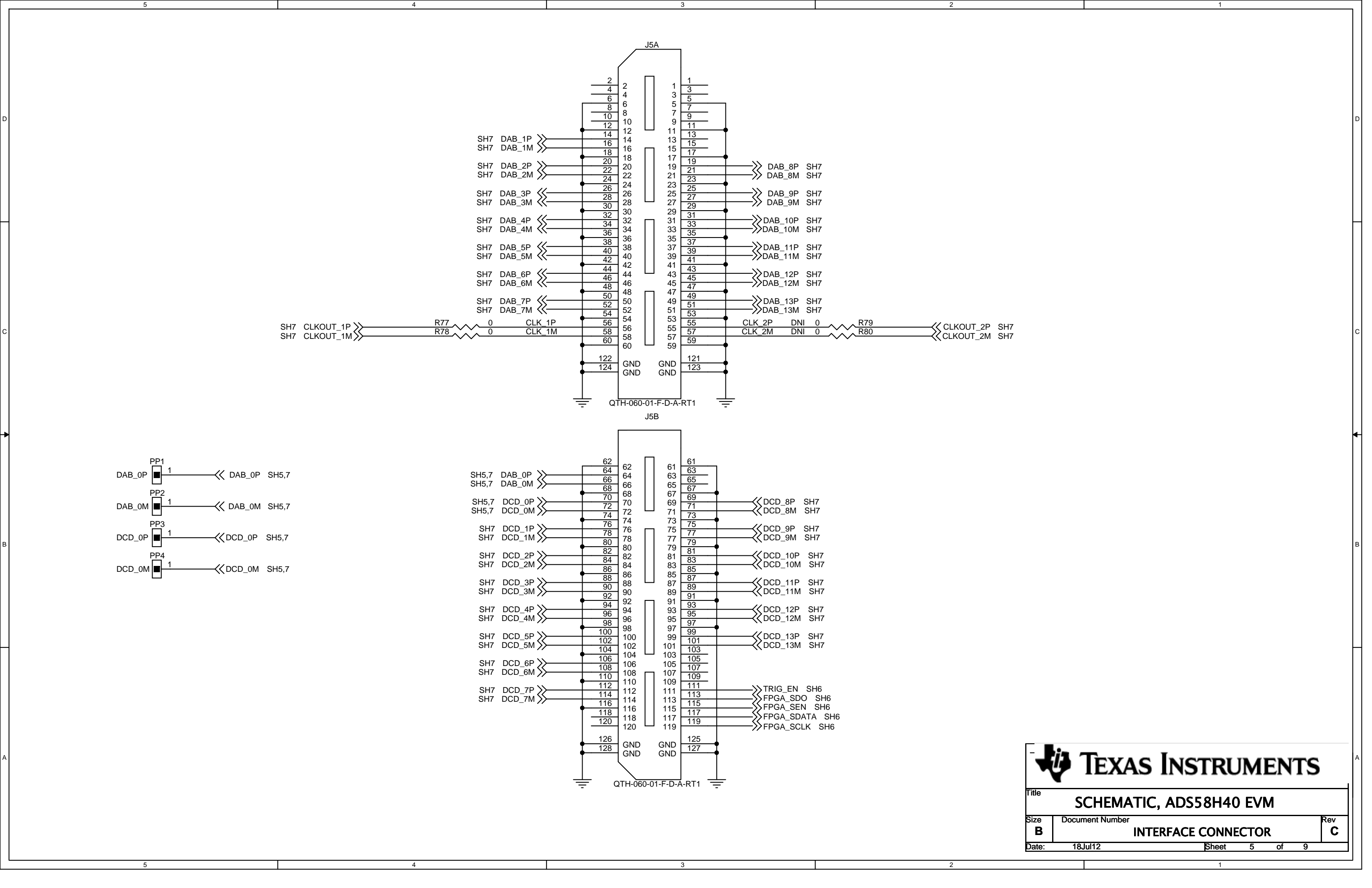


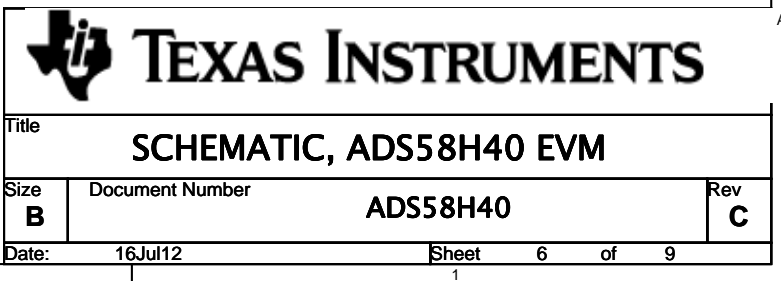
SERIAL INTERFACE



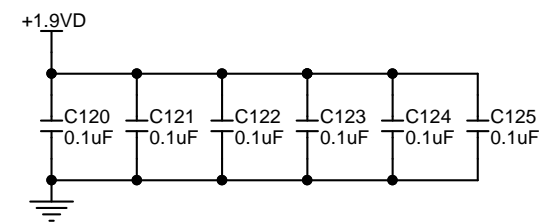
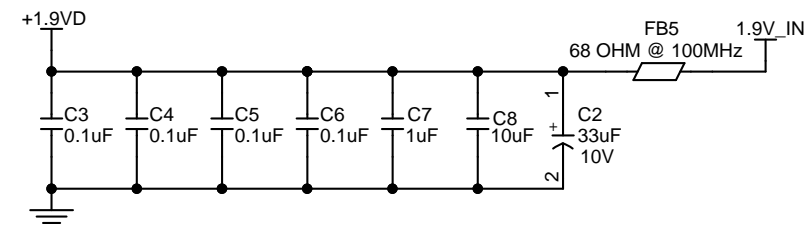
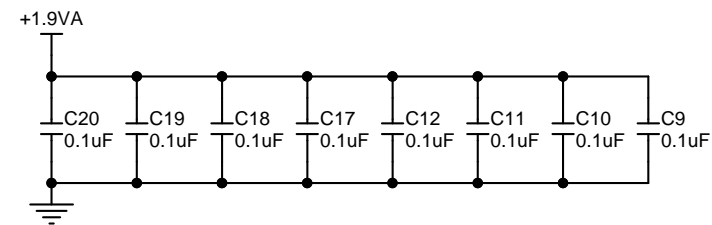
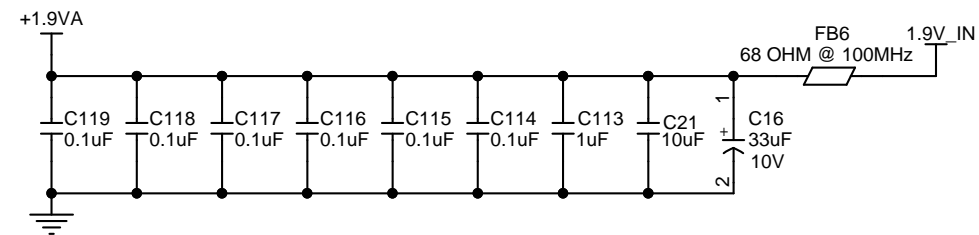
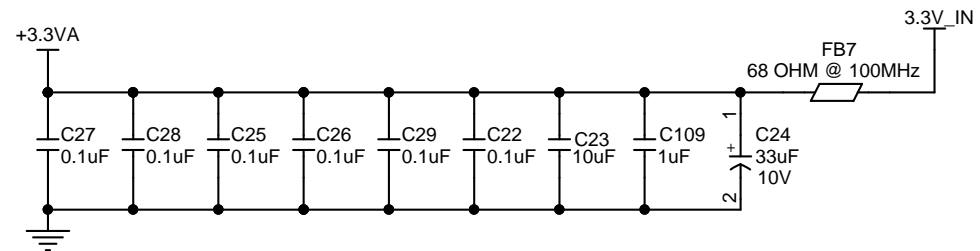
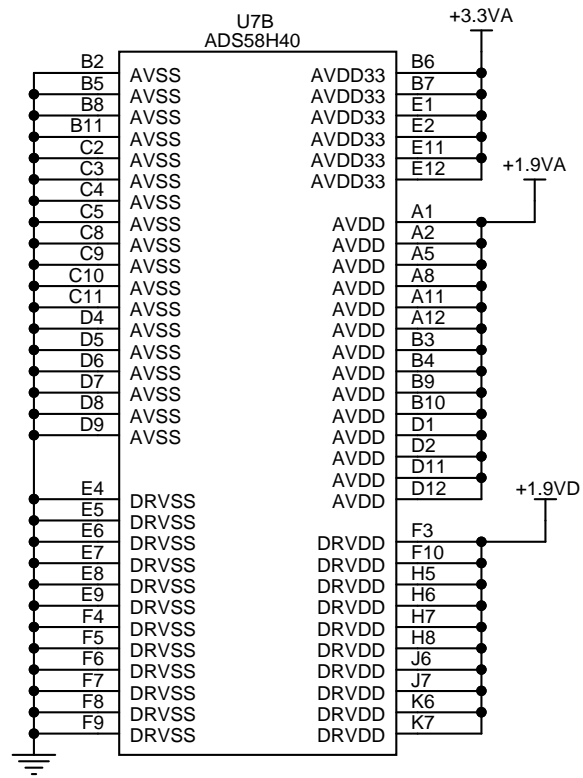
HARDWARE














5	4	3	2	1
D				D
C				C
B				B
A				A

- REV C CHANGES:
- A. Changed SJP2 zero ohm jumper to JP12 header jumper.  
This allows external trigger signal from the TSW1400 to route to JP12 pin 2 position.
  - B. On the four ADC analog input:  
Removed single-ended filter components.  
Added differential 3rd order filter components after the 2nd transformer circuit.  
Added RLR differential arm circuit for 100 ohm application.  
Optimized RLCR component values for 185MHz input frequency center.  
Added SMA connector for both positive and negative leg of the transformer to test external amplifier.
  - C. Changed reference designator for USB and Samtec connector.
  - D. BOM change:  
JP7 shunt pins 2-3  
JP12 shunt pins 2-3

 **TEXAS INSTRUMENTS**

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