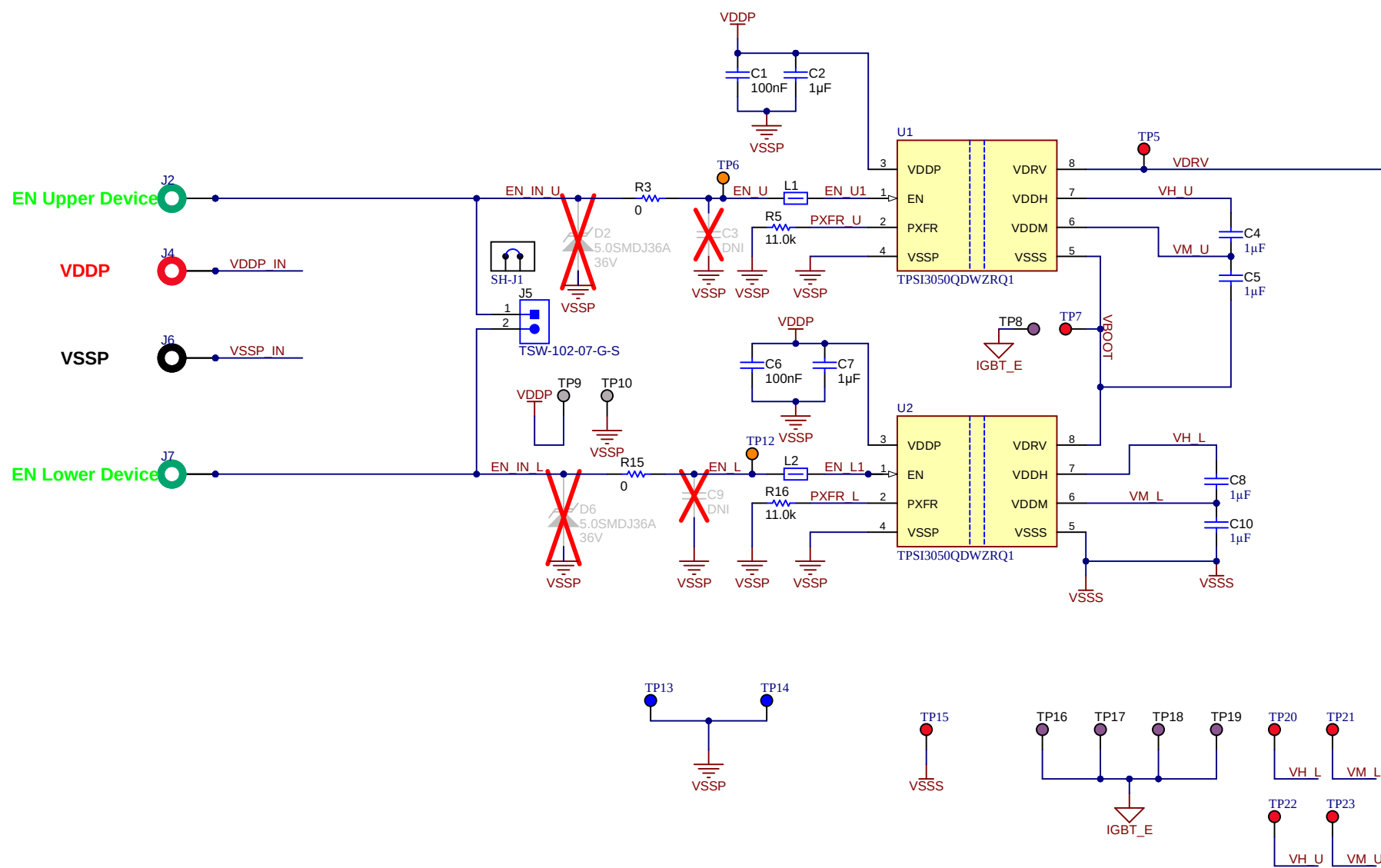
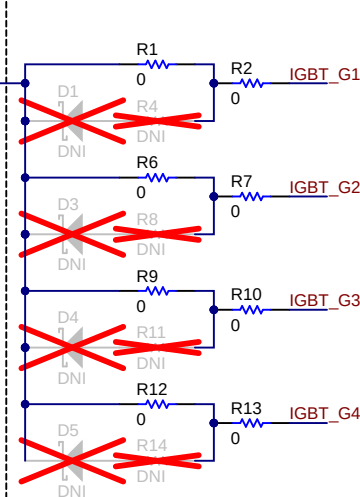
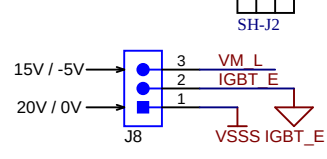




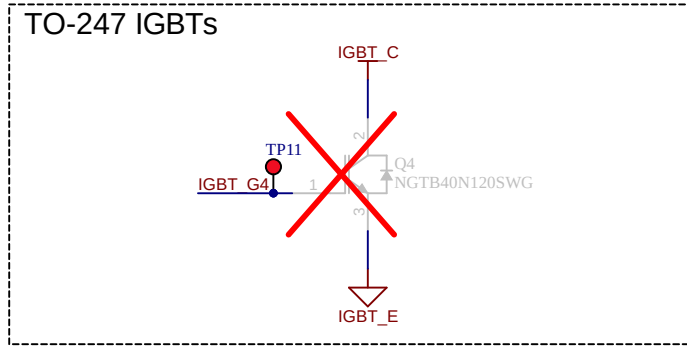
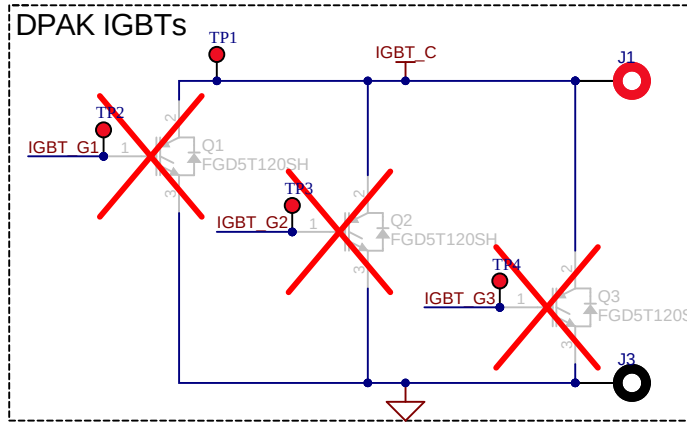
Gate Resistors / Split out



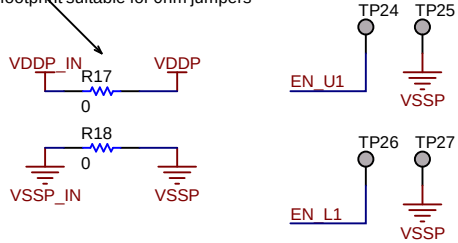
Vdrive SEL



Jumper for Lower only. Add same for U1?



Optional CMC, make footprint suitable for 0hm jumpers



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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 3/29/2022
TID #: N/A	Project Title: Change in menu Project	Project Options Parameter
Number: Cascode TPSI3050 E1	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 3
Drawn By:	File: BlankSheet.SchDoc	Size: B
Engineer: Francisco Lauzurique	Contact: http://www.ti.com/support	



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PCB
LOGO

WEEE logo



Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

PCB
LOGO

Texas Instruments

PCB Number: Cascode_TPS13050
PCB Rev: E1

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 3/15/2022
TID #: N/A	Project Title: Change in menu Project Project Options Parameter	
Number: Cascode_TPS13050_E1	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 3
Drawn By:	File: Hardware.SchDoc	Size: B
Engineer: Francisco Lauzurique	Contact: http://www.ti.com/support	