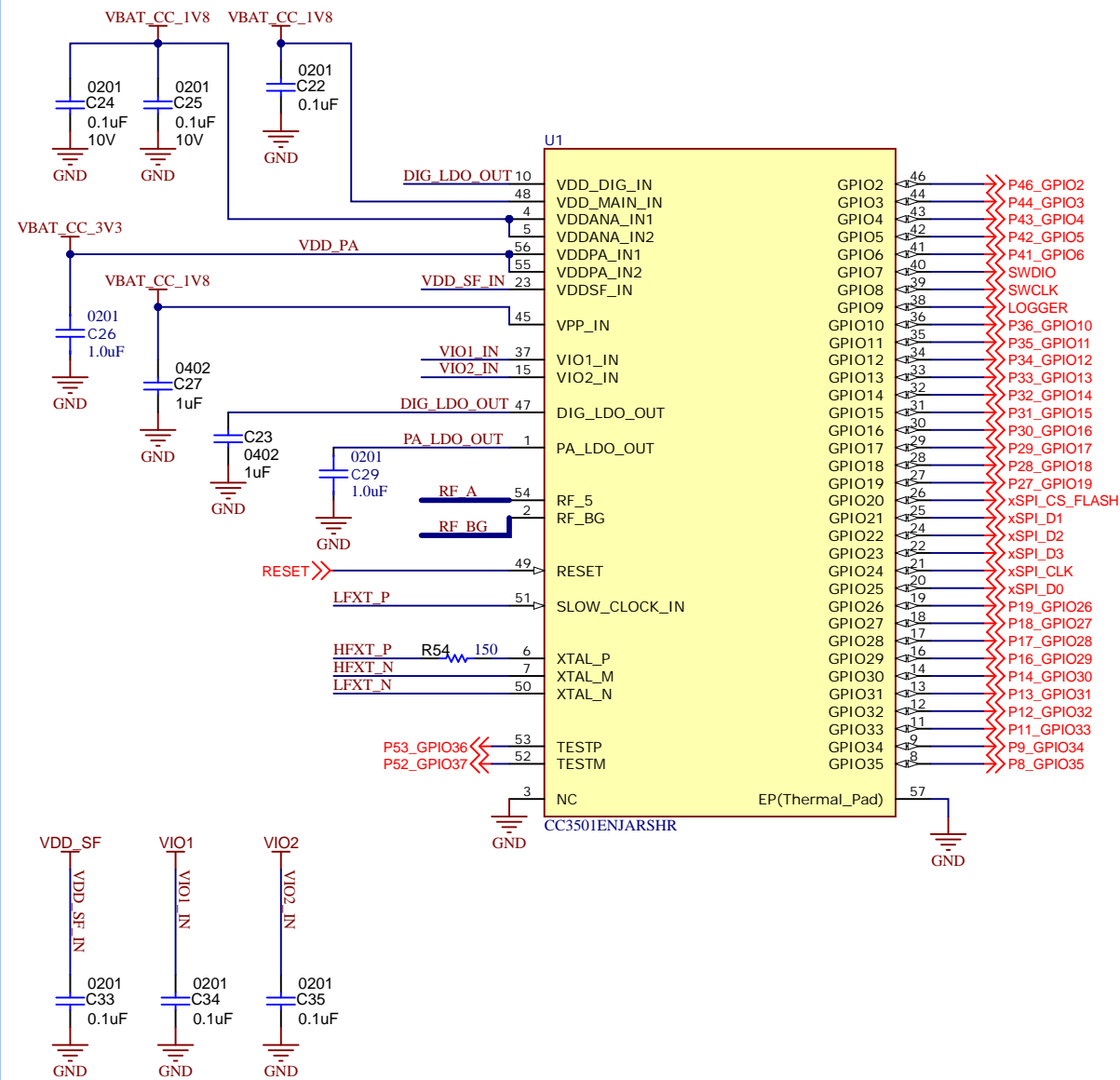
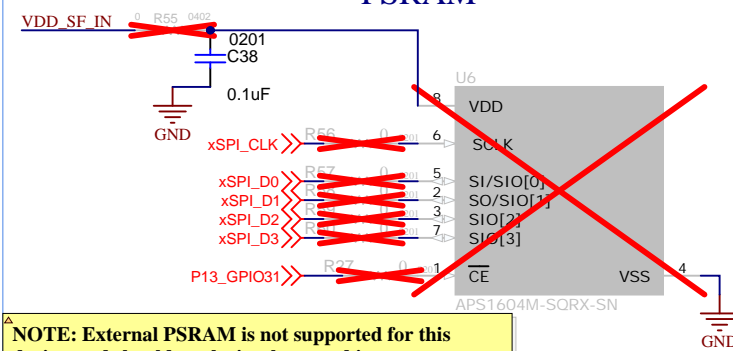


CC3501 TARGET

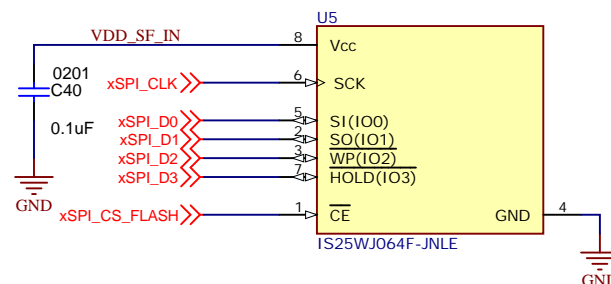


PSRAM

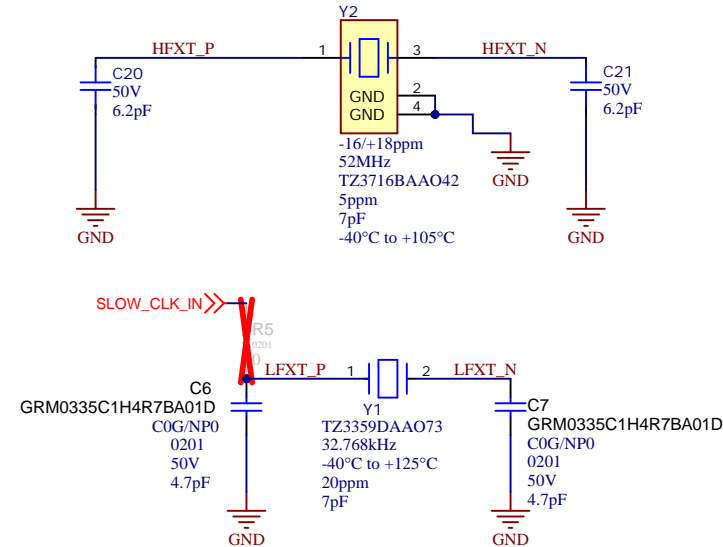


NOTE: External PSRAM is not supported for this device, and should not be implemented in a custom board design with the CC35xxE.

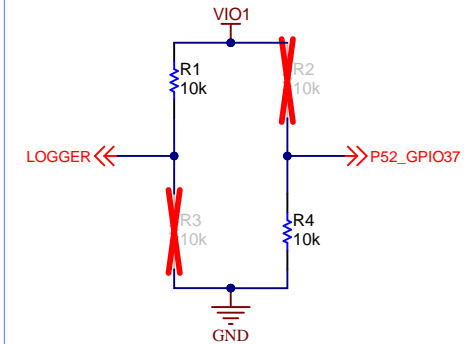
External Flash



Clocks

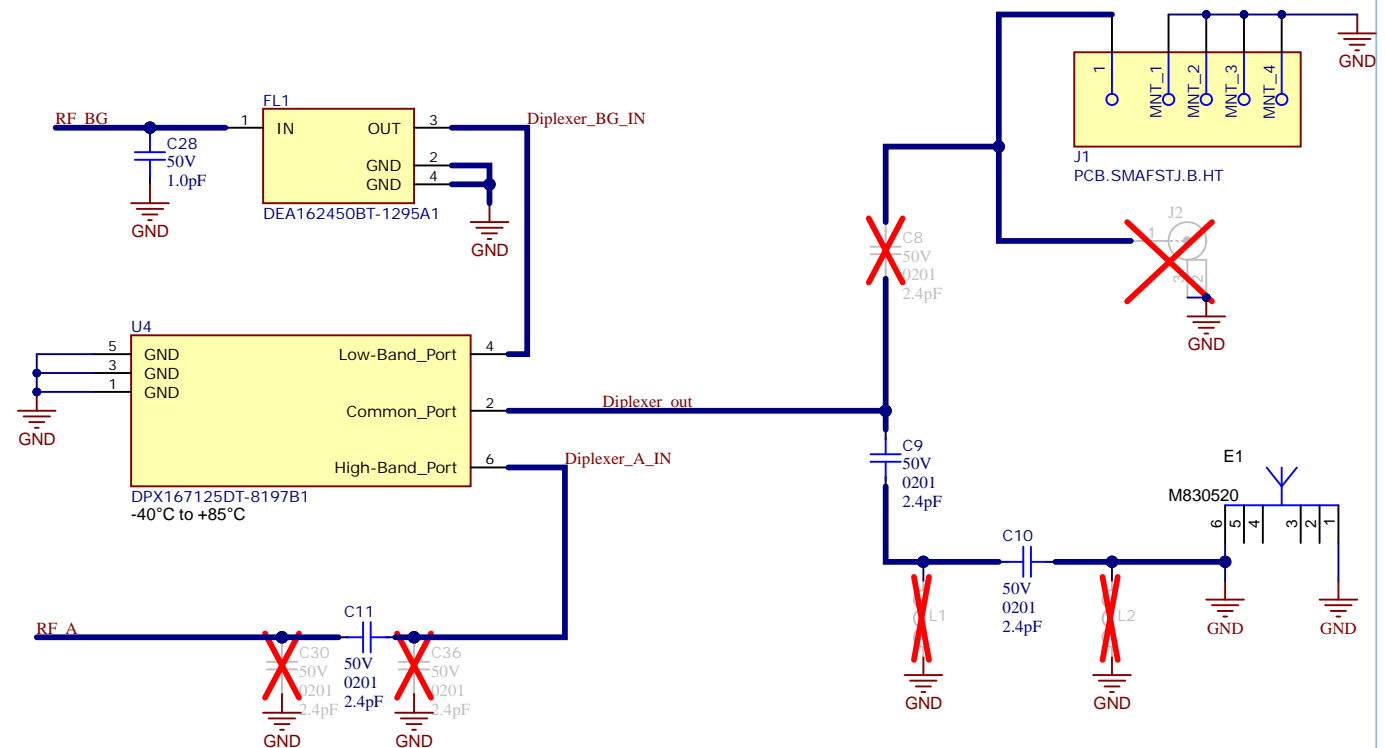


SoP MODES



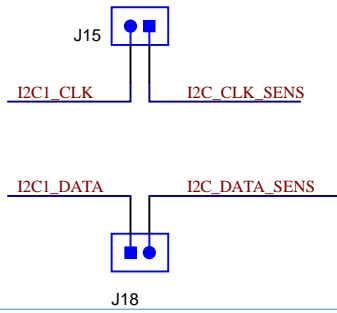
NOTE: SOP mode manipulation is for internal use only. Pull up / pull down resistors are not required for these signals in a custom board design with the CC35xxE.

RF Section

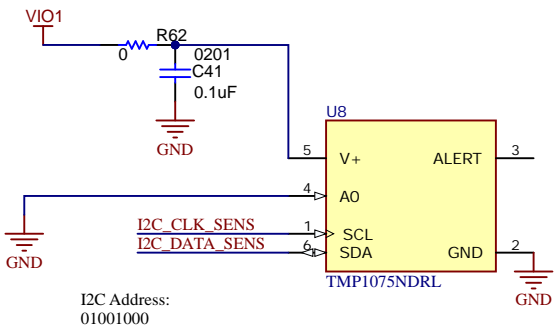


Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 7/31/2025
TID #: N/A	Project Title: LP-EM-CC35X1	
Number: MCU-138	Rev: A	Sheet Title:
SVN Rev: dcf40d21423adf2ac2a963694427b506178818043		Sheet: 1 of 2
Drawn By: Jonathan Cohen	File: MCU138A_EngineArea_SchDoc	Size: B
Engineer: Jonathan Cohen	Contact: http://www.ti.com/support	

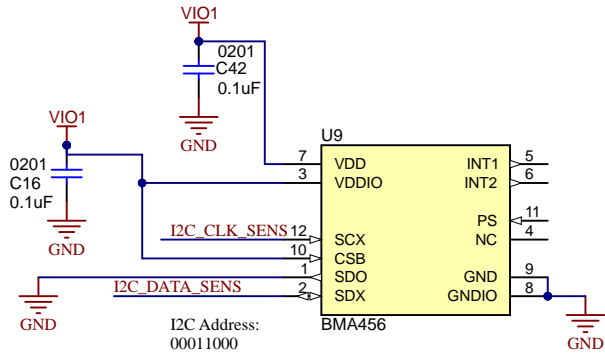
I2C Bus Selection



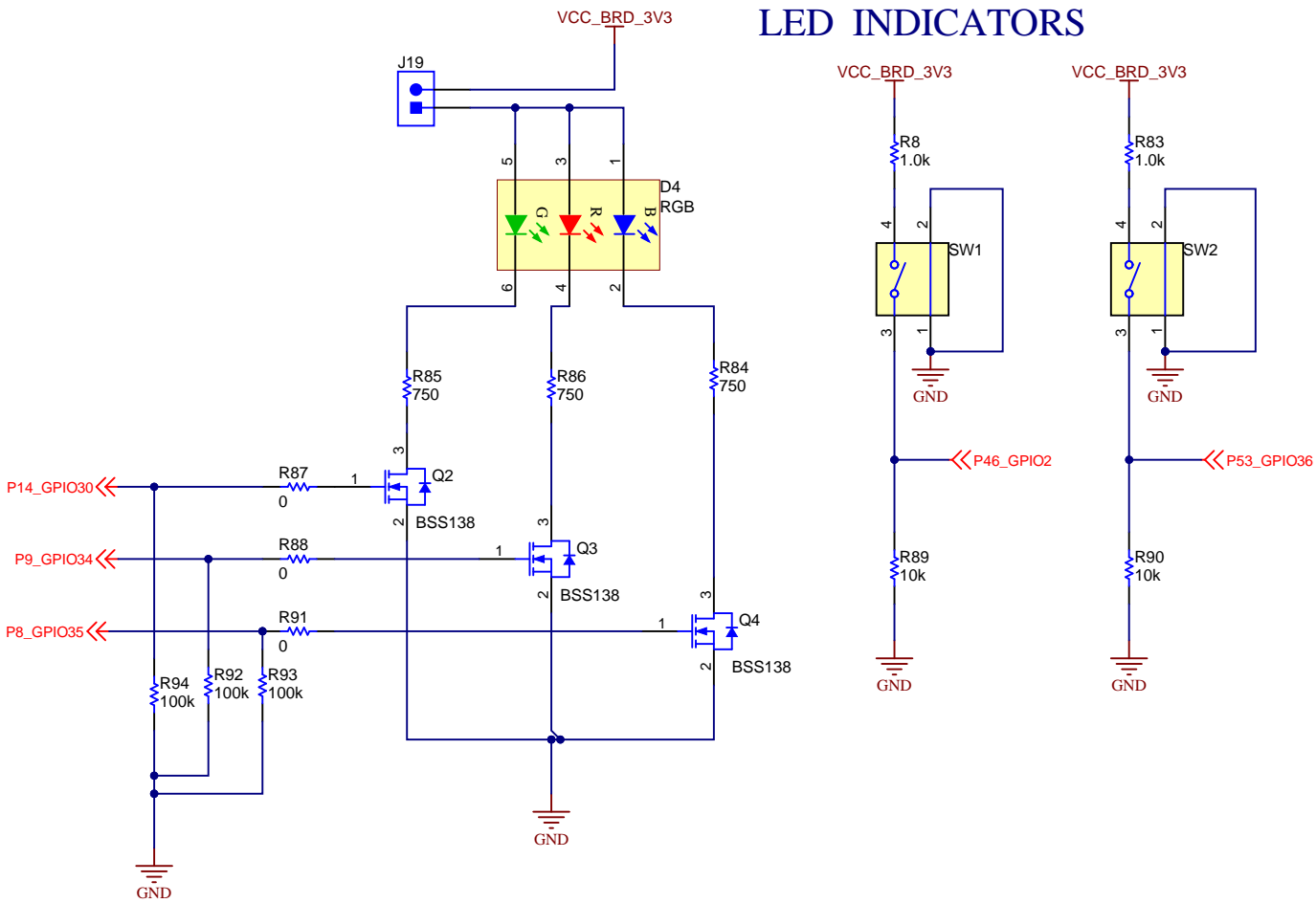
Temp Sensor



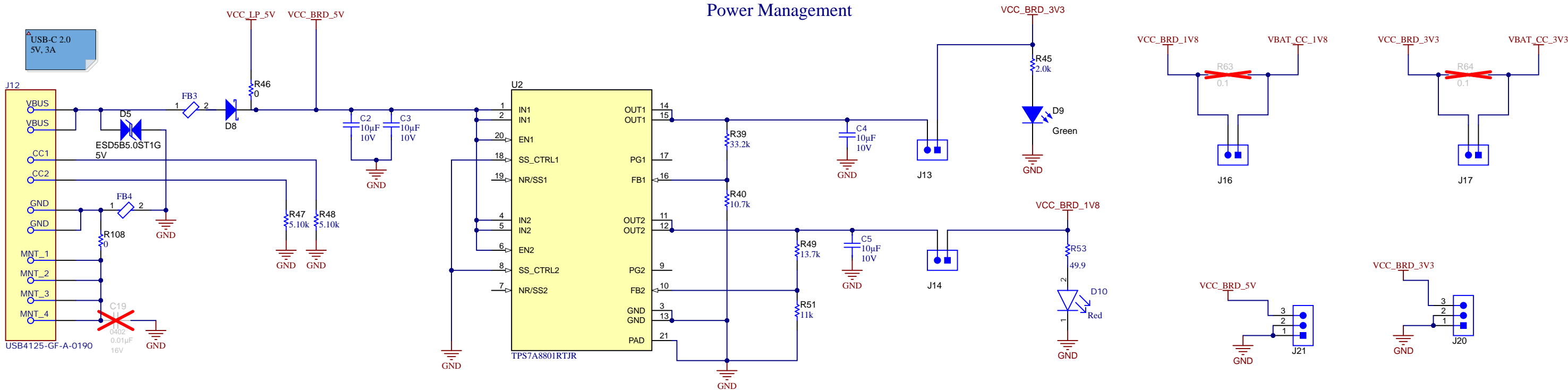
Accelerometer

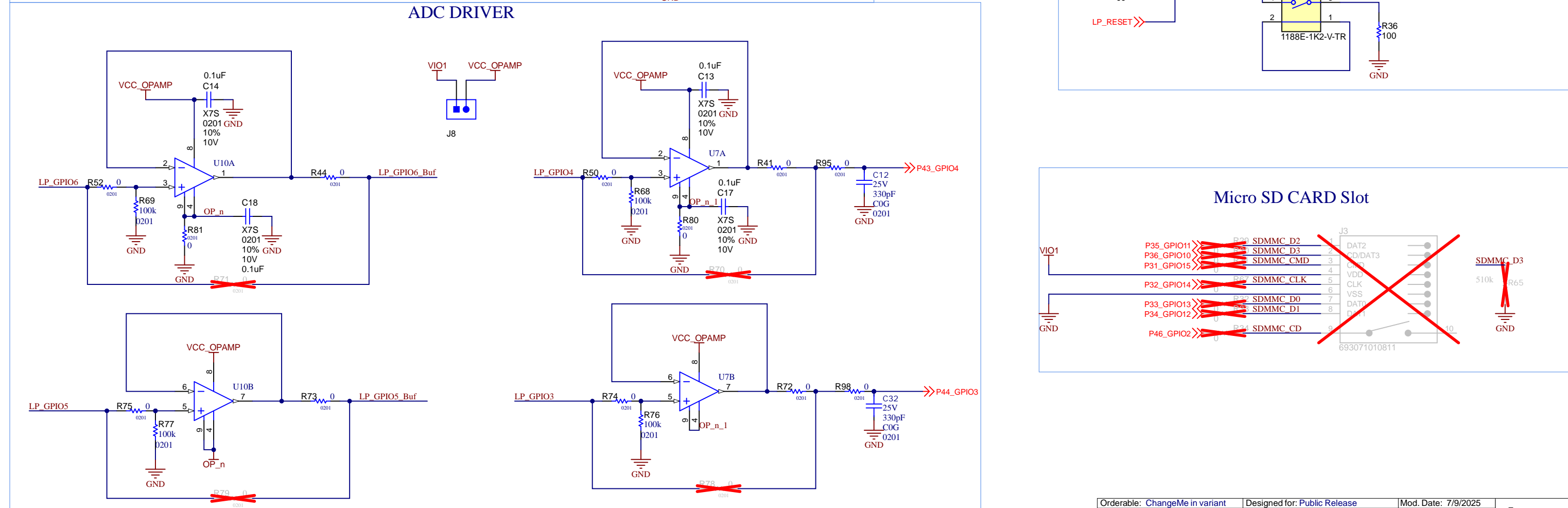
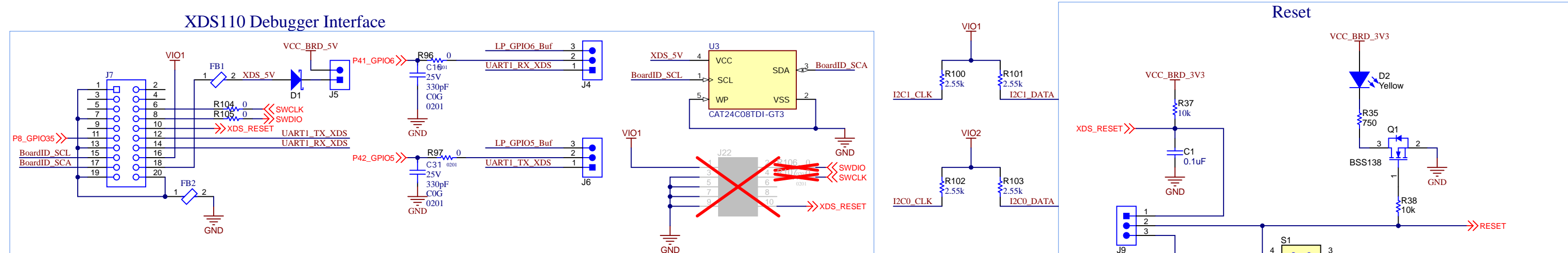
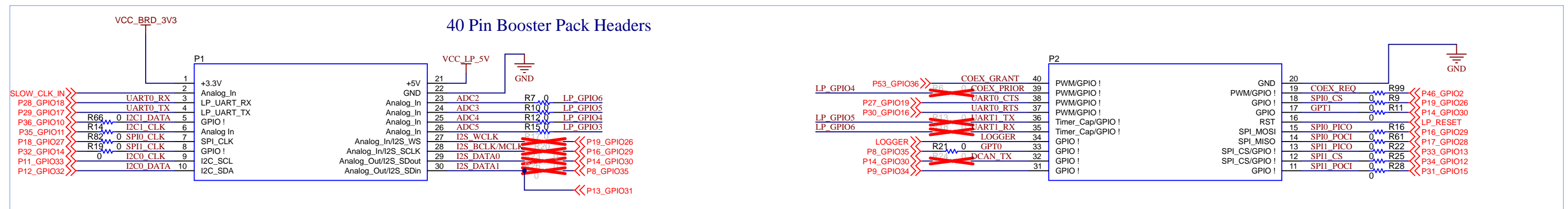


LED INDICATORS



Power Management





Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 7/9/2025	
TID #: N/A	Project Title: LP-EM-CC35X1		
Number: MCU-138	Rev: A	Sheet Title:	
SVN Rev: dcf40d21423ad72ac2a963659442ba50917580243		Sheet: 2 of 3	
Drawn By: Jonathan Cohen	File: MCU138A_HostInterface_SchDoc	Size: B	
Engineer: Jonathan Cohen	Contact: http://www.ti.com/support		http://www.ti.com © Texas Instruments 2025

A

A



Logo7
PCB
LOGO
LaunchPad Rocket

PCB Number: MCU-138
PCB Rev: A

PCB
LOGO
Texas Instruments

Logo8
PCB
LOGO
Texas Instruments

PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo

PCB
LOGO
ESD Susceptible

~~LBL1~~
~~THT-14-423-10~~
~~Size: 0.65" x 0.20 "~~

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.


ZZ3
Assembly Note
 These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note


These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

SH-J1




Place J4: 1-2

SH-J2




Place J5: 1-2

SH-J3




Place J6: 1-2

SH-J4




Place J8: 1-2

SH-J5




Place J9: 1-2

SH-J6




Place J10: 1-2

SH-J7




Place J11: 1-2

SH-J8




Place J13: 1-2

SH-J9




Place J14: 1-2

SH-J10




Place J15: 1-2

SH-J11




Place J16: 1-2

SH-J12




Place J17: 1-2

SH-J13



Place J18: 1-2

SH-J14



Place J19: 1-2