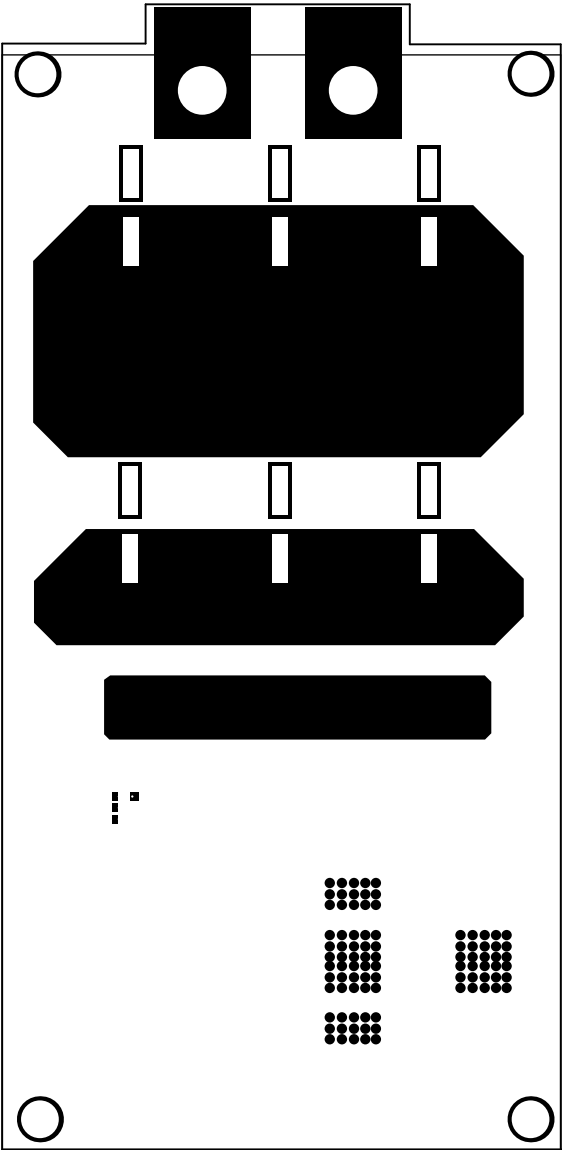
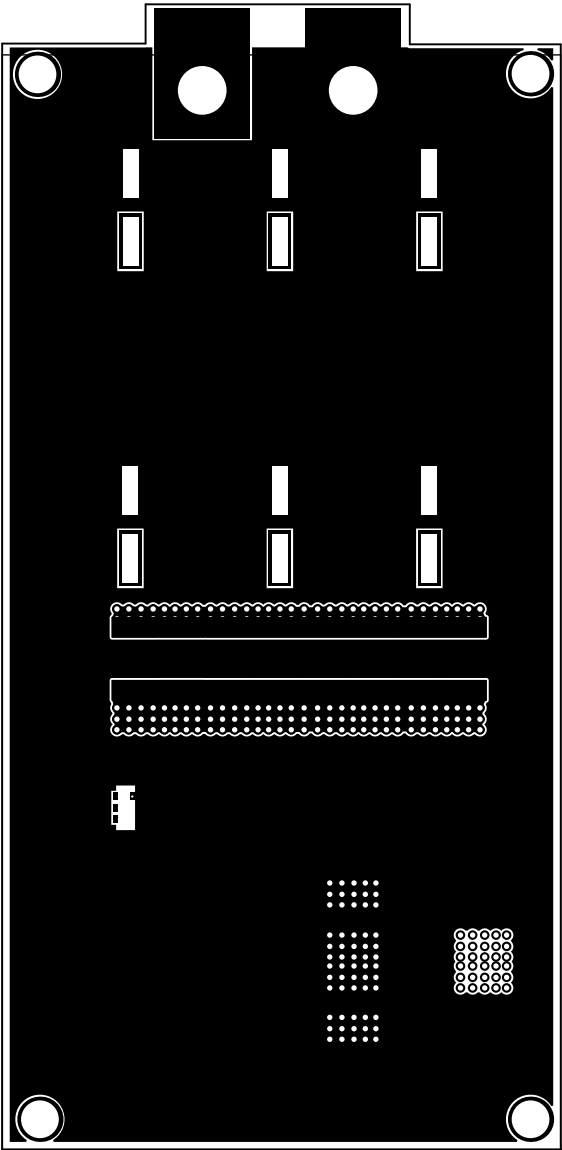


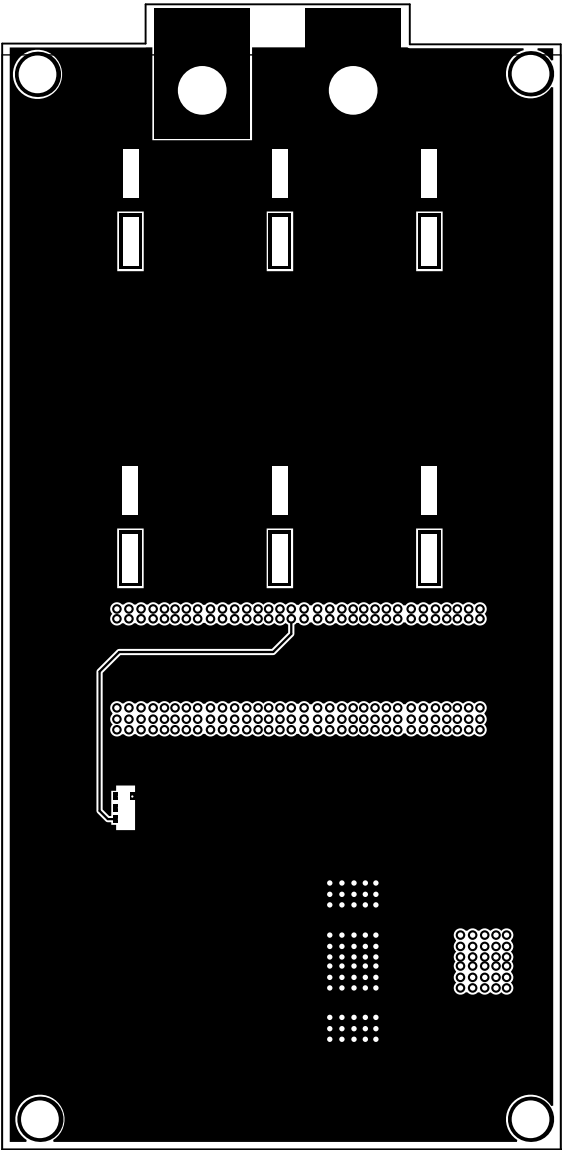
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 2025/10/7	ÀÛÀ 05:04:36	TEXAS INSTRUMENTS



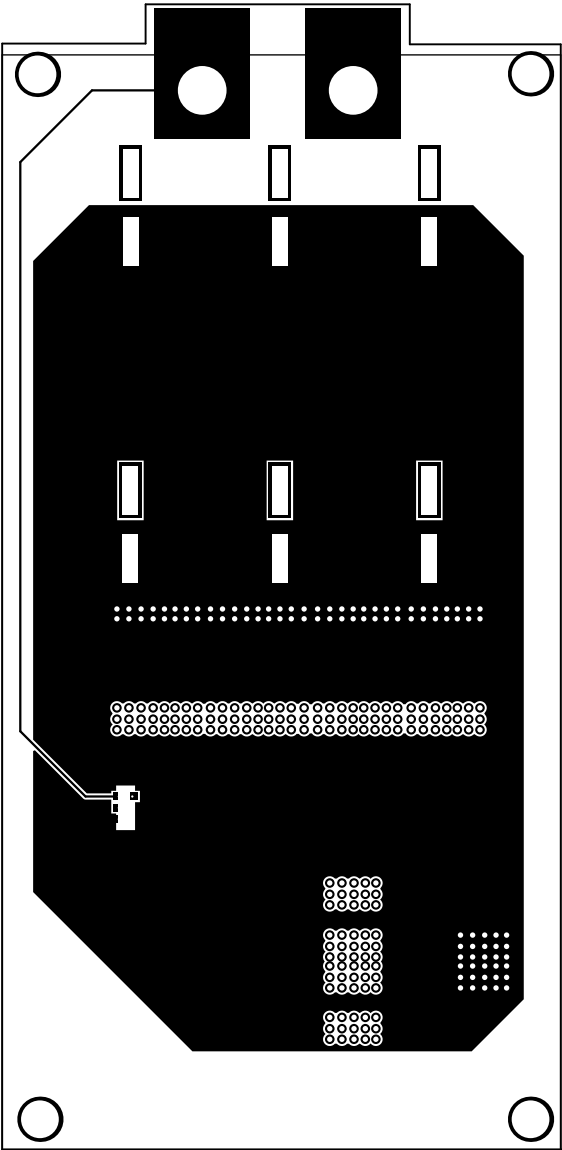
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME = Top Solder	TID #: N/A		
PLOT NAME = Top Solder Mask	GENERATED : 2025/10/7 10:05:04:36	TEXAS INSTRUMENTS	



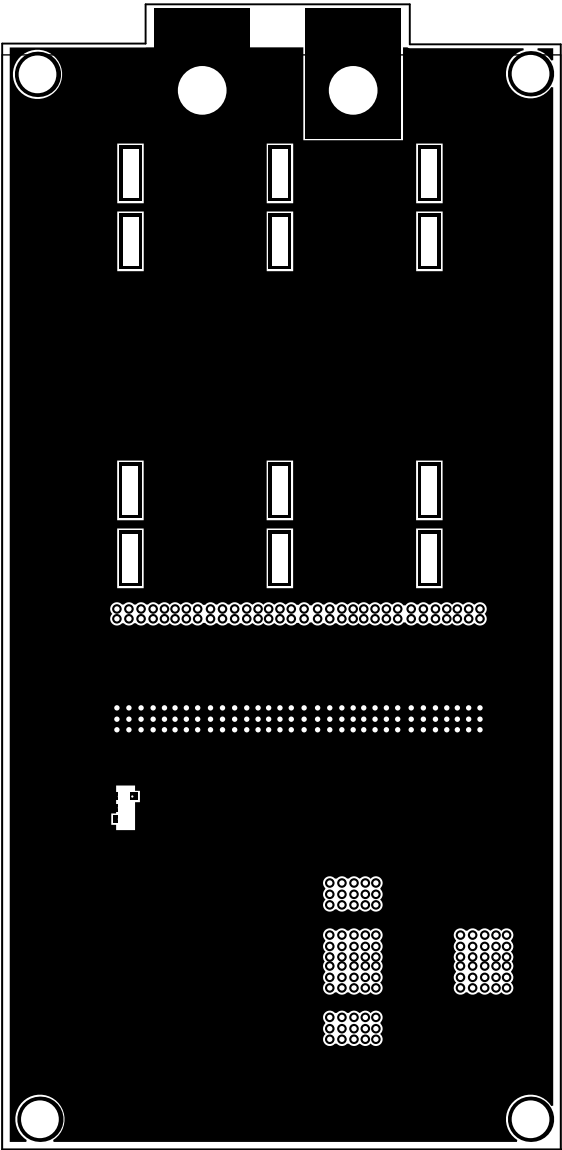
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME = Top Layer	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 2025/10/7	ÀÛÀ 05:04:36	TEXAS INSTRUMENTS



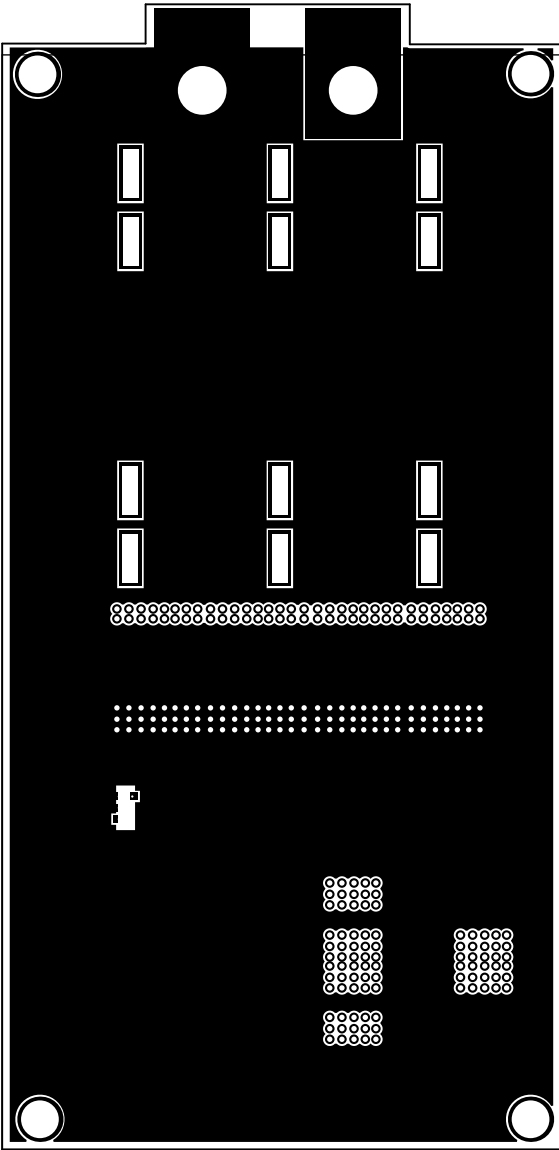
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal Layer 1	GENERATED : 2025/10/7 10:05:04:36	TEXAS INSTRUMENTS	



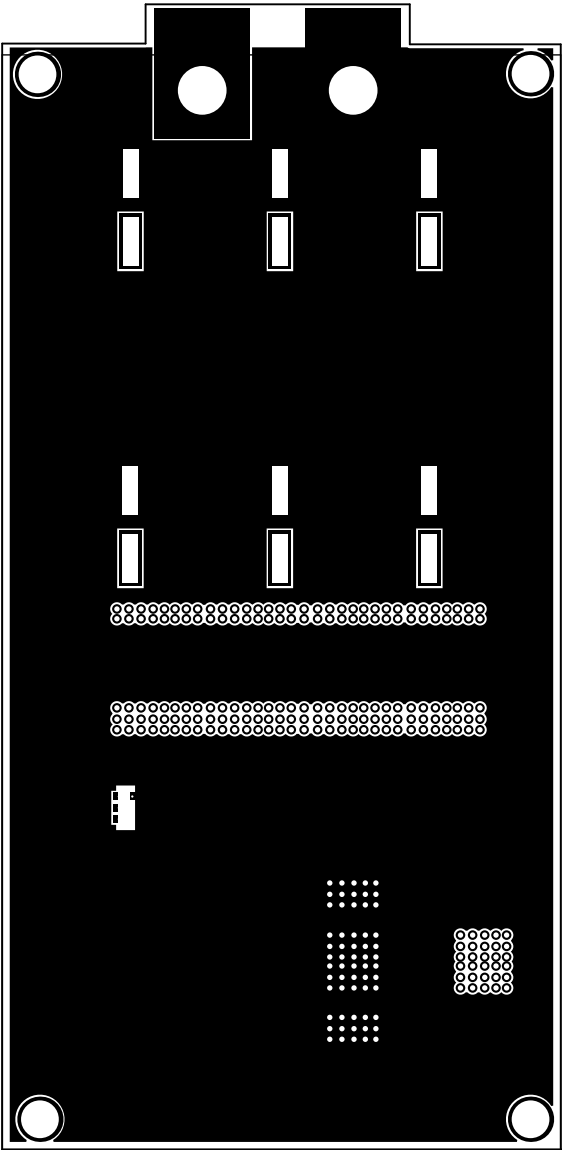
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal Layer 2	GENERATED : 2025/10/7	ÀÛÀ 05:04:36	TEXAS INSTRUMENTS



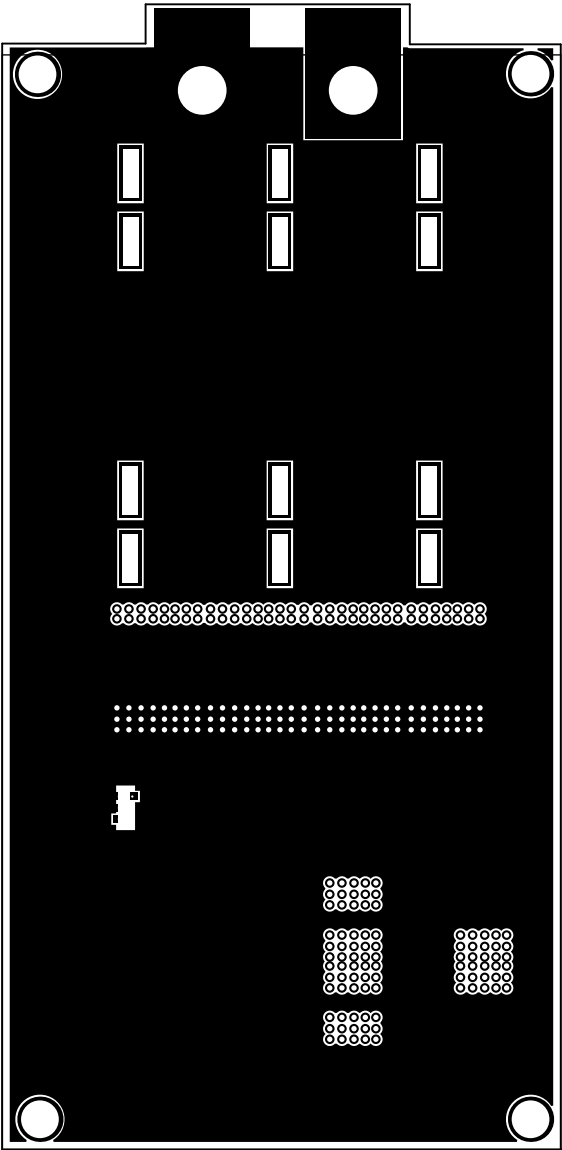
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal Layer 3	GENERATED : 2025/10/7	ÀÛÀ 05:04:36	TEXAS INSTRUMENTS



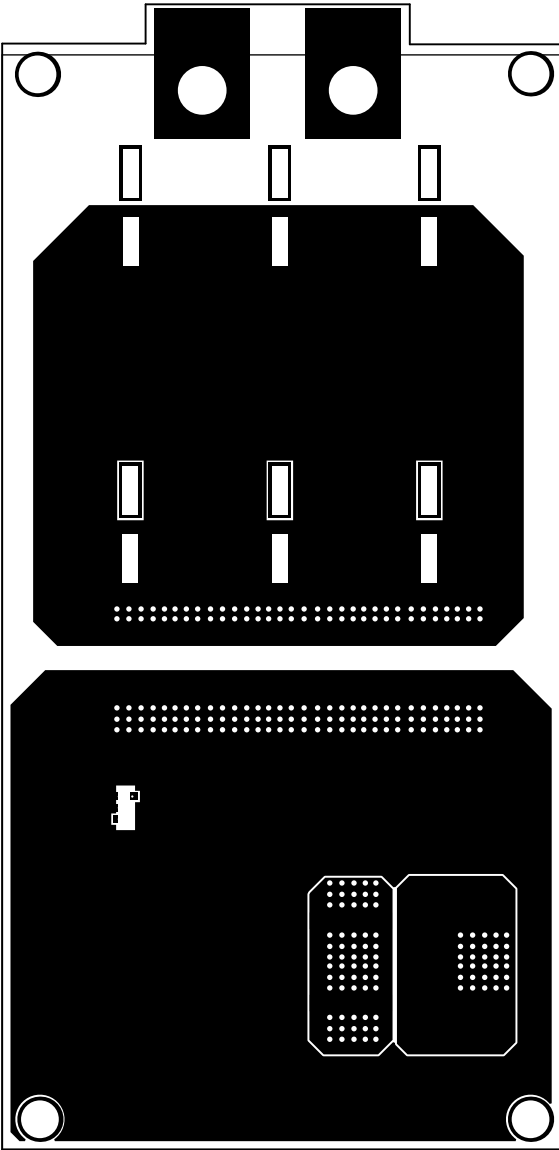
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal Layer 4	GENERATED : 2025/10/7	ÀUÀ 05:04:36	TEXAS INSTRUMENTS



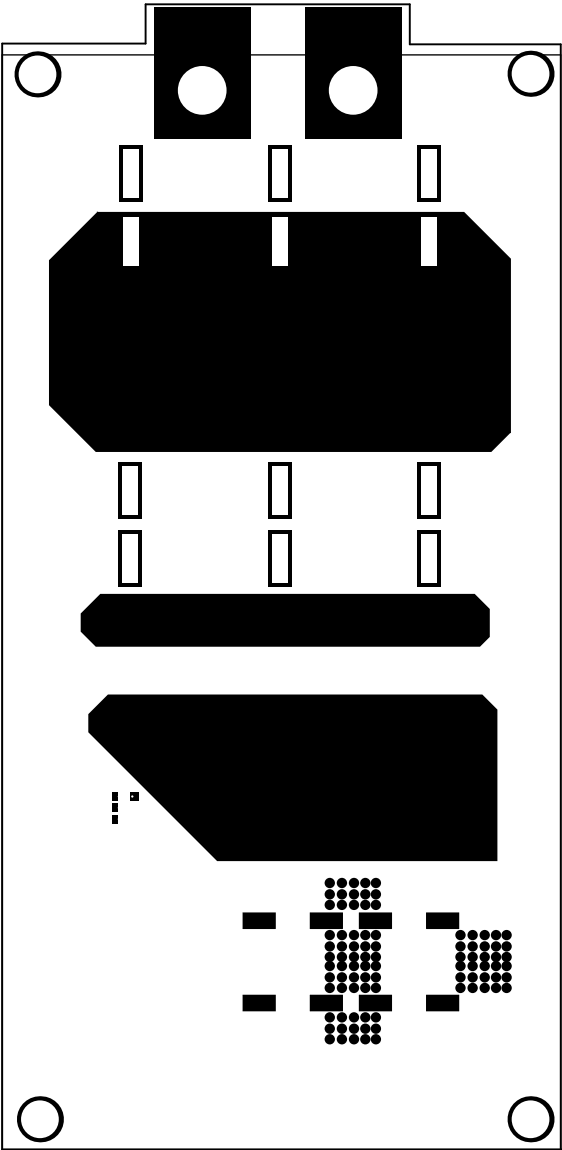
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal Layer 5	GENERATED : 2025/10/7	시간 05:04:36	TEXAS INSTRUMENTS



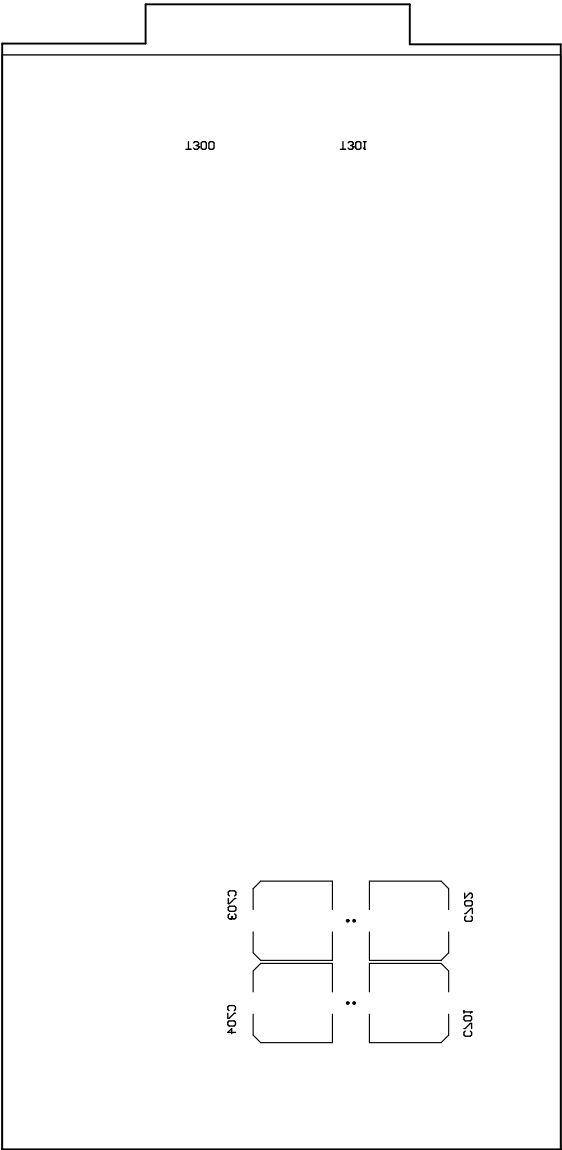
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal Layer 6	GENERATED : 2025/10/7	ÀÛÀ 05:04:36	TEXAS INSTRUMENTS



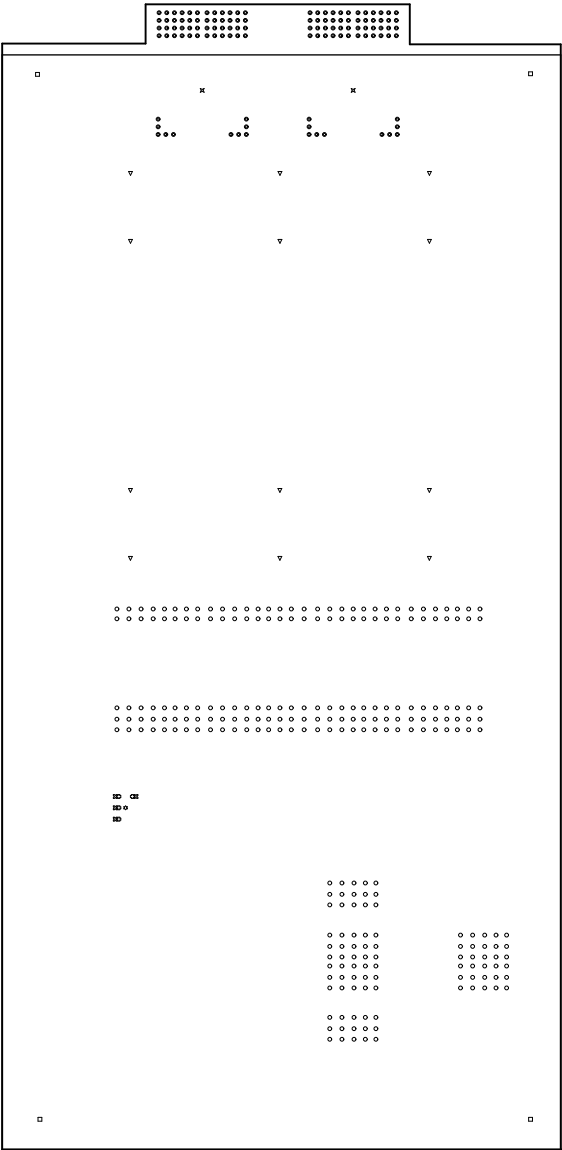
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME = Bottom Layer	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 2025/10/7	ÀÛÀ 05:04:37	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 2025/10/7	ÀÛÀ 05:04:37	TEXAS INSTRUMENTS



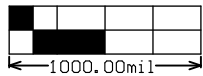
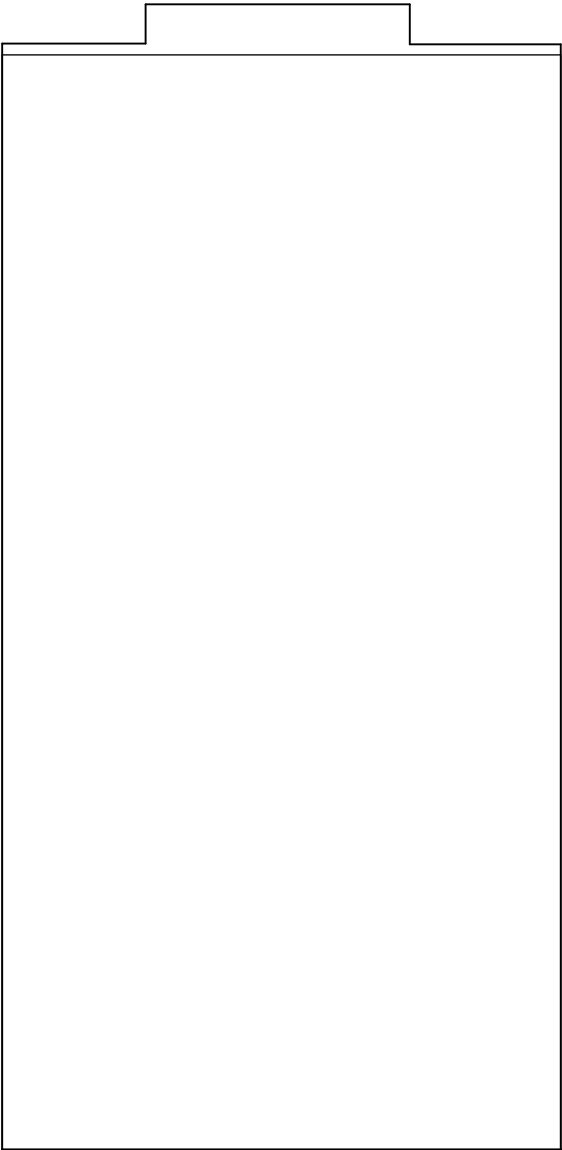
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 2025/10/7	ÀÛÀ 05:04:37	TEXAS INSTRUMENTS



Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
⊗	4	10.00mil (0.254mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	4	11.81mil (0.300mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	116	16.00mil (0.406mm)	PTH	Round	Top Layer - Bottom Layer	
○	250	28.00mil (0.711mm)	PTH	Round	Top Layer - Bottom Layer	
☆	1	78.74mil (2.000mm)	NPTH	Rectangle	Top Layer - Bottom Layer	
▽	12	78.74mil (2.000mm)	NPTH	Rectangle	Top Layer - Bottom Layer	
□	4	196.85mil (5.000mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	2	254.00mil (6.452mm)	PTH	Round	Top Layer - Bottom Layer	
393 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 2025/10/7	ÀÛŕ 05:04:37	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41155	REV: E1	SUN REV: Not in version control
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED : 2025/10/7 05:04:37	TEXAS INSTRUMENTS	