

**Buttons, Switches,
LEDs**

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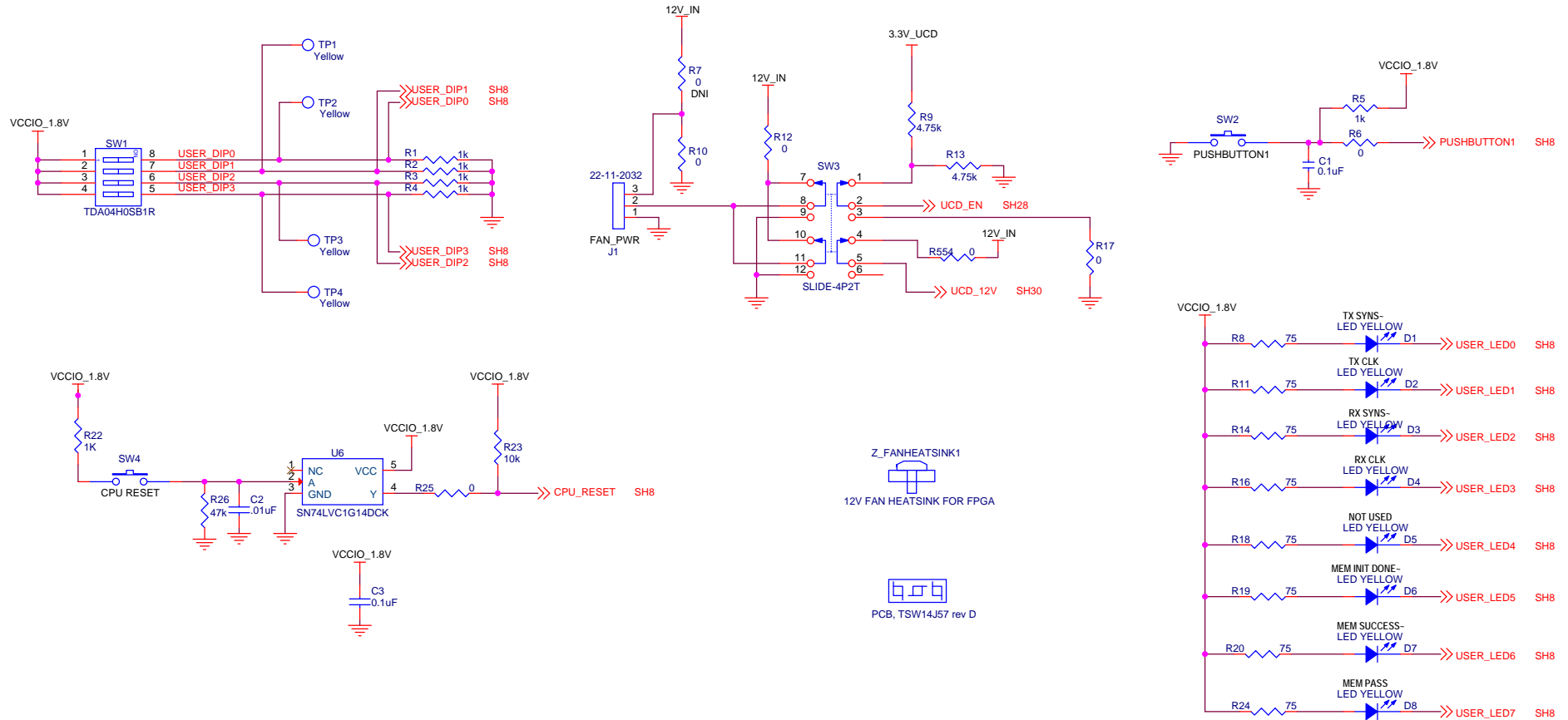
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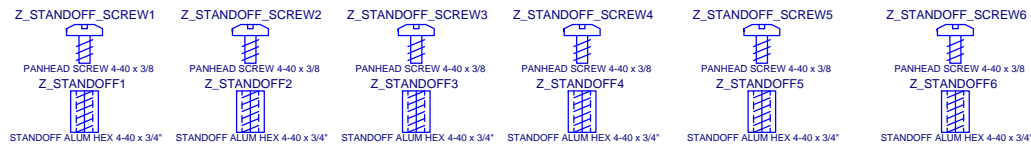
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Drawn: L. NGUYEN	Size B	Document Number	Rev D
Engineer: J. SETON / E. DWOBENG			
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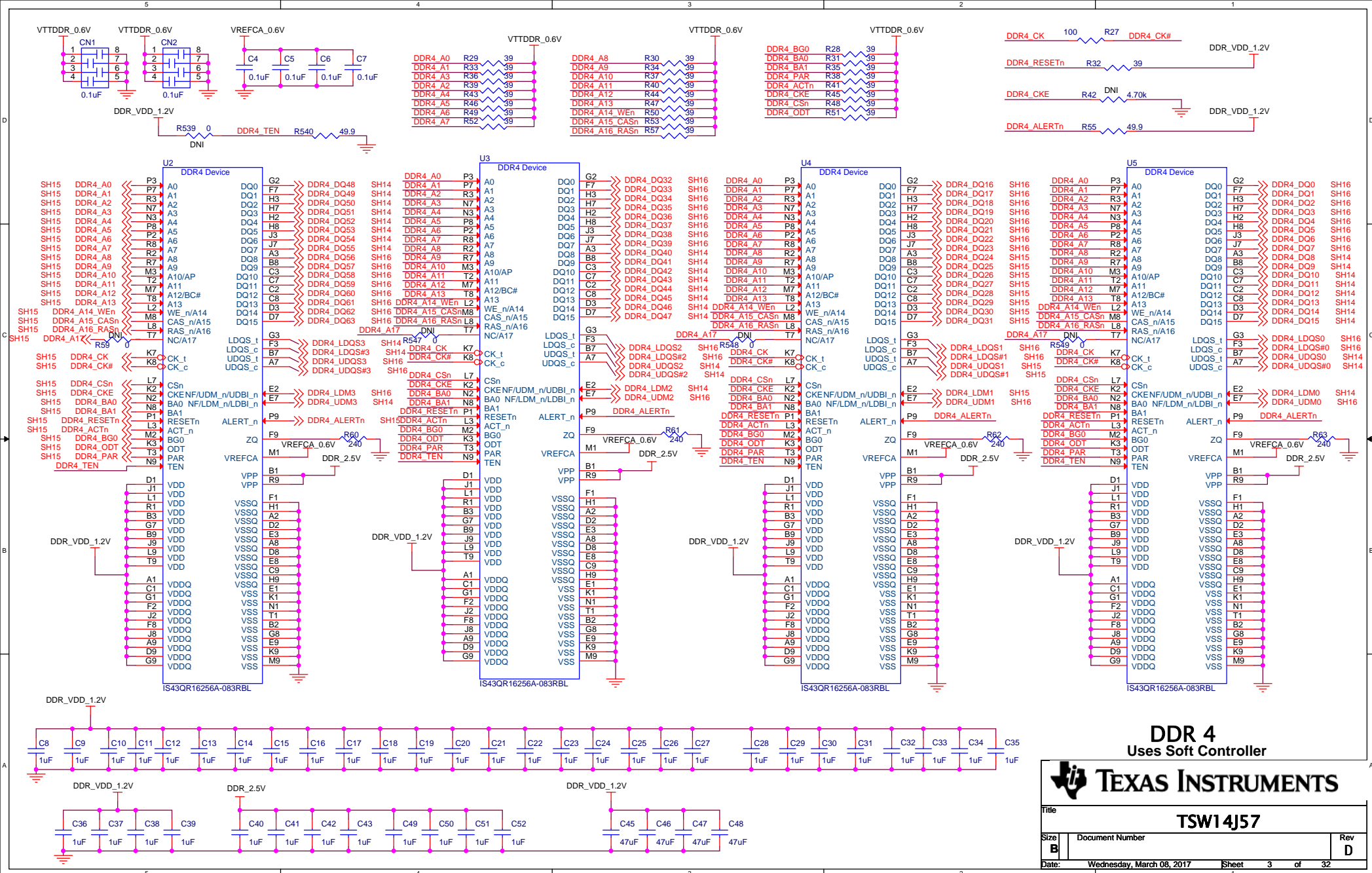
Buttons, Switches, LEDs

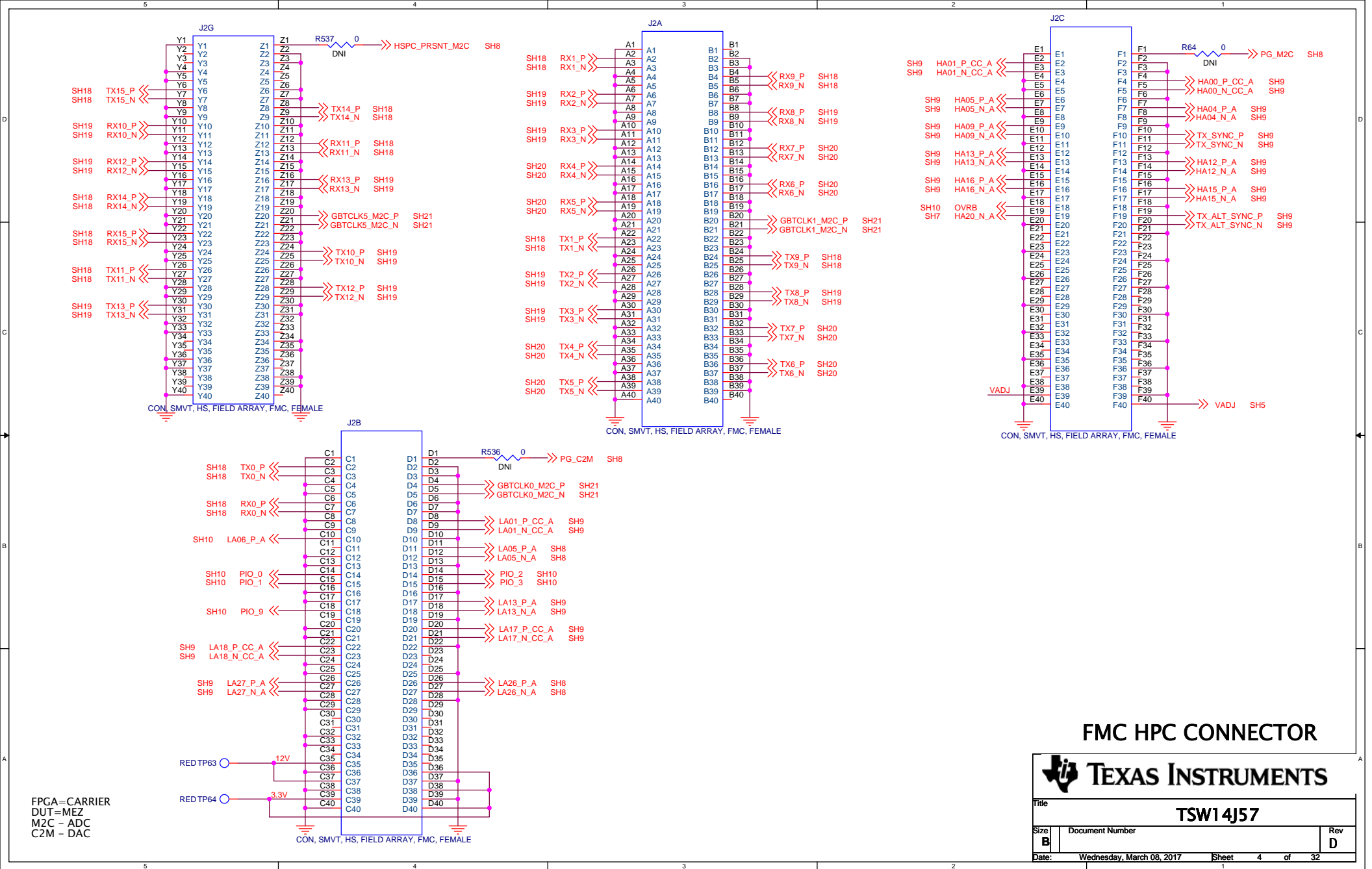


MECHANICAL PARTS




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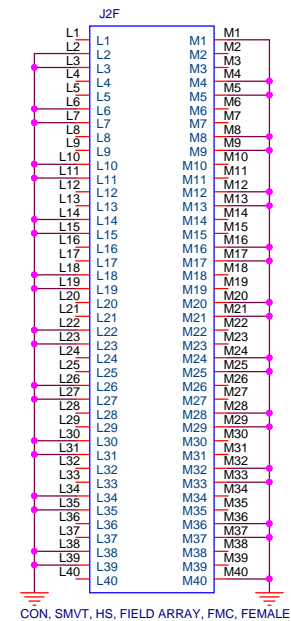
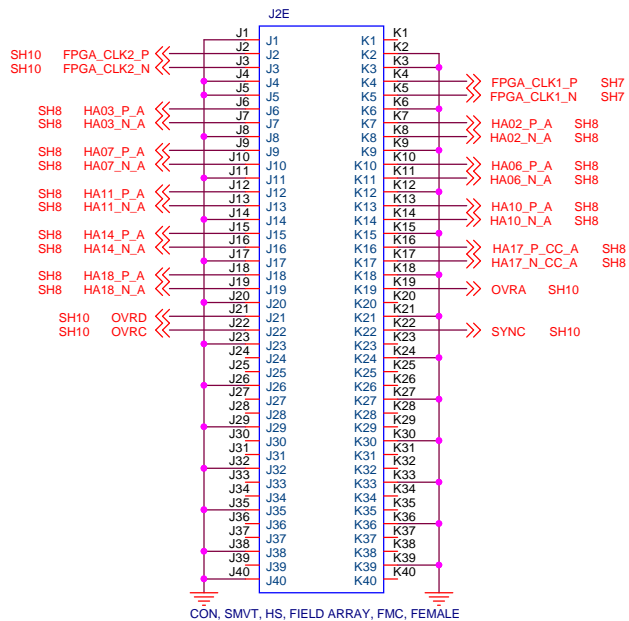
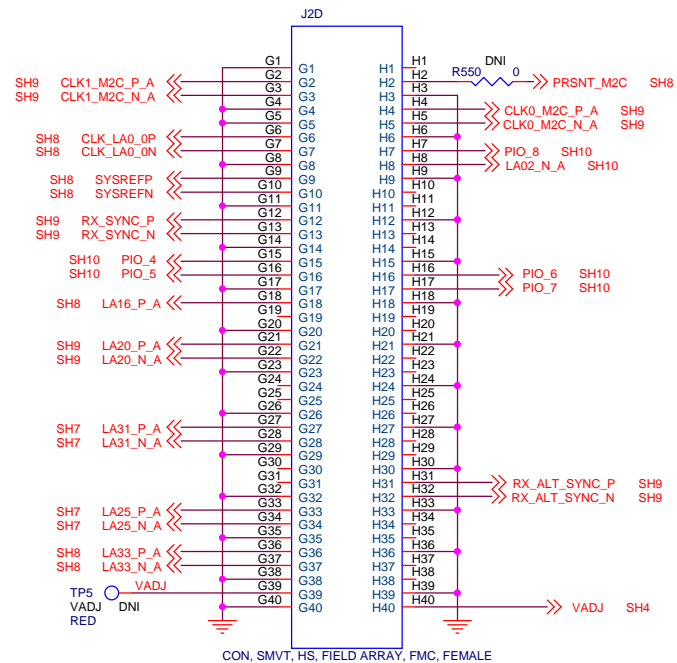


FMC HPC CONNECTOR



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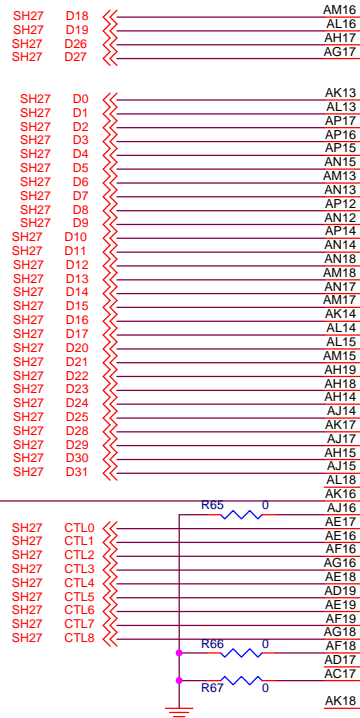
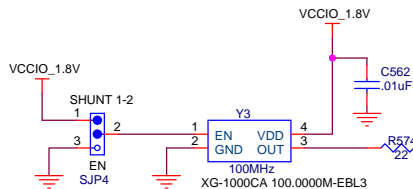
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FMC HPC CONNECTOR



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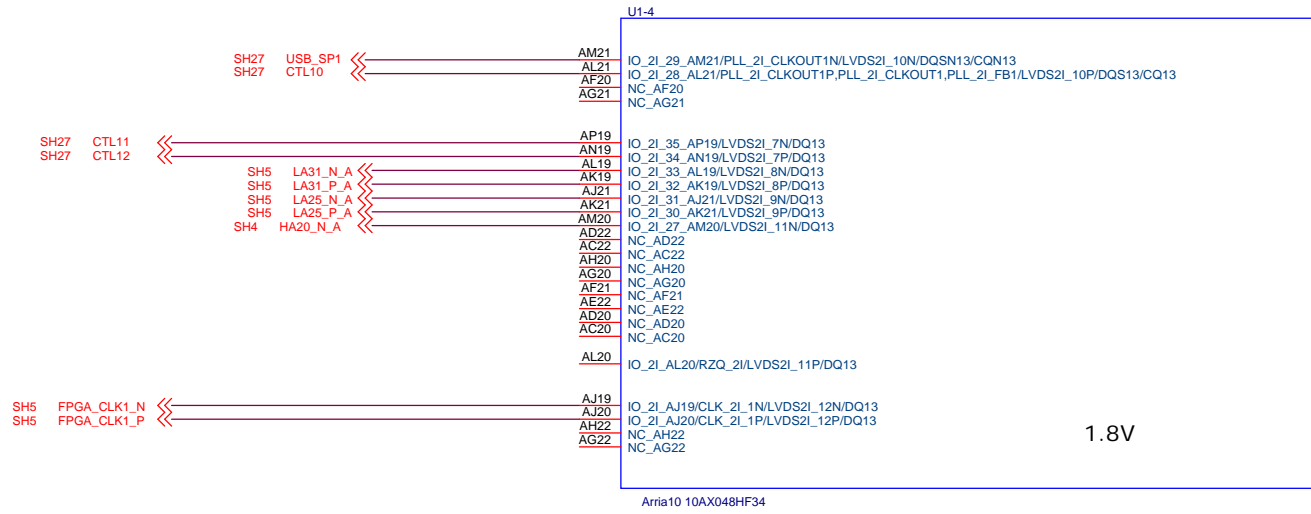


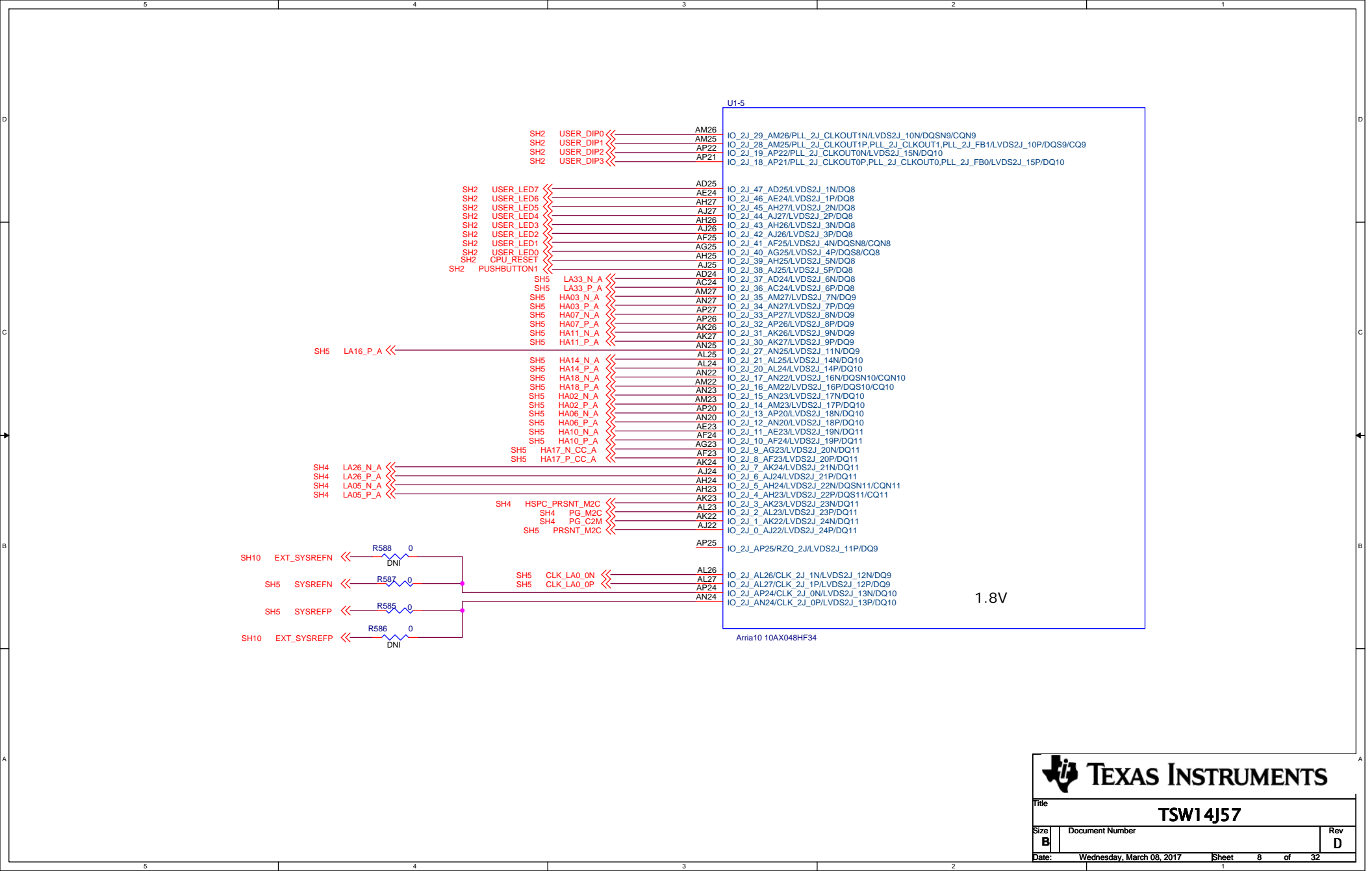
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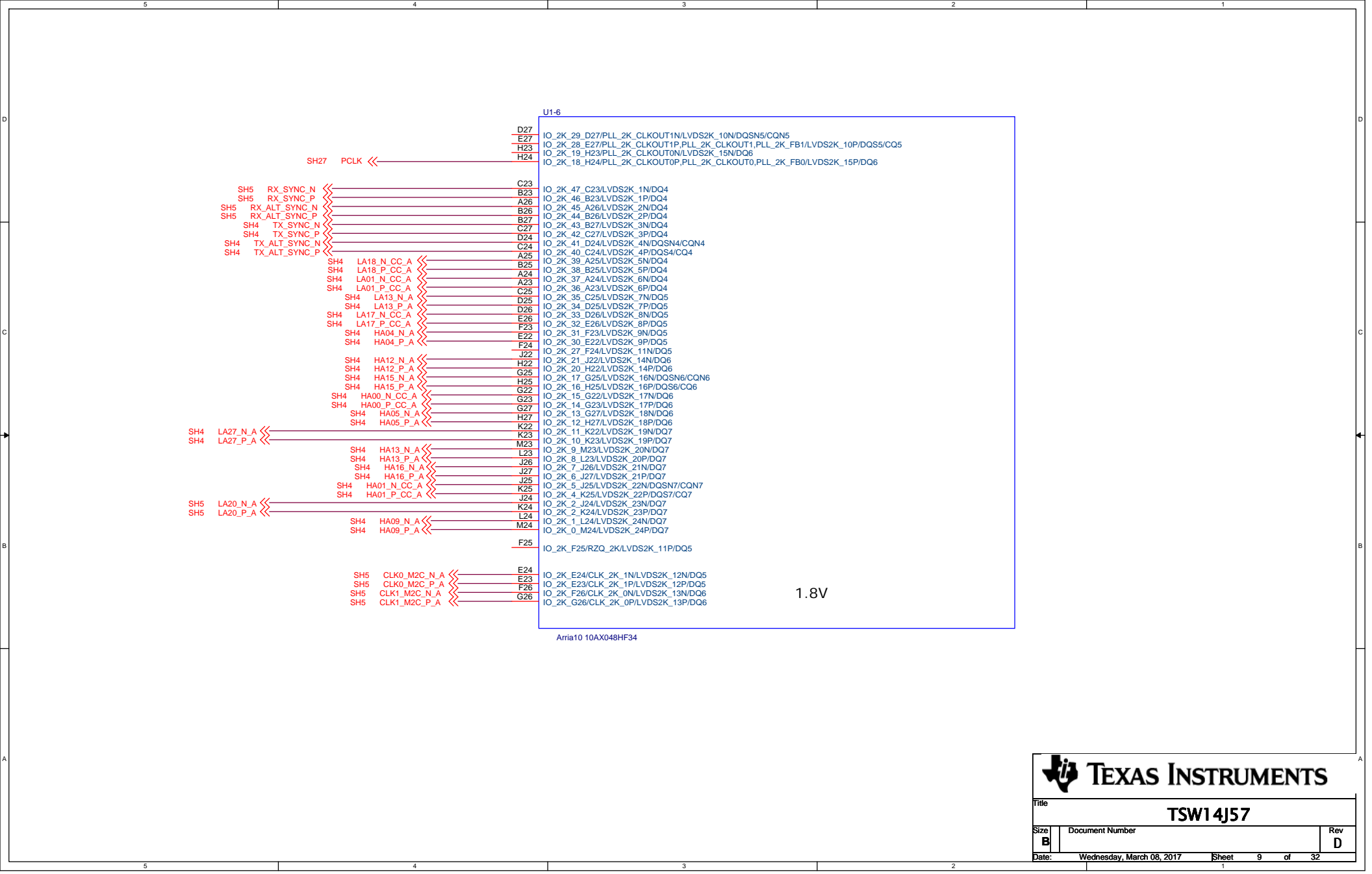
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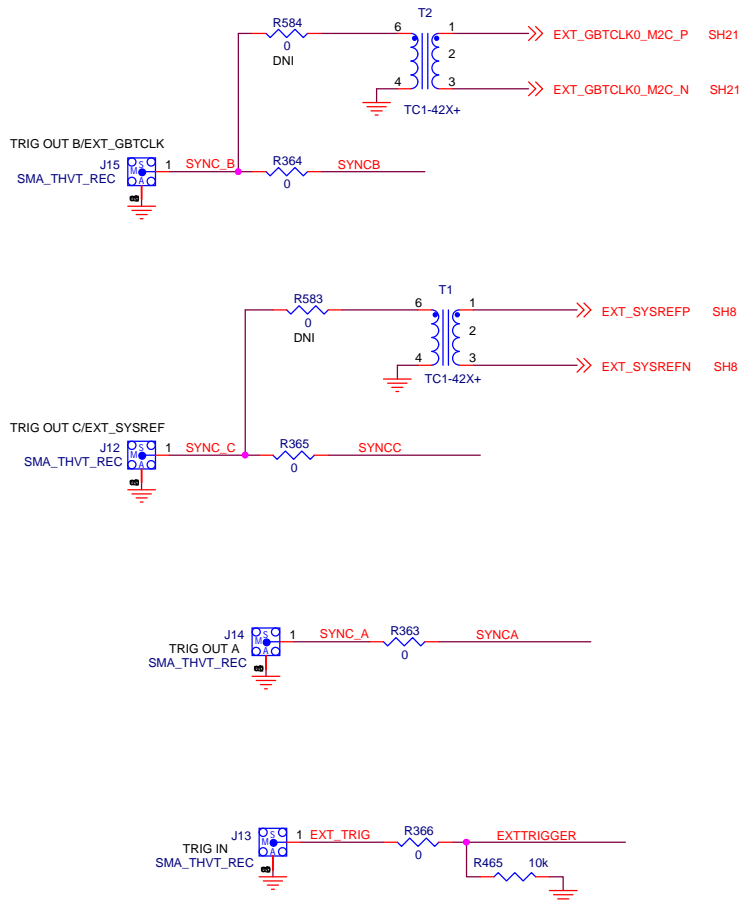
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1.8V (VAR)

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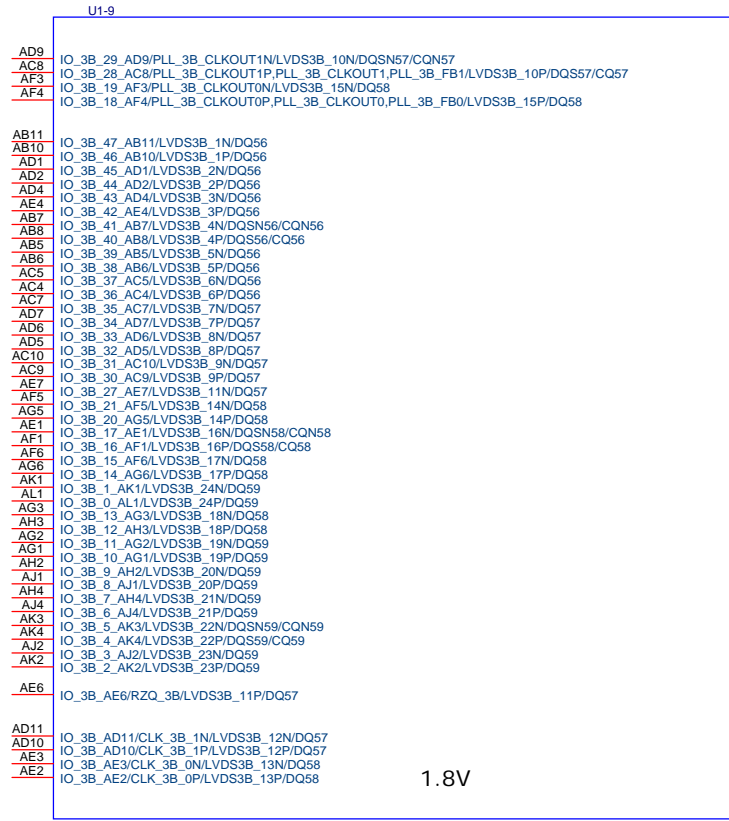


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U1-10

~~T1~~
~~U1~~ IO_3C_29_T1/PLL_3C_CLKOUT1N/LVDS3C_10N/DQSN53/CQN53
~~W5~~ IO_3C_28_U1/PLL_3C_CLKOUT1P/PLL_3C_CLKOUT1P/PLL_3C_FB1/LVDS3C_10P/DQS53/CQ53
~~W4~~ IO_3C_19_W5/PLL_3C_CLKOUT0N/LVDS3C_15N/DQ54
IO_3C_18_W4/PLL_3C_CLKOUT0P/PLL_3C_CLKOUT0P/PLL_3C_FB0/LVDS3C_15P/DQ54

~~R4~~
~~T4~~ IO_3C_47_R4/LVDS3C_1N/DQ52
~~P5~~ IO_3C_46_T4/LVDS3C_1P/DQ52
~~P4~~ IO_3C_45_P5/LVDS3C_2N/DQ52
~~R3~~ IO_3C_44_P4/LVDS3C_2P/DQ52
~~T3~~ IO_3C_43_R3/LVDS3C_3N/DQ52
~~T6~~ IO_3C_42_T3/LVDS3C_3P/DQ52
~~T5~~ IO_3C_41_T6/LVDS3C_4N/DQSN52/CQN52
~~U5~~ IO_3C_40_T5/LVDS3C_4P/DQS52/CQ52
~~U6~~ IO_3C_39_U5/LVDS3C_5N/DQ52
~~V4~~ IO_3C_38_U6/LVDS3C_5P/DQ52
~~V5~~ IO_3C_37_V4/LVDS3C_6N/DQ52
~~R2~~ IO_3C_36_V5/LVDS3C_6P/DQ52
~~P2~~ IO_3C_35_R2/LVDS3C_7N/DQ53
~~P1~~ IO_3C_34_P2/LVDS3C_7P/DQ53
~~R1~~ IO_3C_33_P1/LVDS3C_8N/DQ53
~~V3~~ IO_3C_32_R1/LVDS3C_8P/DQ53
~~U3~~ IO_3C_31_V3/LVDS3C_9N/DQ53
~~U2~~ IO_3C_30_U3/LVDS3C_9P/DQ53
~~Y9~~ IO_3C_27_U2/LVDS3C_11N/DQ53
~~Y8~~ IO_3C_21_Y9/LVDS3C_14N/DQ54
~~W7~~ IO_3C_20_Y8/LVDS3C_14P/DQ54
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~~AA8~~ IO_3C_15_AA9/LVDS3C_17N/DQ54
~~AA6~~ IO_3C_14_AA8/LVDS3C_17P/DQ54
~~AA5~~ IO_3C_13_AA6/LVDS3C_18N/DQ54
~~Y4~~ IO_3C_12_AA5/LVDS3C_18P/DQ54
~~Y3~~ IO_3C_11_Y4/LVDS3C_19N/DQ55
~~AA4~~ IO_3C_10_Y3/LVDS3C_19P/DQ55
~~AA3~~ IO_3C_9_AA4/LVDS3C_20N/DQ55
~~AB3~~ IO_3C_8_AA3/LVDS3C_20P/DQ55
~~AB2~~ IO_3C_7_AB3/LVDS3C_21N/DQ55
~~AC2~~ IO_3C_6_AB2/LVDS3C_21P/DQ55
~~AC3~~ IO_3C_5_AC2/LVDS3C_22N/DQSN55/CQN55
~~Y2~~ IO_3C_4_AC3/LVDS3C_22P/DQS55/CQ55
~~Y1~~ IO_3C_3_Y2/LVDS3C_23N/DQ55
~~AA1~~ IO_3C_2_Y1/LVDS3C_23P/DQ55
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IO_3C_0_AB1/LVDS3C_24P/DQ55

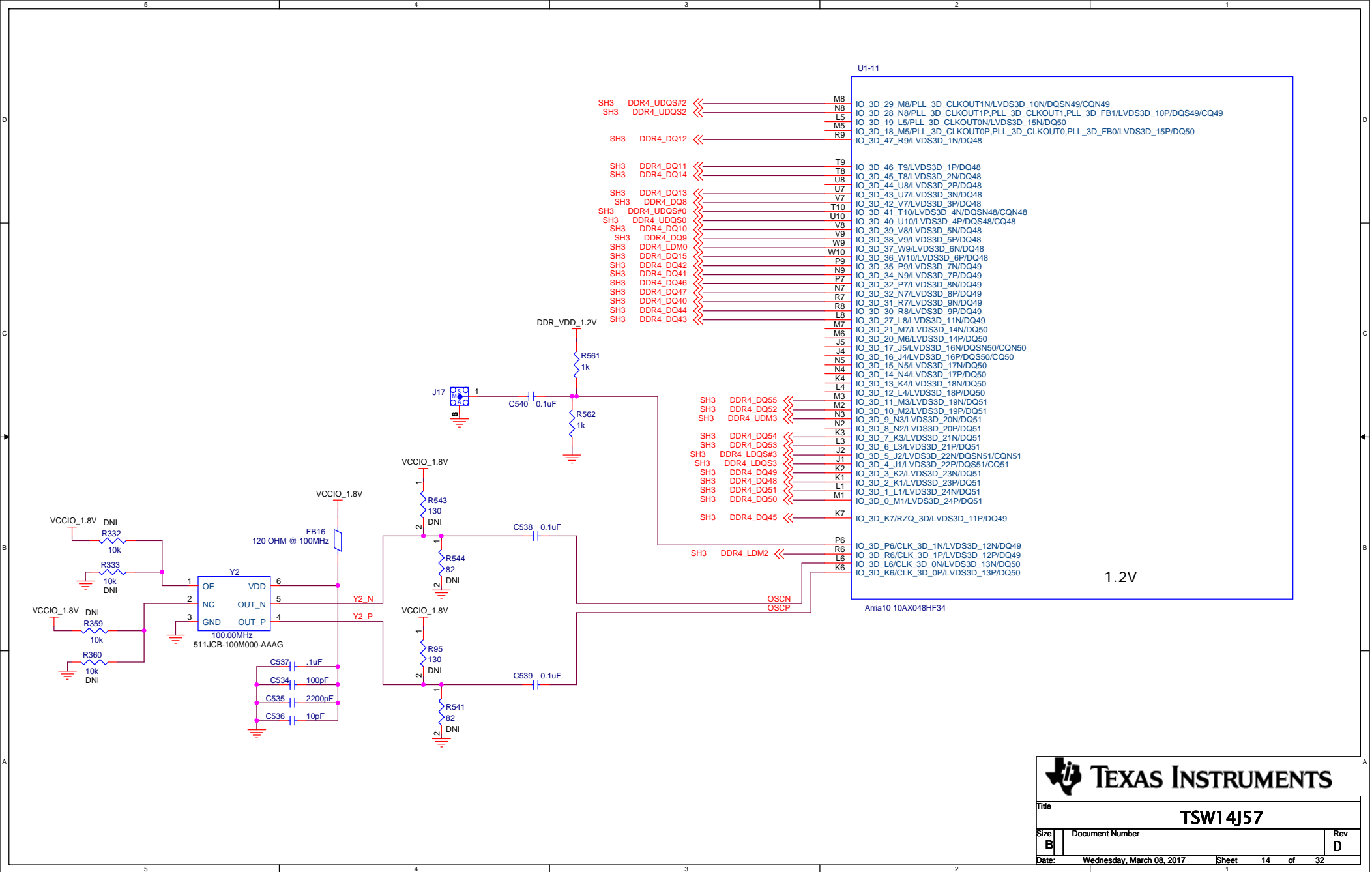
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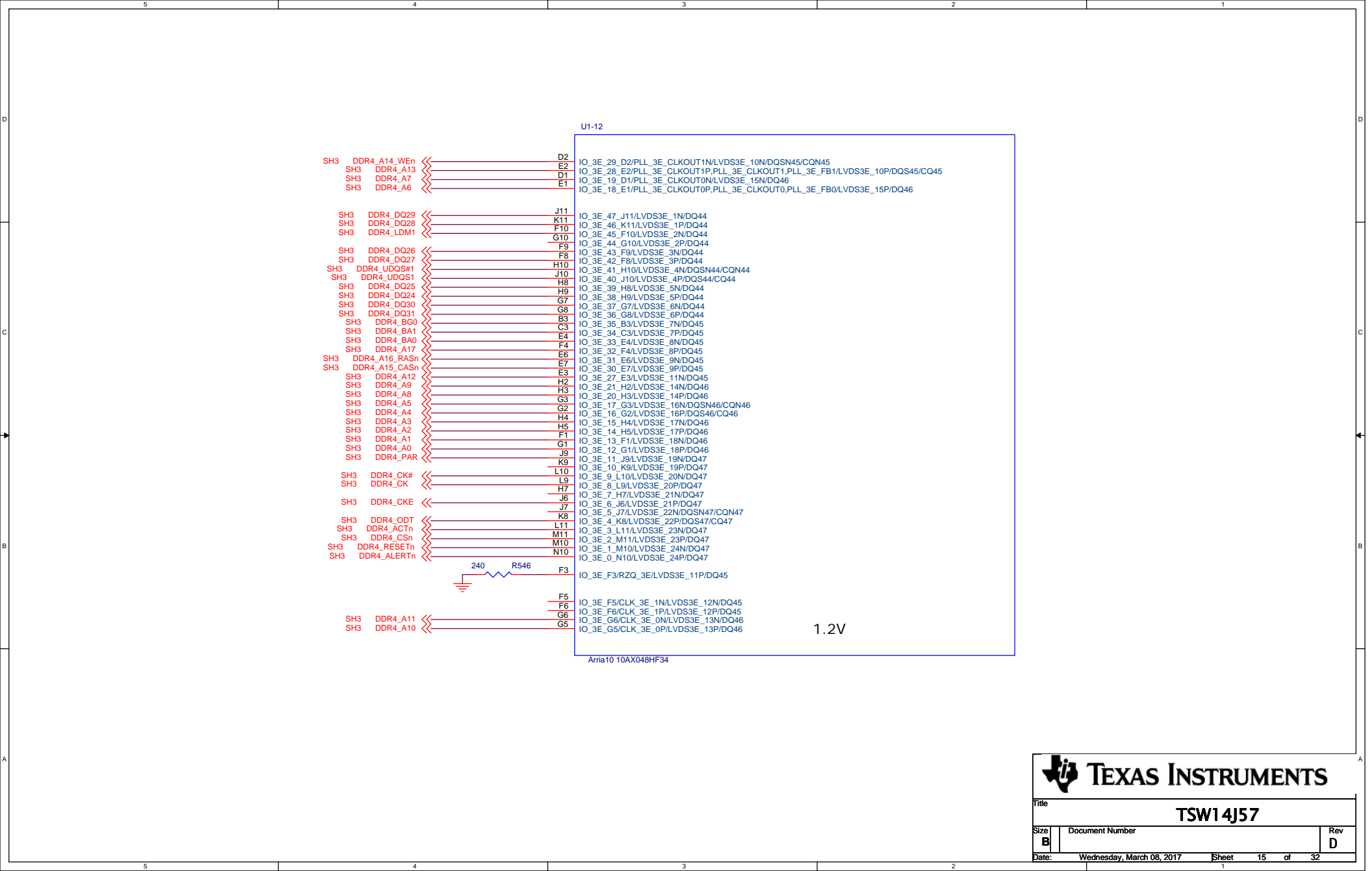
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~~W1~~ IO_3C_W1/CLK_3C_1P/LVDS3C_12P/DQ53
~~Y7~~ IO_3C_Y7/CLK_3C_0N/LVDS3C_13N/DQ54
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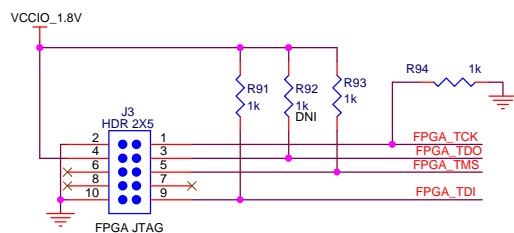
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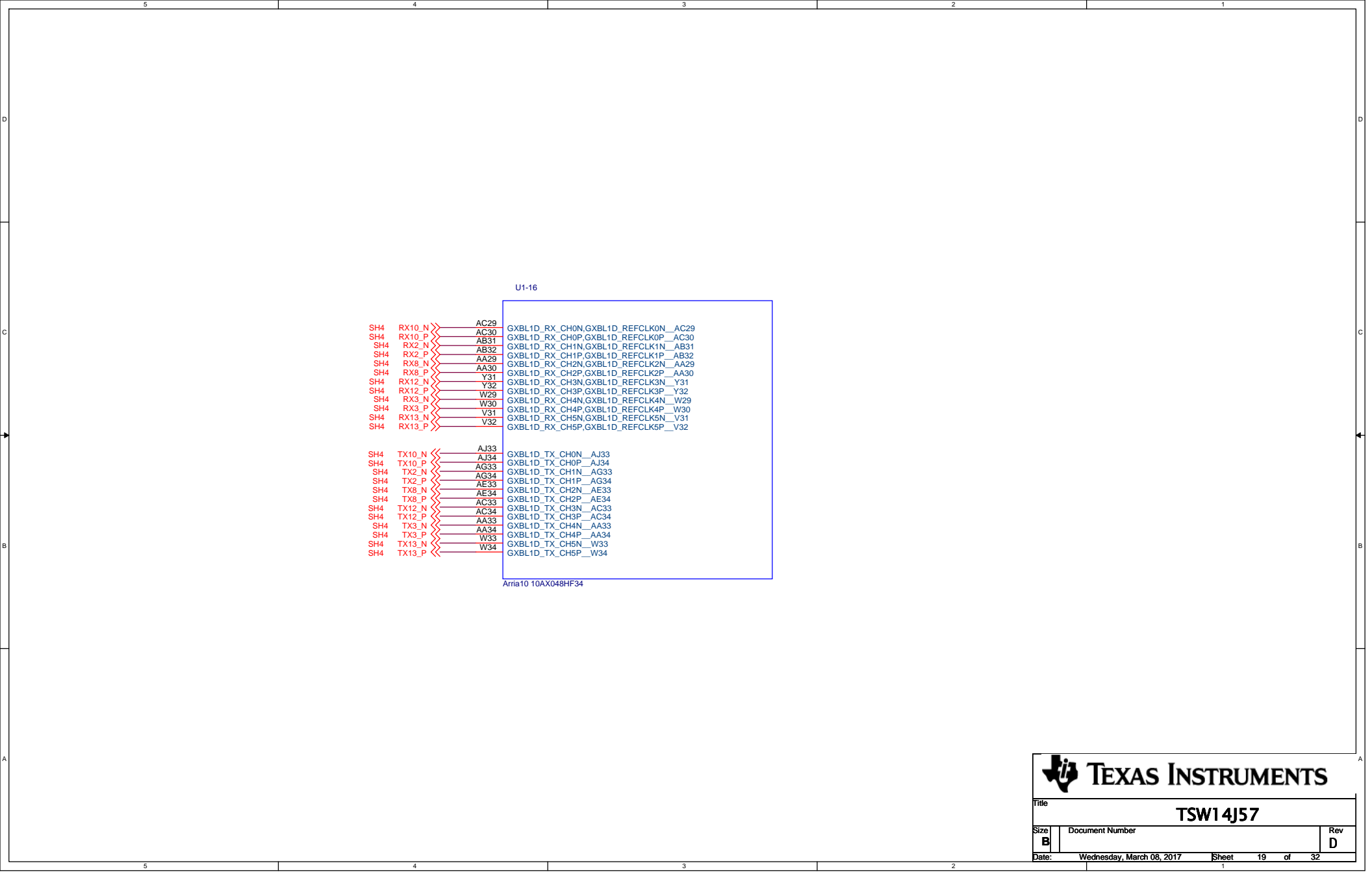
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SH4	RX1_N	>>	AJ29	GXBL1C_RX_CH1N,GXBL1C_REFCLK1N__AJ29
SH4	RX1_P	>>	AJ30	GXBL1C_RX_CH1P,GXBL1C_REFCLK1P__AJ30
SH4	RX9_N	>>	AG29	GXBL1C_RX_CH2N,GXBL1C_REFCLK2N__AG29
SH4	RX9_P	>>	AG30	GXBL1C_RX_CH2P,GXBL1C_REFCLK2P__AG30
SH4	RX11_N	>>	AF31	GXBL1C_RX_CH3N,GXBL1C_REFCLK3N__AF31
SH4	RX11_P	>>	AF32	GXBL1C_RX_CH3P,GXBL1C_REFCLK3P__AF32
SH4	RX15_N	>>	AE29	GXBL1C_RX_CH4N,GXBL1C_REFCLK4N__AE29
SH4	RX15_P	>>	AE30	GXBL1C_RX_CH4P,GXBL1C_REFCLK4P__AE30
SH4	RX14_N	>>	AD31	GXBL1C_RX_CH5N,GXBL1C_REFCLK5N__AD31
SH4	RX14_P	>>	AD32	GXBL1C_RX_CH5P,GXBL1C_REFCLK5P__AD32
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SH4	TX0_P	<<	AP32	GXBL1C_TX_CH0P__AP32
SH4	TX1_N	<<	AM31	GXBL1C_TX_CH1N__AM31
SH4	TX1_P	<<	AM32	GXBL1C_TX_CH1P__AM32
SH4	TX9_N	<<	AK31	GXBL1C_TX_CH2N__AK31
SH4	TX9_P	<<	AK32	GXBL1C_TX_CH2P__AK32
SH4	TX11_N	<<	AH31	GXBL1C_TX_CH3N__AH31
SH4	TX11_P	<<	AH32	GXBL1C_TX_CH3P__AH32
SH4	TX15_N	<<	AN33	GXBL1C_TX_CH4N__AN33
SH4	TX15_P	<<	AN34	GXBL1C_TX_CH4P__AN34
SH4	TX14_N	<<	AL33	GXBL1C_TX_CH5N__AL33
SH4	TX14_P	<<	AL34	GXBL1C_TX_CH5P__AL34


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HIGH SPEED BANK



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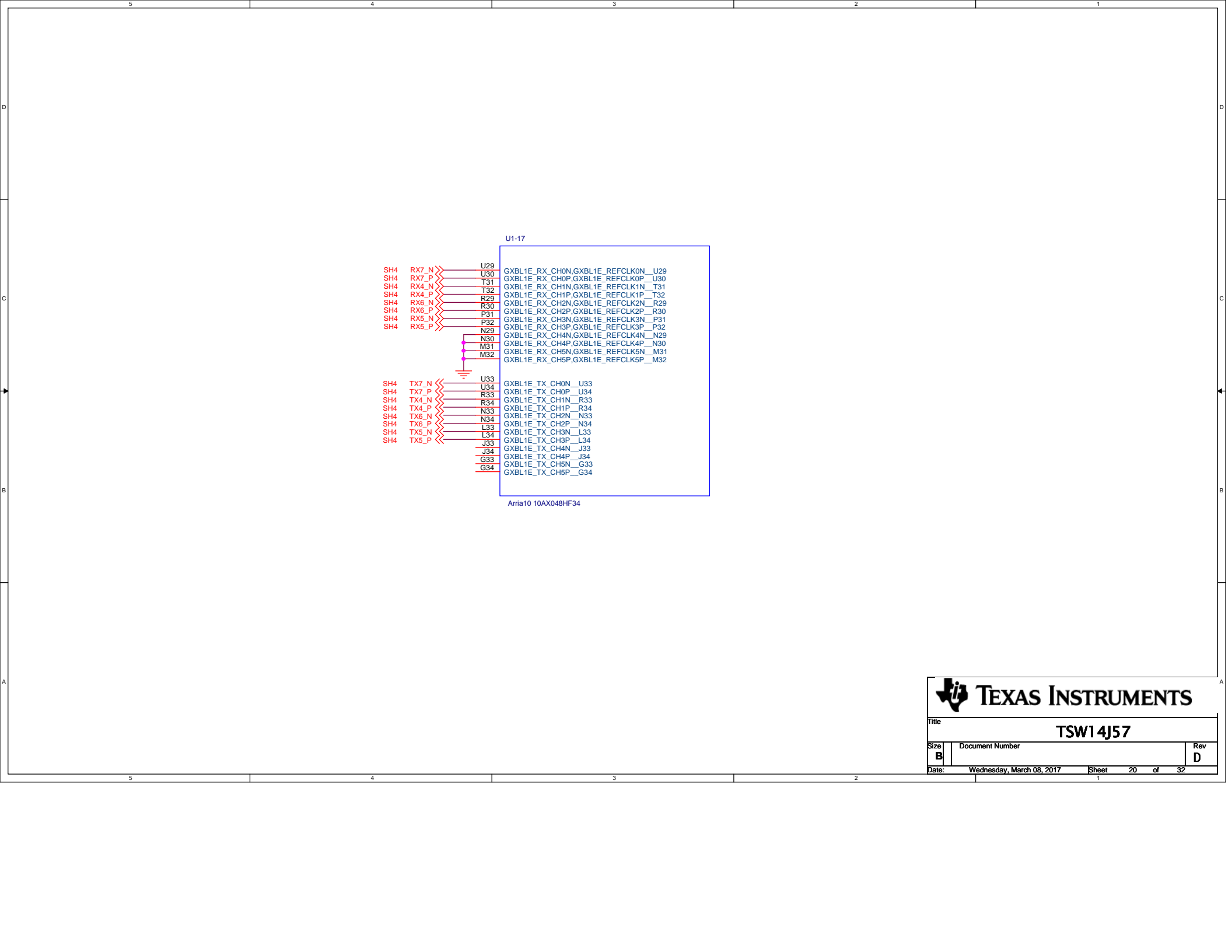
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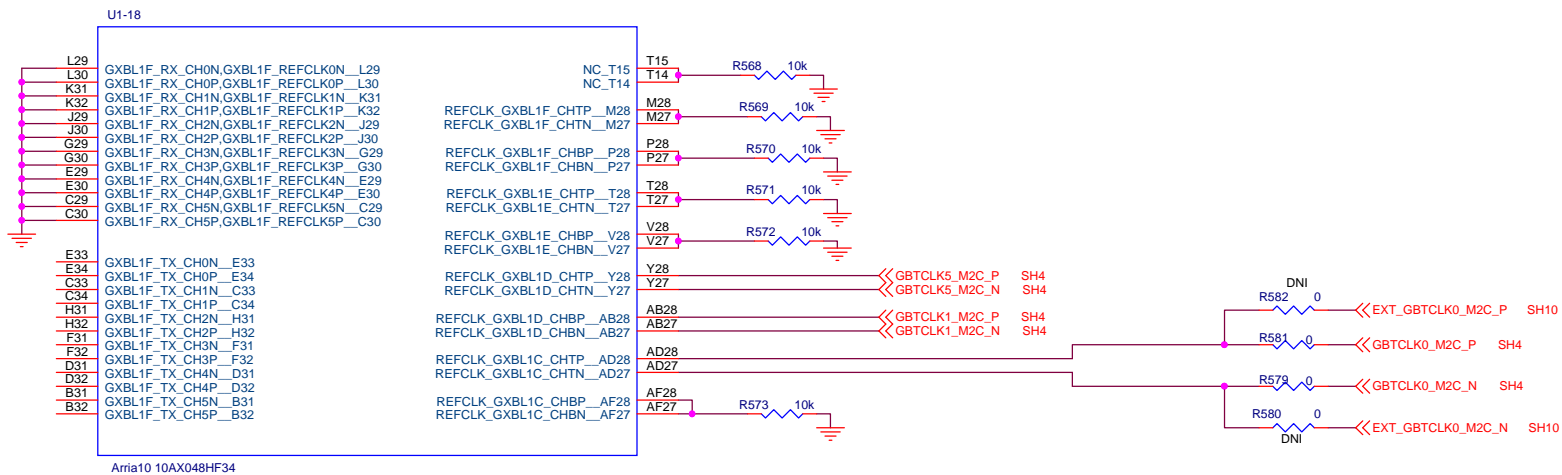
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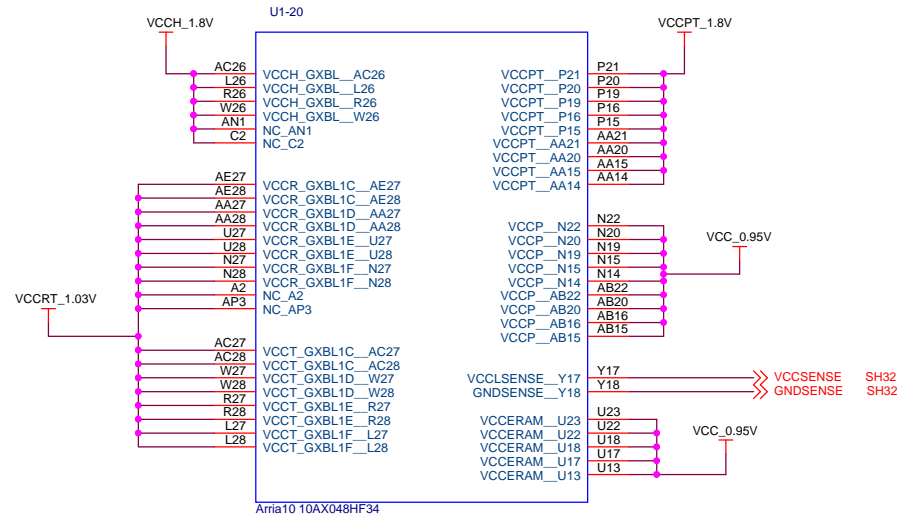
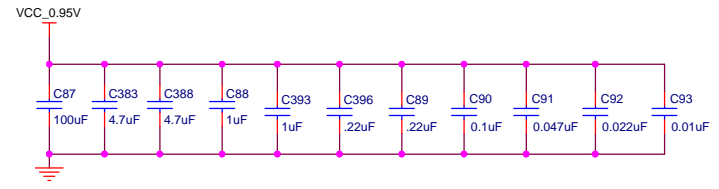
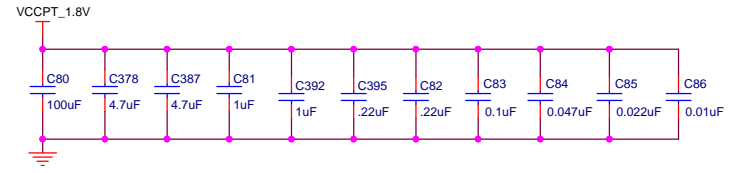
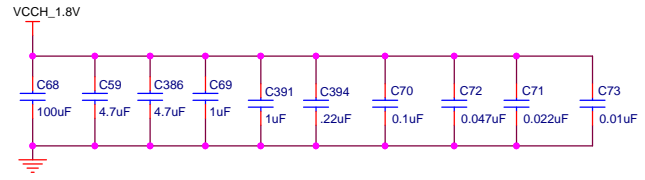
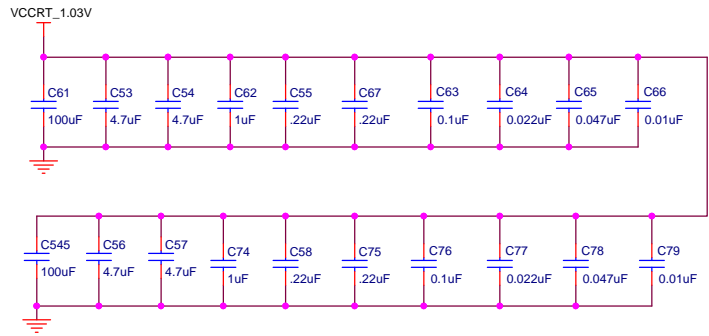
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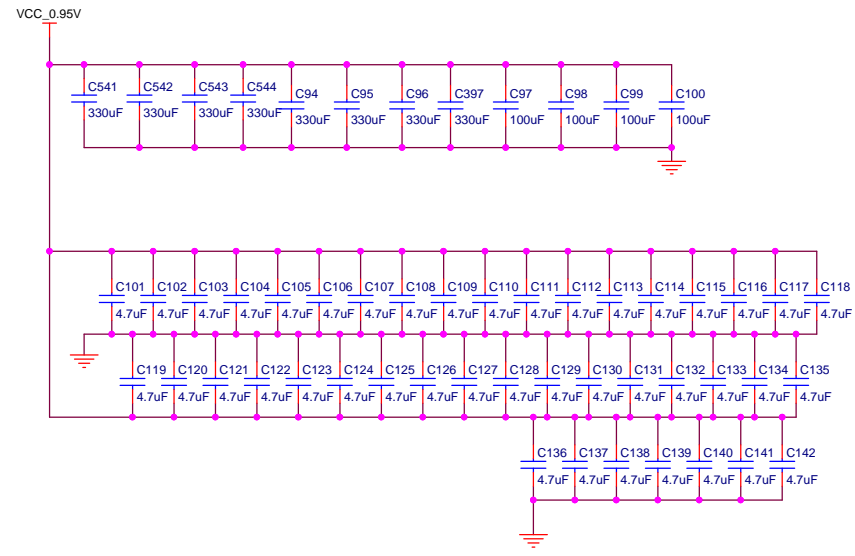
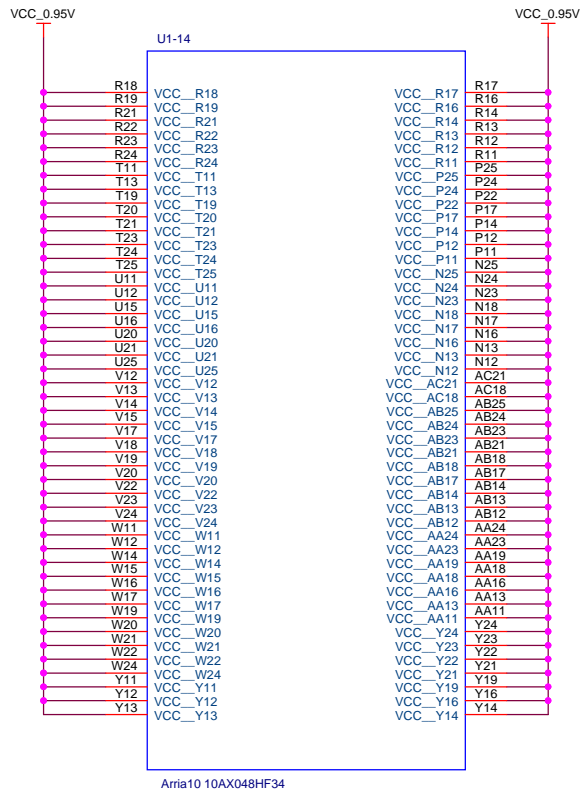



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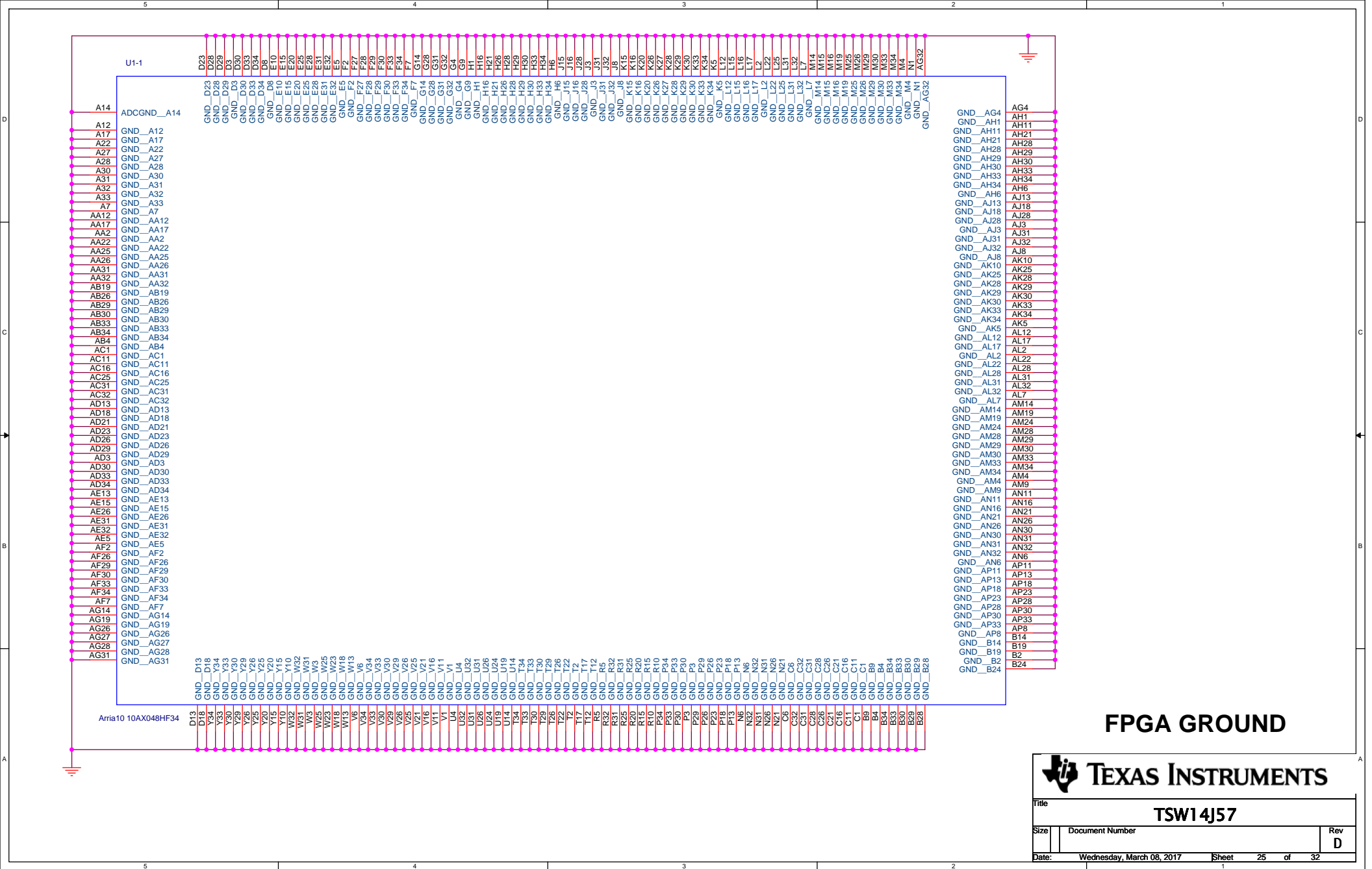
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FPGA GROUND



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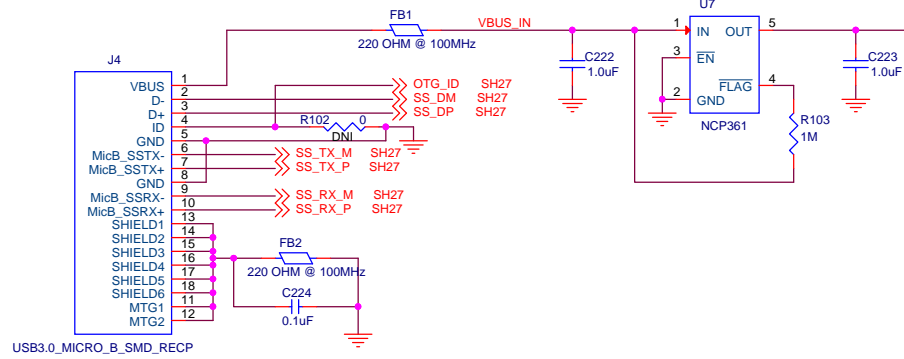
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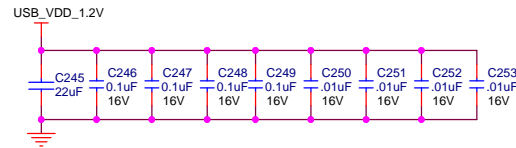
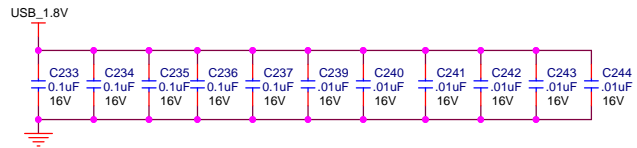
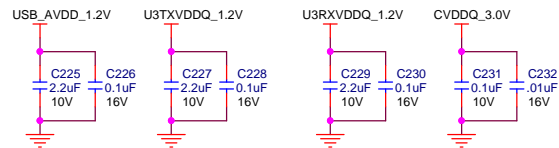
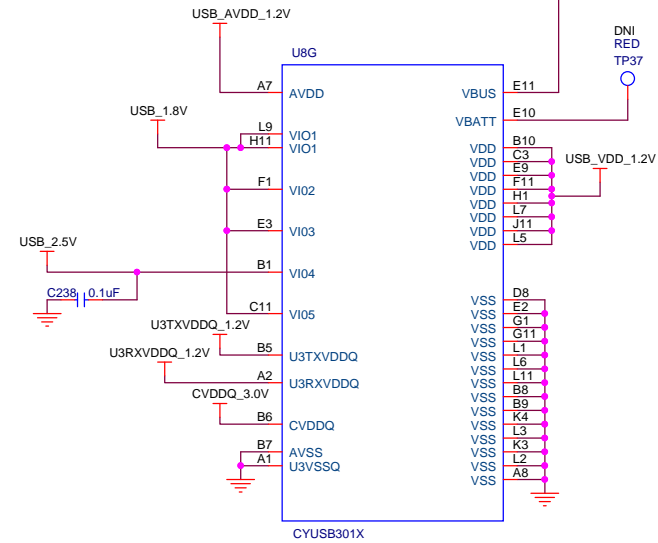
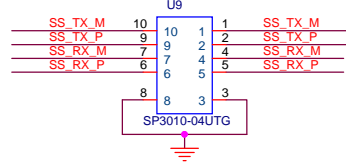
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USB Overvoltage Protection



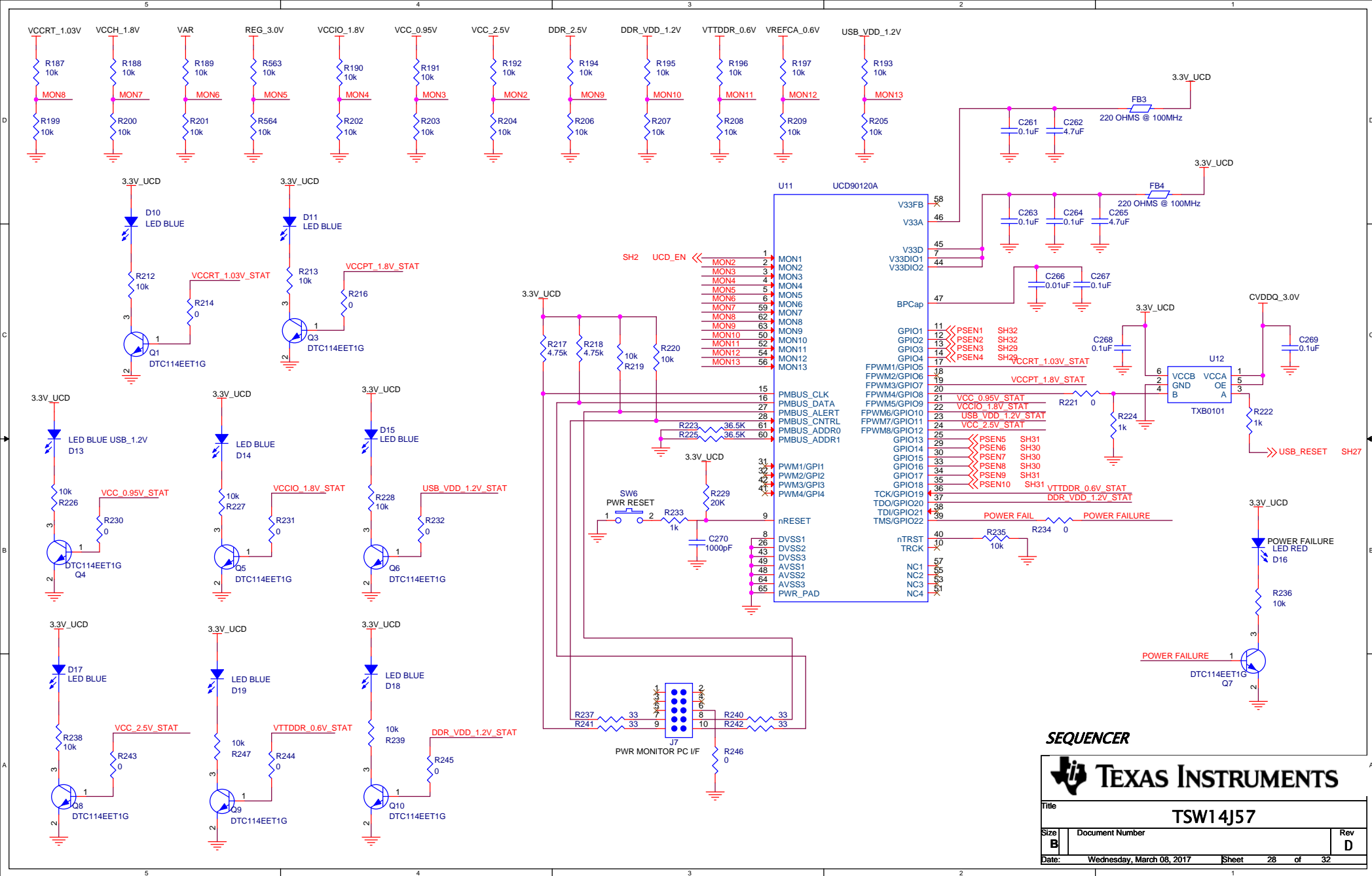
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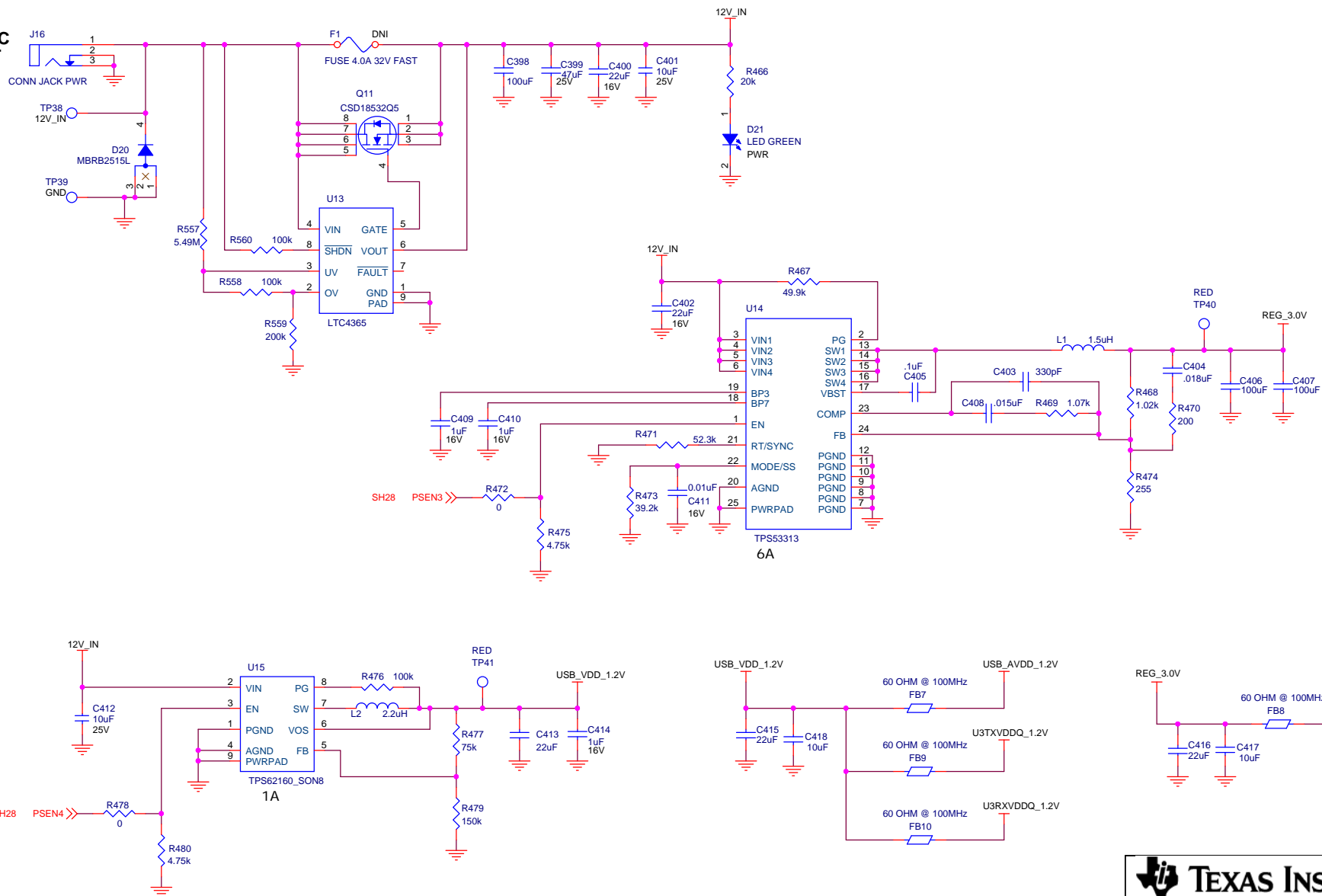


USB



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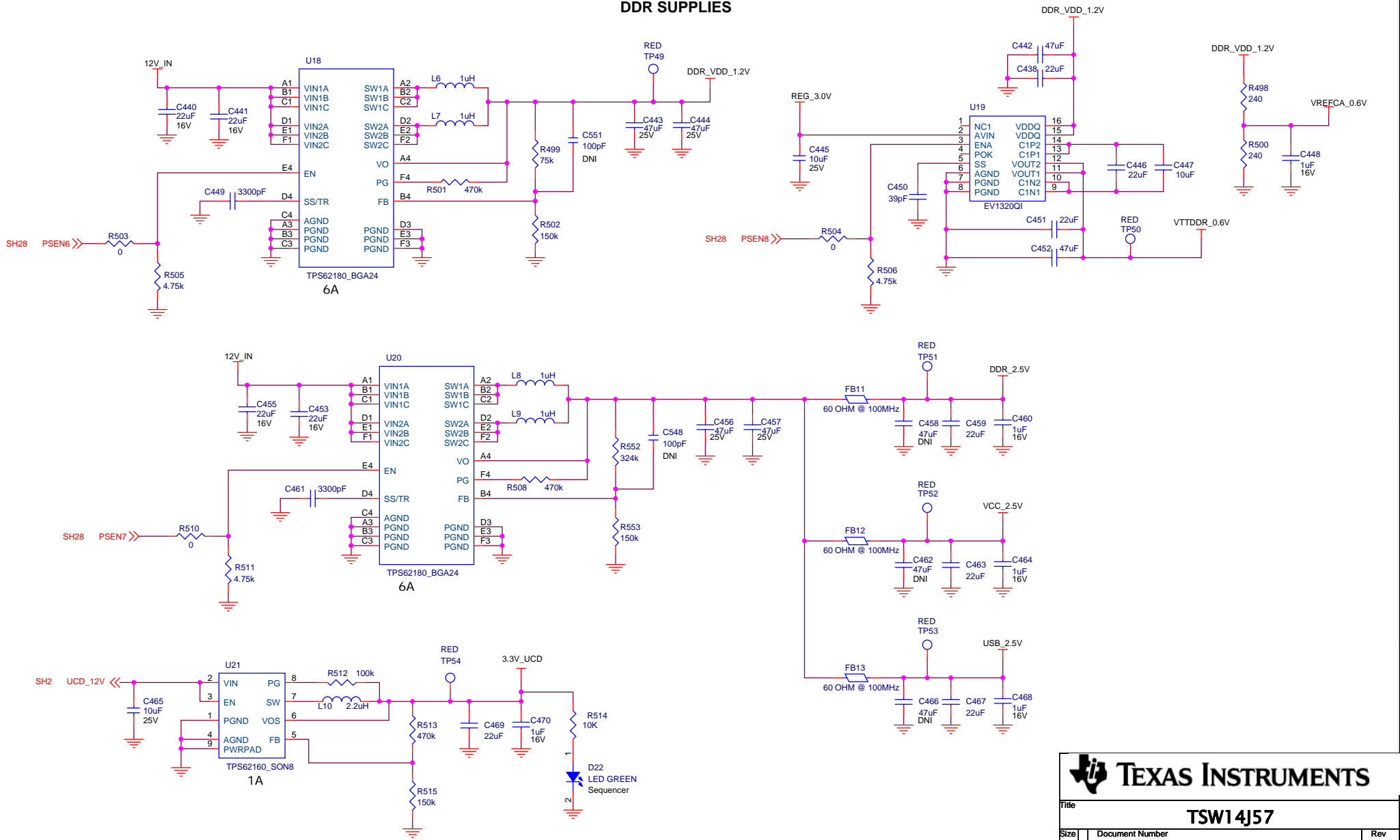


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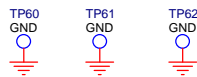
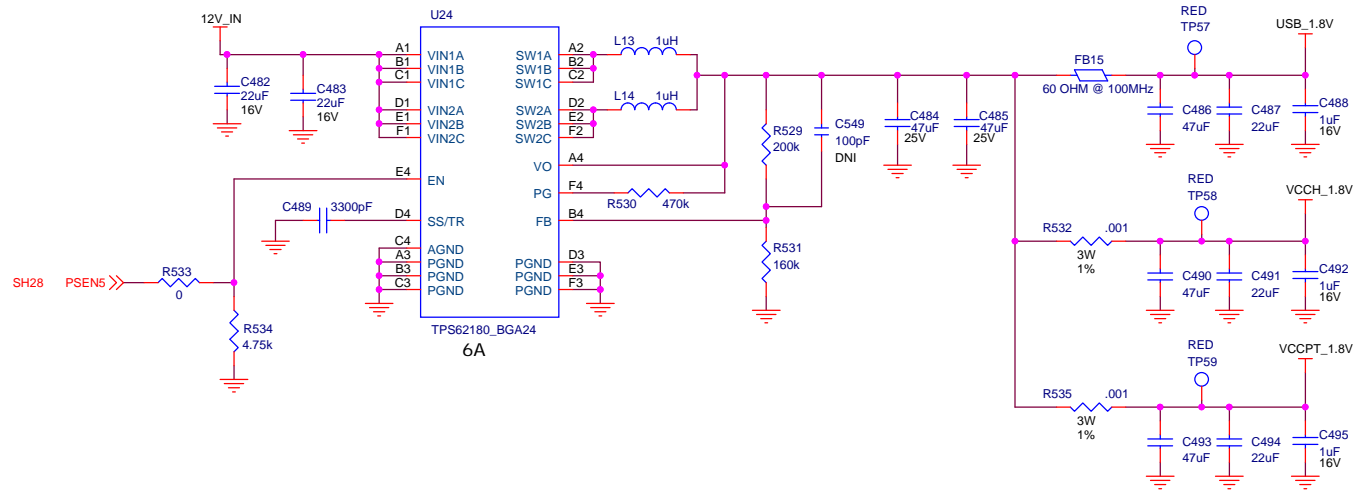
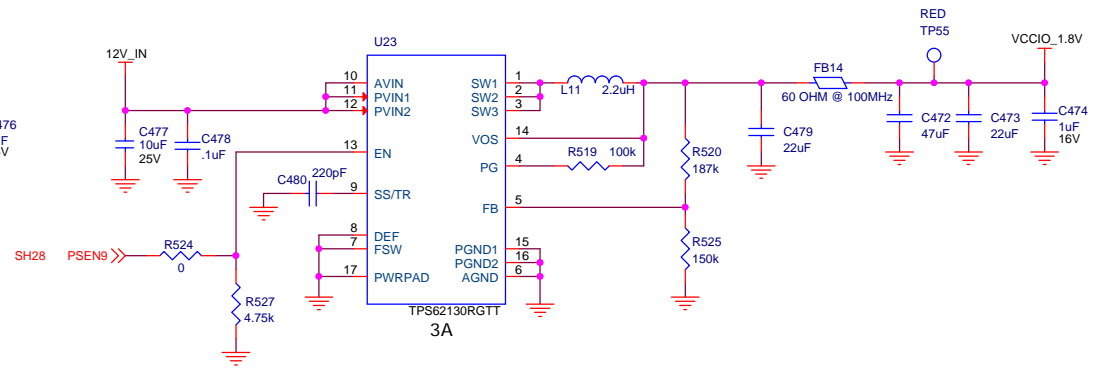
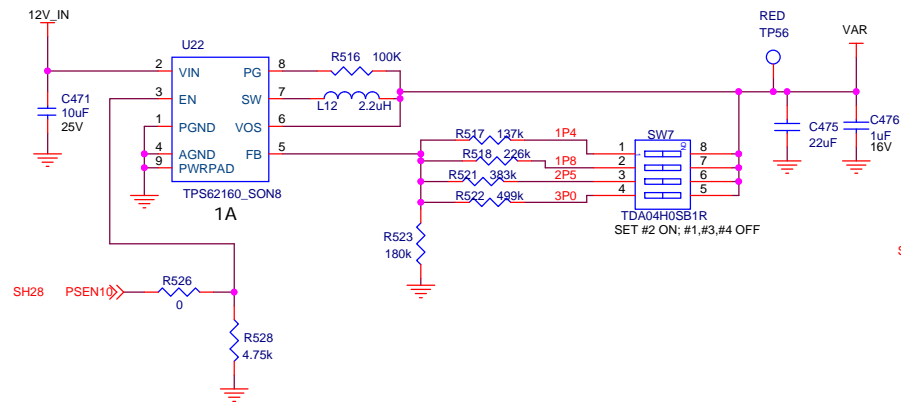
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DDR SUPPLIES



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