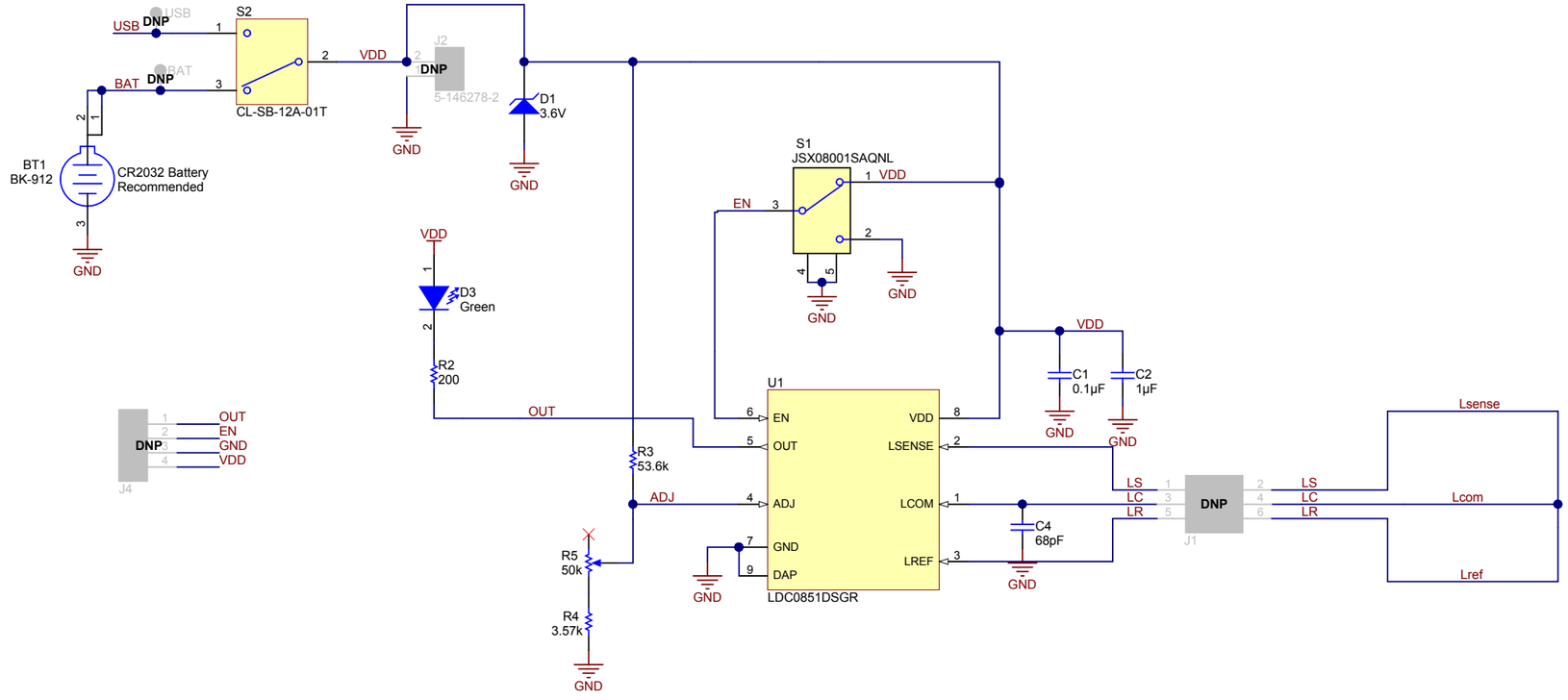

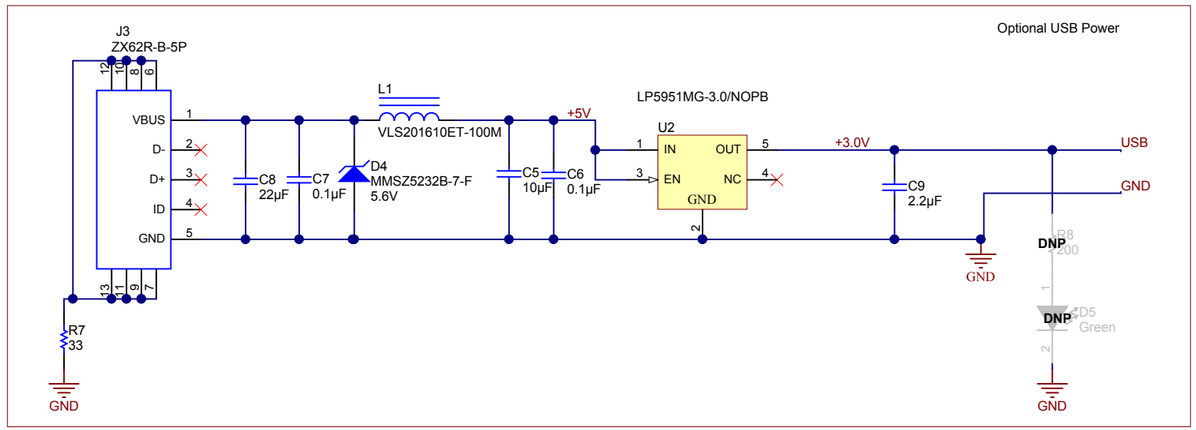

Label Table	
Variant	Label Text
001	LDC0851EVM

PCB Number: SV601198
PCB Rev: A

PCB
LOGO
Texas Instruments

DNP FID1 DNP FID2 DNP FID3 DNP FID4 DNP FID5 FID6

PCB
LOGO
Texas Instruments

PCB
LOGO
Pb-Free Symbol

PCB
LOGO
FCC disclaimer

LBL1
PCB Label
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 11/11/2015
TID #: TID	Project Title: LDC0851EVM	
Number: SV601198 Rev: A	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 1
Drawn By:	File: SV601198A.SchDoc	Size: B
Engineer: Luke LaPointe	Contact: http://www.ti.com/support	

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