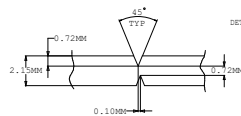


FABRICATION NOTES:

- FABRICATE PCB IN ACCORDANCE WITH IPC-6012C, CLASS 2; PER IPC-6011.
PCB SHALL BE MANUFACTURED USING 370HR OR EQUIVALENT.
- MATERIALS:
 - LAMINATE AND PREPREG (B-STAGE) TO BE IN ACCORDANCE WITH IPC-4101/126.
(MIN.TG 180)
 - COPPER FOIL TO BE IN ACCORDANCE WITH IPC-MF-150. UNLESS OTHERWISE SPECIFIED,
FOR OUTER LAYERS 1.45 OZ. COPPER WEIGHT IS TO BE CONSIDERED "FINISHED".
THE COPPER FOIL THICKNESS TOLERANCES SHALL BE AS PER IPC 6012B TABLE NO.3-7 AND 3-8.
- ALL HOLES SHALL BE LOCATED WITHIN 0.15MM DIAMETER OF TRUE POSITION.
LAYER TO LAYER REGISTRATION SHALL BE WITHIN 0.125MM.
- BOW AND TWIST SHALL NOT EXCEED MORE THAN 0.75% OF THE DESIGN LENGTH.
- CONDUCTOR WIDTH SHALL NOT BE LESS THAN 20% FROM ITS ORIGINAL DATA. INCREASE FOR MATCHING
IMPEDANCE MISTRAL SHALL APPROVE THE MODIFIED WIDTHS AND SPACING.
TRACE WIDTH SHALL BE MEASURED ON THE SURFACE IN CONTACT WITH THE LAMINATE.
- AUTOMATED OPTICAL INSPECTION OF ALL THE LAYERS IS REQUIRED.
- FINISH:
 - ALL EXPOSED CONDUCTIVE PATTERN AREAS NOT COVERED WITH SOLDER MASK OR OTHER PLATING SHALL BE ENIG,
ELECTROLESS NICKEL/IMMERSION GOLD, ELECTROLESS NICKEL SHALL BE 3-6 MICRONS, TYPICAL IMMERSION GOLD
THICKNESS SHALL BE 0.04-0.06 MICRONS OF SOLDERABLE IMMERSION GOLD SURFACE.
 - APPLY LIQUID PHOTO IMAGEABLE SOLDER MASK PER IPC-SM-840, CLASS H, TO BOTH SIDES OF THE BOARD OVER BARE COPPER.
VIA HOLES THAT HAVE MASK OPEN SHALL BE FILLED WITH NON CONDUCTIVE INK AND CAP PLATED.
ALL OTHER VIA HOLES SHALL BE FILLED WITH NON CONDUCTIVE INK AND COVERED WITH SOLDER MASK,
ONLY SOLDERMASK IMAGES THAT ARE 0.08(0.003") PER SIDE SHALL BE REDUCED IF REQUIRED.
ALL OTHER SOLDER MASK IMAGES SHALL NOT BE ENLARGED. DEFAULT COLOUR OF SOLDER MASK SHALL BE GREEN.
- SILKSCREEN SHALL BE WHITE, PERMANENT, ORGANIC, NON-CONDUCTIVE INK. THERE SHALL BE
NO SILKSCREEN ON ANY SOLDERABLE COMPONENT PAD. CLIPPING OF SILK SCREEN SHALL BE ALLOWED
IF THE SILK SCREEN FALLS ON SOLDERABLE AREAS.
- SURFACE AND VIA HOLES FINISH SHALL NOT BE LESS THAN 25UM [0.00079"], INCREASE OF LASER
VIA'S, BLIND VIA'S SHALL NOT BE LESS THAN 12UM [0.00047"] AND BURIED VIA'S SHALL NOT BE LESS THAN 15UM [0.0006"].
- ALL HOLES SURROUNDED BY LAND <0.010" SHALL BE COMPLAINT TO IPC6012, CLASS 2.
- MARKING:
 - BOARD SHALL MEET THE REQUIREMENTS OF UL-796E WITH FLAMMABILITY RATING OF MINIMUM 94V-0. UL LOGO, UL FILE NUMBER,
MANUFACTURER'S IDENTIFICATION AND DATE CODE LETTER SHALL BE RENDERED IN SILKSCREEN.
- TEST REQUIREMENTS:
 - 100% NET LIST ELECTRICAL VERIFICATION USING MISTRAL SUPPLIED IPC-D-356 NET LIST FOR OPENS AND SHORTS.
- THEIVING IS ALLOWED ONLY IN THE PANEL FRAME, NOT IN THE CIRCUIT AREA.
- TEAR DROPS SHALL BE ADDED ON INTERNAL AND EXTERNAL LAYER FOR ALL THE VIA'S AND THROUGH HOLE PADS.
- FINISHED PCB THICKNESS SHALL BE 0.0850" +/-10%.
- MIN TRACE WIDTH/SPACING ON BOARD IS 0.0031"/0.0033".
- ALL THE IMPEDANCE SHALL BE MATCHED AS PER IMPEDANCE TABLE WITH +/-10% TOLERANCE.
- ALL UNCONNECTED VIA'S SHALL BE SUPPRESSED IN INTERNAL LAYERS.
- V-SCORE TO BE DONE AS PER DETAIL 'A'.
- FOR STACKUP DETAILS 'PROCI41_STACKUP.PDF' SHALL BE REFERRED.



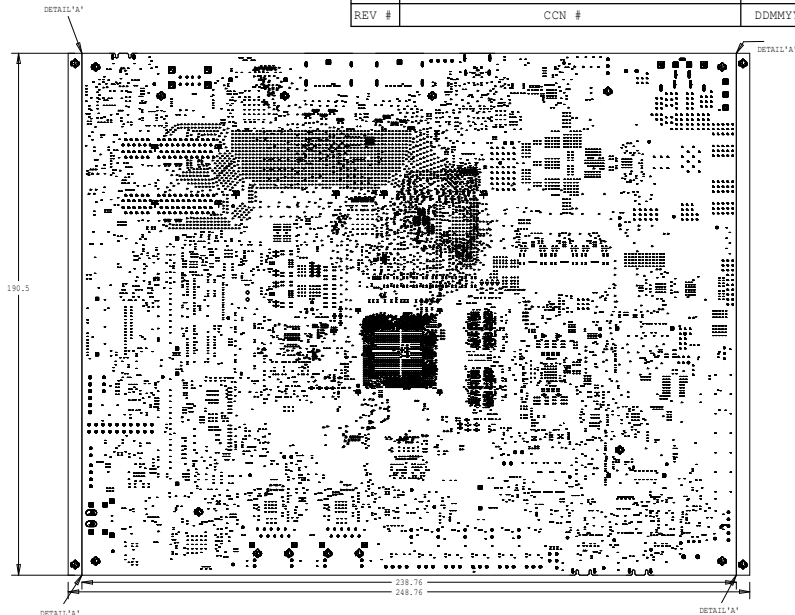
LAYER STACKUP

LAYER NAME	FINISHED Cu	X-SECTION	DIELECTRIC THICKNESS
PRIMARY SIDE SILKSCREEN			[INCHES]
PRIMARY SIDE SOLDERMASK			
L01 PRIMARY SIDE	1.67oz		0.0034
L02 GROUND-PLANE-1	0.5oz		0.0038
L03 INNER-SIGNAL-1	0.5oz		0.0038
L04 GROUND-PLANE-2	0.5oz		0.0038
L05 INNER-SIGNAL-2	0.5oz		0.005
L06 GROUND-PLANE-3	1oz		0.0046
L07 INNER-SIGNAL-3	1oz		0.005
L08 POWER-PLANE-1	1oz		0.0052
L09 POWER-PLANE-2	1oz		0.005
L10 INNER-SIGNAL-4	1oz		0.0046
L11 GROUND-PLANE-4	1oz		0.005
L12 INNER-SIGNAL-5	0.5oz		0.0038
L13 GROUND-PLANE-5	0.5oz		0.0038
L14 INNER-SIGNAL-6	0.5oz		0.0038
L15 GROUND-PLANE-6	0.5oz		0.0038
L16 SECONDARY SIDE	1.67oz		0.0034
SECONDARY SIDE SOLDERMASK			
SECONDARY SIDE SILKSCREEN			

IMPEDANCE SPECIFICATIONS


SL#	TYPE	LAYER	TRACEWIDTH (Mils)	SPACING (Mils)	IMPEDANCE (Ohms)	REF LAYER
01	EDGE COUPLED MICROSTRIP	L1/L16	3.7	7.3	100	L2/L15
02	EDGE COUPLED MICROSTRIP	L1	5.2	8	120	L3
03	EDGE COUPLED MICROSTRIP	L1/L16	5.2	8	120	L2/L15
04	EDGE COUPLED MICROSTRIP	L1/L16	5.2	5.8	85	L2/L15
05	MICROSTRIP	L1/L16	5	5	50	L2/L13
06	EDGE COUPLED STRIPLINE	L1/L16	3.3	6.5	100	L2/L4, L13/L15
07	EDGE COUPLED STRIPLINE	L5, L12	3.3	5	100	L4/L6, L11/L13
08	EDGE COUPLED STRIPLINE	L10	3.3	8.3	100	L9/L11
09	EDGE COUPLED STRIPLINE	L3, L12	3.3	4	30	L4/L6, L11/L13
10	EDGE COUPLED STRIPLINE	L5, L12	4.25	5	90	L4/L6, L11/L13
11	EDGE COUPLED STRIPLINE	L1	5	7	30	L6/L8
12	EDGE COUPLED STRIPLINE	L2, L14	4.75	5	85	L2/L4, L13/L15
13	EDGE COUPLED STRIPLINE	L5, L12	4.75	5	85	L4/L6, L11/L13
14	EDGE COUPLED STRIPLINE	L3	5.1	6.9	80	L2/L4
15	EDGE COUPLED STRIPLINE	L5	5.1	5.5	80	L4/L6
16	EDGE COUPLED STRIPLINE	L14	7	6	66	L13/L15
17	STRIPLINE	L3, L14	3.5	-	50	L2/L4, L13/L15
18	STRIPLINE	L5, L12	4	-	50	L4/L6, L11/L13
19	STRIPLINE	L7, L10	4.3	-	50	L6/L8, L9/L11
20	STRIPLINE	L3	5.5	-	40	L2/L4
21	STRIPLINE	L5	5.5	-	40	L4/L6
22	STRIPLINE	L9	7.5	-	33	L2/L4
23	EDGE COUPLED MICROSTRIP	L1/L16	5	15.5	100 DS1	L2/L15
24	EDGE COUPLED STRIPLINE	L3, L12	3.3	9.7	100 DS1	L2/L4, L13/L15
25	EDGE COUPLED STRIPLINE	L5, L12	4	10	100 DS1	L4/L6, L11/L13
26	EDGE COUPLED STRIPLINE	L3	3	8	120	L2/L4
27	STRIPLINE	L1	3	-	66	L6/L8
28	STRIPLINE	L7	7.5	-	33	L2/L4
29	EDGE COUPLED STRIPLINE	L7	7	6	66	L6/L8

REVISIONS		
REV #	DESCRIPTION	DATE
REV #	CCN #	DDMMYY



DRILL CHART: TOP TO BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
1	8.0	+3.0/-3.0	PLATED	5441
2	8.0	+3.0/-3.0	PLATED	3
3	10.0	+3.0/-3.0	PLATED	869
4	20.0	+3.0/-3.0	PLATED	276
5	28.0	+3.0/-3.0	PLATED	128
6	32.0	+2.0/-2.0	PLATED	2
7	32.0	+3.0/-3.0	PLATED	3
8	36.0	+3.0/-3.0	PLATED	31
9	40.0	+2.0/-2.0	PLATED	12
10	40.0	+3.0/-3.0	PLATED	79
11	44.0	+2.0/-2.0	PLATED	33
12	46.0	+3.0/-3.0	PLATED	6
13	48.0	+3.0/-3.0	PLATED	2
14	56.0	+3.0/-3.0	PLATED	2
15	60.0	+3.0/-3.0	PLATED	6
16	66.0	+3.0/-3.0	PLATED	6
17	68.0	+3.0/-3.0	PLATED	3
18	86.0	+3.0/-3.0	PLATED	2
19	90.0	+3.0/-3.0	PLATED	4
20	34.0	+2.0/-2.0	NON-PLATED	2
21	34.0	+3.0/-3.0	NON-PLATED	2
22	40.0	+1.969/-1.969	NON-PLATED	2
23	40.0	+3.0/-3.0	NON-PLATED	11
24	52.0	+3.0/-3.0	NON-PLATED	1
25	58.0	+3.0/-3.0	NON-PLATED	2
26	66.0	+3.0/-3.0	NON-PLATED	4
27	92.0	+2.0/-2.0	NON-PLATED	4
28	108.0	+3.0/-3.0	NON-PLATED	4
29	126.0	+3.0/-3.0	NON-PLATED	18
30	25.0x24.0	+3.0/-3.0	PLATED	6
31	42.0x24.0	+2.0/-2.0	PLATED	2
32	70.0x26.0	+2.0/-2.0	PLATED	4
33	82.0x24.0	+2.0/-2.0	PLATED	2
34	86.0x24.0	+3.0/-3.0	PLATED	1
35	86.0x24.0	+3.0/-3.0	PLATED	2
36	90.0x24.0	+2.0/-2.0	PLATED	4
37	90.0x24.0	+3.0/-3.0	PLATED	4
38	158.0x78.0	+3.0/-3.0	NON-PLATED	2

DRILL CHART: TOP TO L2-GND1				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
1	5.0	+2.0/-2.0	PLATED	1278
DRILL CHART: L2-GND1 TO L3-SIGNAL1				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
1	5.0	+2.0/-2.0	PLATED	1041
DRILL CHART: L3-SIGNAL1 TO L4-GND2				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
1	5.0	+2.0/-2.0	PLATED	392
DRILL CHART: L4-GND2 TO L5-SIGNAL2				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
1	5.0	+2.0/-2.0	PLATED	342
DRILL CHART: L4-GND2 TO L13-GND5				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
1	8.0	+3.0/-3.0	PLATED	305
DRILL CHART: L13-SIGNAL5 TO L13-GND5				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
1	5.0	+2.0/-2.0	PLATED	631
DRILL CHART: L13-GND5 TO L14-SIGNAL6				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
1	5.0	+2.0/-2.0	PLATED	1026
DRILL CHART: L14-SIGNAL6 TO L15-GND6				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
1	5.0	+2.0/-2.0	PLATED	1073
DRILL CHART: L15-GND6 TO BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
1	5.0	+2.0/-2.0	PLATED	702

SIGNATURES		DATE	 TEXAS INSTRUMENTS		PROC184E2
LAYOUT BY NP		290824			
REVIEWED BY ZA		290824			
APPROVED BY AMB		290824			
J742S2/TDA4VPE/TDA4APE EVALUATION MODULE					