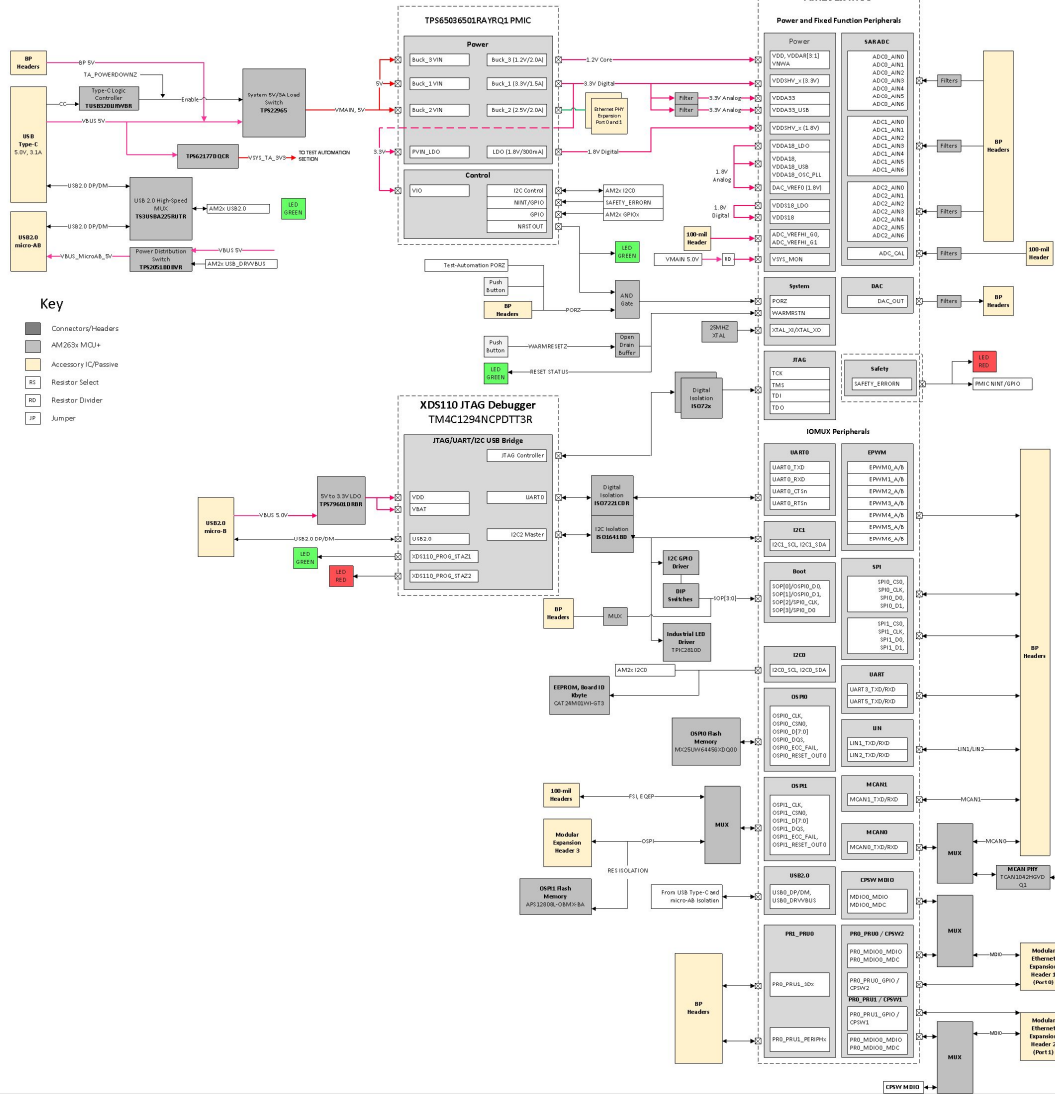


LP-AM261 - AM261x Launchpad EVM

SYSTEM BLOCK DIAGRAM



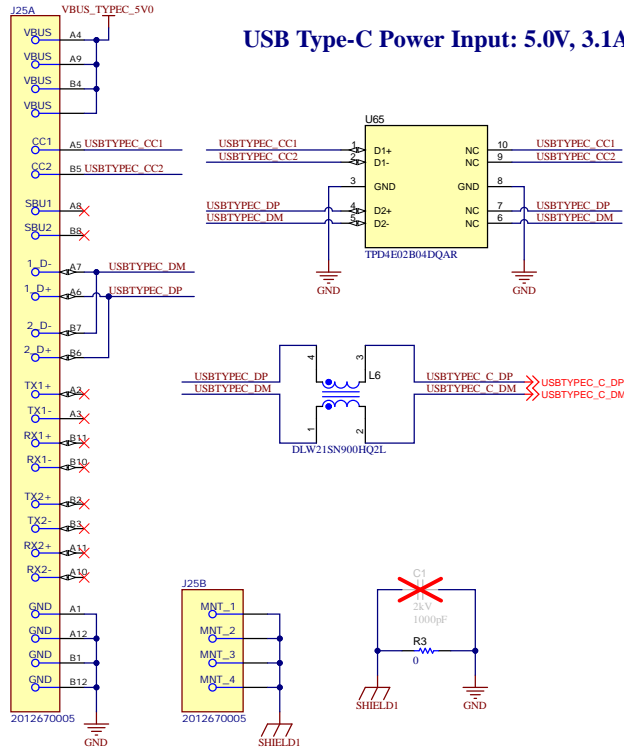
Revision History					
Rev	ECN #	Approved Date	Approved by	Notes	
E1	YES	2024-03-07	Sridhar Potluri	Initial revision	
E2	N/A	2025-05-12	Sridhar Potluri	<p>Drafted from E1</p> <ul style="list-style-type: none"> - Booster pack connectivity updated - Test Automation power (VDSYS_TA_3v3) added - OSPI interfaces changed to OSPIO – MX2UW6445XDQD00 OSPIN – AP512808L0BMM-BXA - PMIC Part updated to TPSE6536601RAYRQ1 - VCORE_1V2 power Changed to VCORE_1V2S - Added active low mux for SOP pins whose enable is controlled by SOP_DRIVER_OEN and BP_BO_MUX_EN_N - SOC Part updated to XAM2612ACHFFHIZFG - Replaced TXSB108RGYR (U29) with the TXSB108ERGYYR - DNI# R31 and populated the R50 - Added Design note about non usage of K4(GP0E1) for the functionality of OSPIO_RESET_OUT0 - DNI# RZ25 and RZ29 to resolve the MDO issue - Updated the PMIC part no. to TPSE6536601RAYRQ1 	

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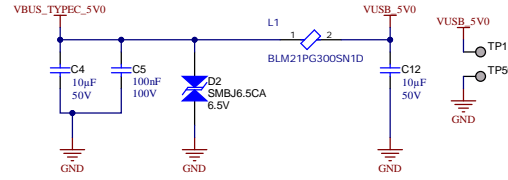
Orderable: LP-AM261	Designed for:	Mod. Date: 5/12/2025
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193	Rev: E2	Sheet Title: Cover sheet
SVN Rev: 448 [Locally Modified]	Assembly Variant: 001	Sheet: 1 of 24
Drawn By: Vijetha J. Kiran	File: PROC193E2_01_CoverSheet.SchDoc	Size: B
Engineer: Vijetha J. Kiran	Contact:	



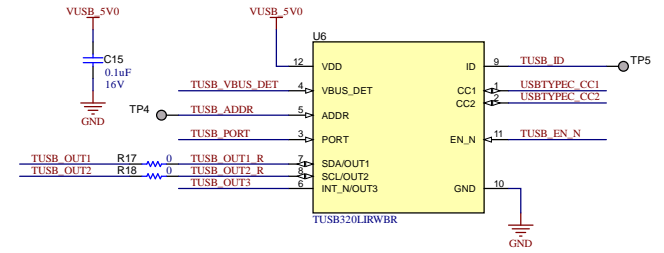
USB Type-C Power Input: 5.0V, 3.1A



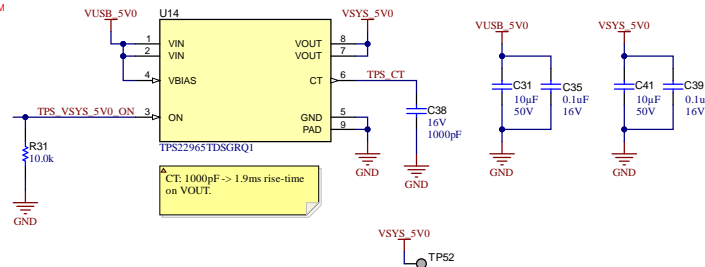
USB 5.0V Input Power Filtering



USB Type-C CC Logic Controller

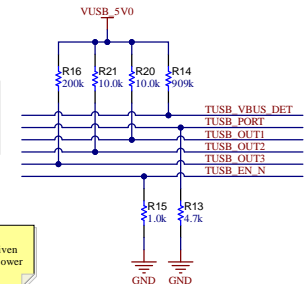


USB 5.0V Input Power Load Switch (4A max)

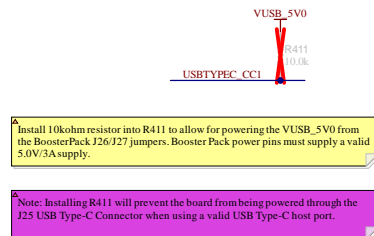


PORT: pull low to enumerate as down-stream (sink) device mode.

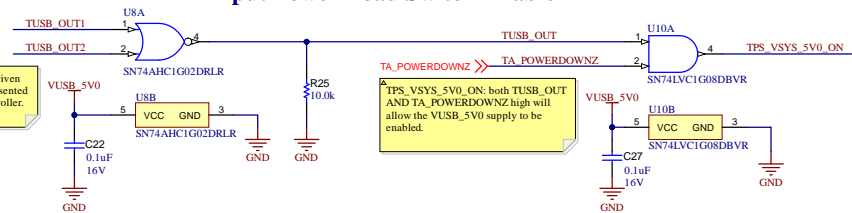
EN_N: grounded, to enable the TUSB320 by default, but can be driven high by test automation header to power down system.



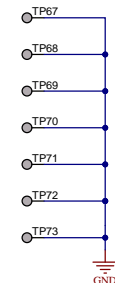
USB Type-C DFP CC Emulation



Input Power Load Switch Enable

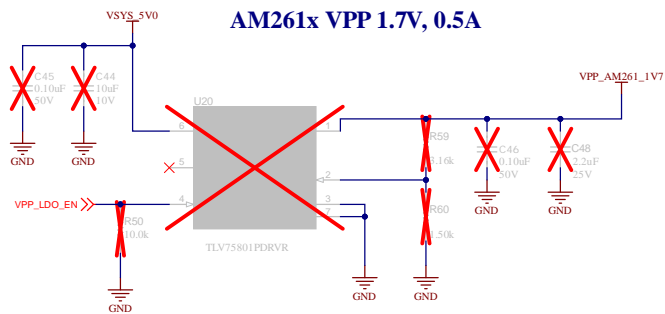



GND Test Points



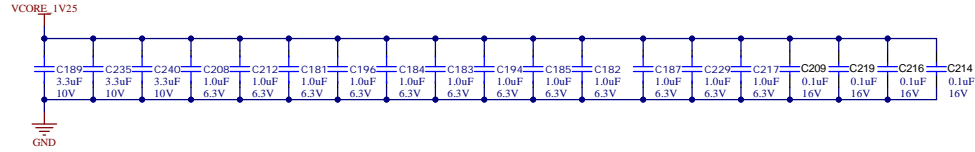
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Orderable: LP-AM261	Designed for: AM261 Launchpad	Mod. Date: 12/20/2024
TID #: N/A	Project Title: USB TYPE-C INPUT PWR	
Number: PROC193	Rev: E2	Sheet Title: USB TYPE-C INPUT PWR
SVN Rev: 381	Assembly Variant: 001	Sheet 2 of 24
Drawn By: Vijetha J. Kiran	File: PROC193E2_02_USB_TYPE_C_IN_PWR_SchDdSize: B	
Engineer: Vijetha J. Kiran	Contact:	

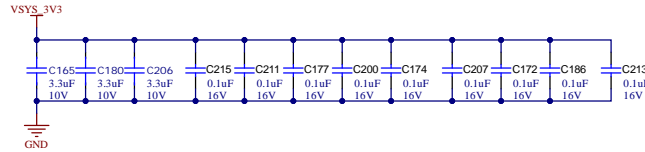


Orderable: LP-AM261	Designed for:	Mod. Date: 12/20/2024	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments
TID #: N/A	Project Title: AM261 Launchpad		
Number: PROC193	Rev: E2	Sheet Title: VPP 1.7V LDO	
SVN Rev: 353	Assembly Variant: 0	Sheet: 4 of 24	
Drawn by: Vijetha J. Kiran	File: PROC193E2_0_VPP1V7_PWR_SchDoc	Size: B	
Engineer: Vijetha J. Kiran	Contact:		

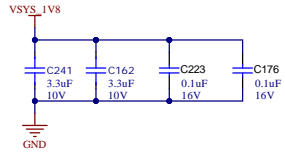
VDD 1V25 Core Digital



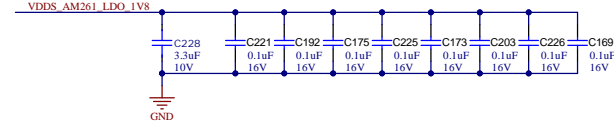
VDDSHV 3V3 Digital



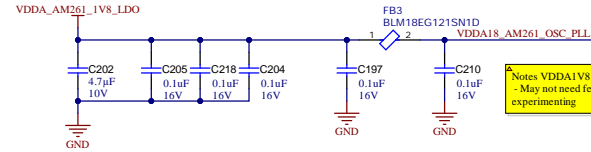
VDDSHV 1V8 Digital



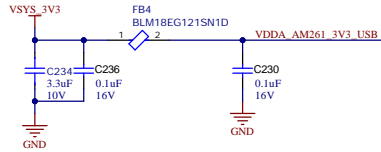
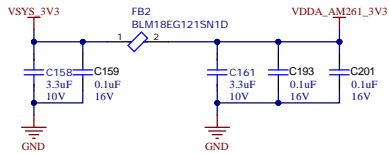
VDDS 1V8 Digital



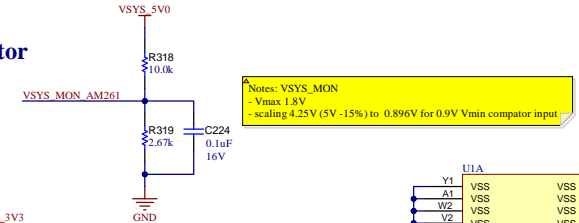
VDDA 1V8 Analog



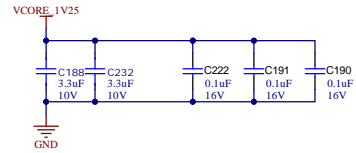
VDDA 3V3 Analog



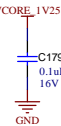
VSYS Voltage Monitor



VDDAR[3:2] 1V25 SRAM Array



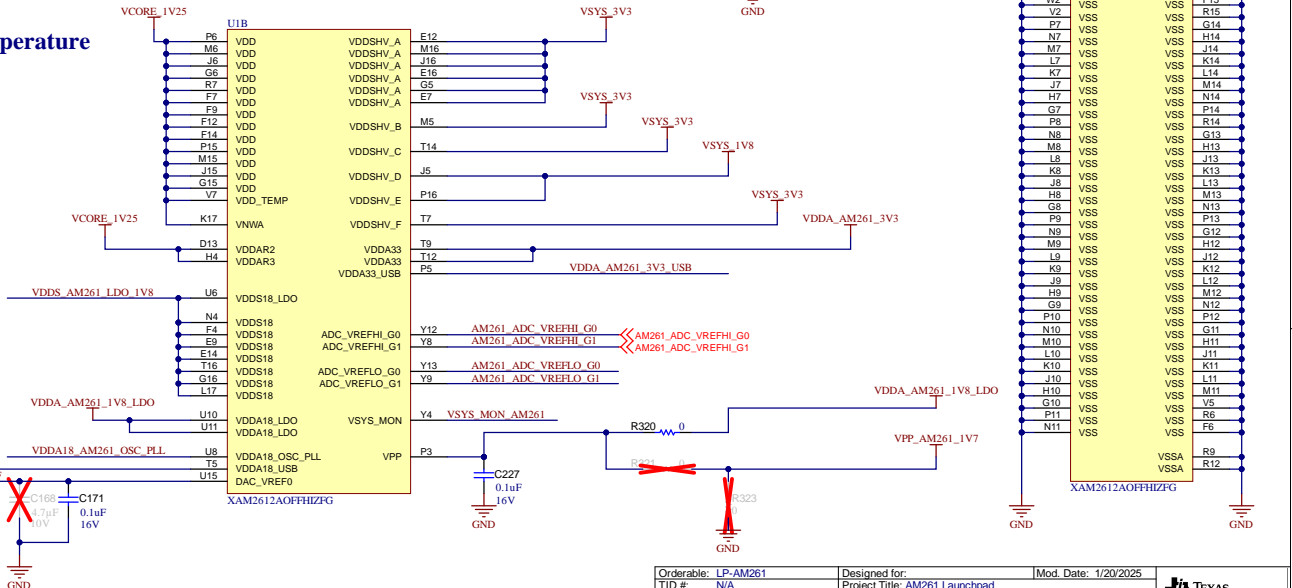
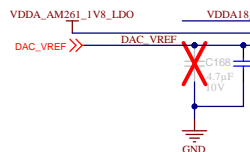
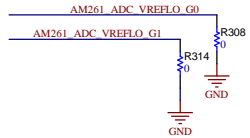
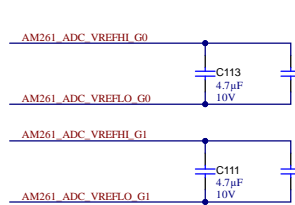
VNWA 1V2



VDD 1V2 Temperature



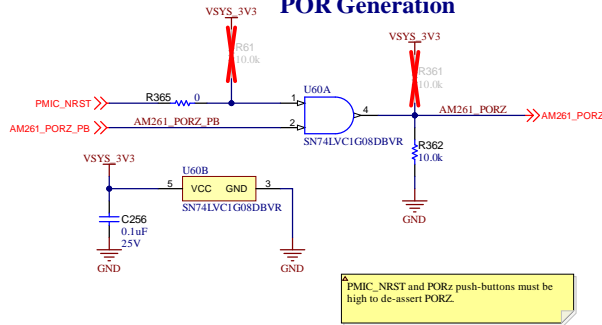
ADC VREF Decoupling



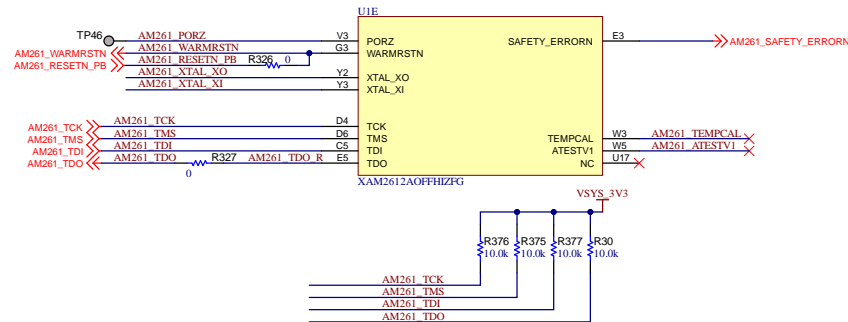
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Orderable: LP-AM261	Designed for: AM261 Launchpad	Mod. Date: 1/20/2025
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193	Rev: E2	Sheet Title: SoC Power
SVN Rev: 421	Assembly Variant: 001	Sheet: 5 of 24
Drawn By: Vijetha J. Kiran	File: PROC193E2_05 SoC Power.SchDoc	Size: B
Engineer: Vijetha J. Kiran	Contact:	

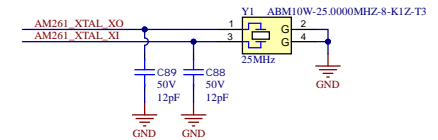
AM261x Clock, Reset, Boot, JTAG



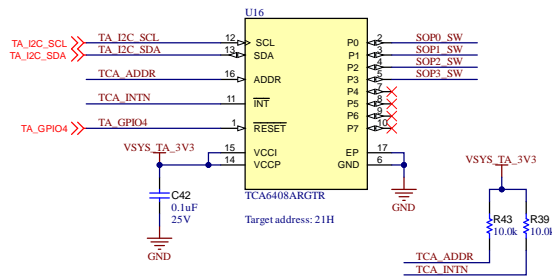
PMIC_NRST and PORz push-buttons must be high to de-assert PORZ.



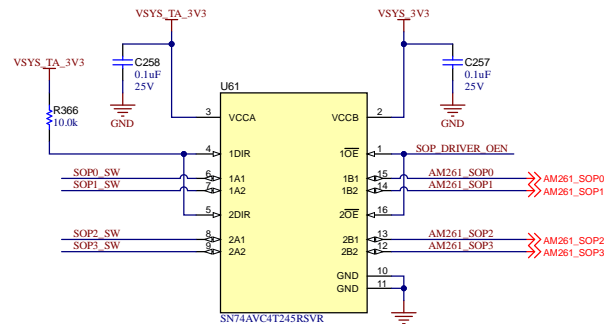
25 MHz Crystal



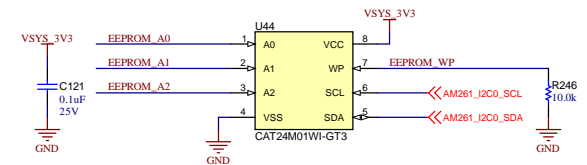
Test Automation SOP Select



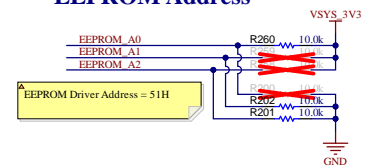
SOP State Driver



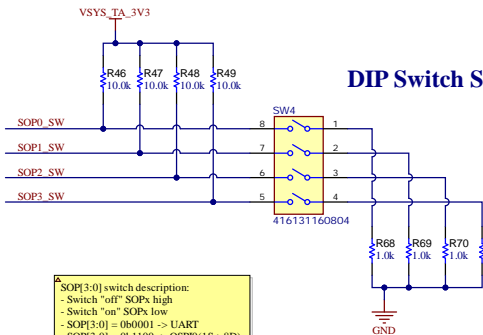
Board ID EEPROM



EEPROM Address

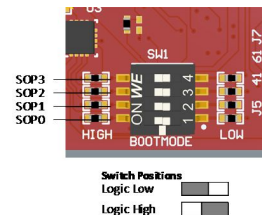


DIP Switch SOP Select




SOP[3:0] switch description:

- Switch "off" SOPx high
- Switch "on" SOPx low
- SOP[3:0] = 0b0001 -> UART
- SOP[3:0] = 0b1100 -> OSPI0(1S->8)
- SOP[3:0] = 0b0011 -> OSPI0(8S),SD
- SOP[3:0] = 0b1011 -> DevBoot
- SOP[3:0] = 0b1110 -> USB2.0 DFU

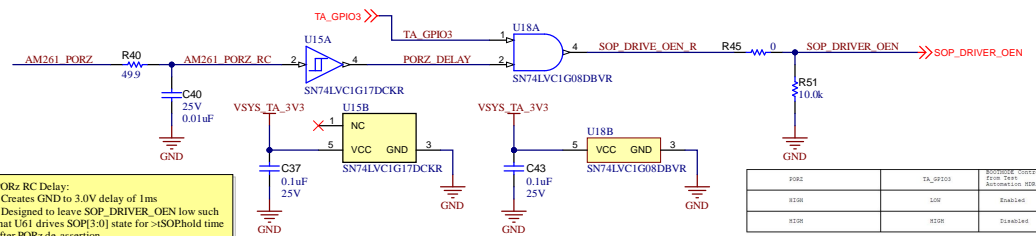


Switch Positions

Logic Low

Logic High 

PORZ SOP Driver RC Delay



PORz RC Delay:

- Creates GND to 3.0V delay of 1ms
- Designed to leave SOP_DRIVER_OEN low such that U61 drives SOP[3:0] state for $>t_{SOPhold}$ time after PORz de-assertion

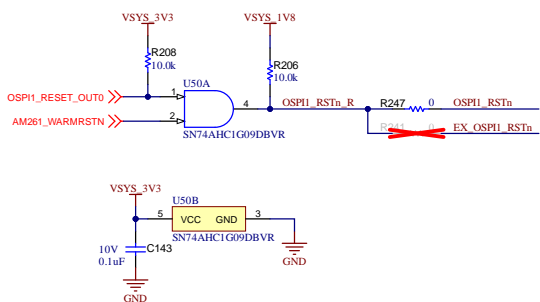
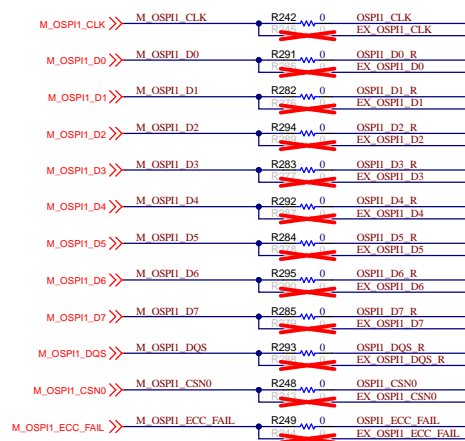
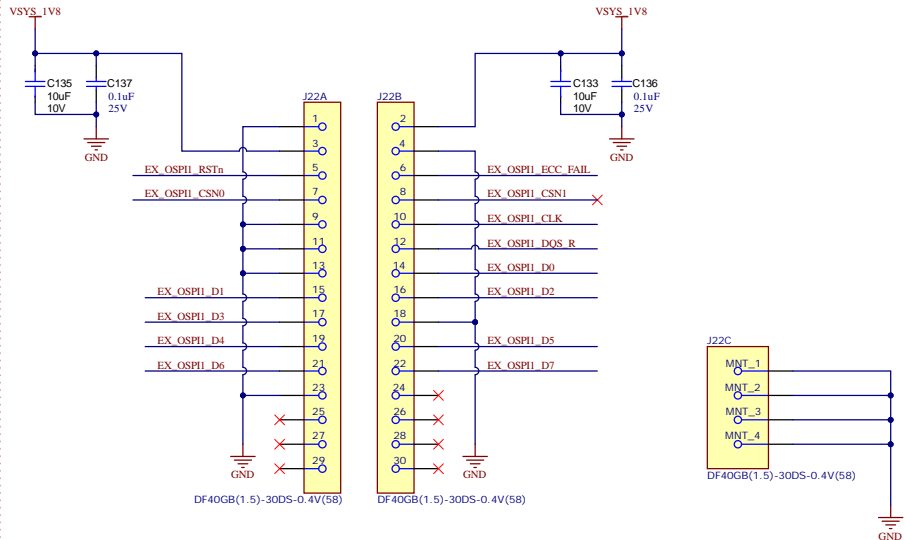
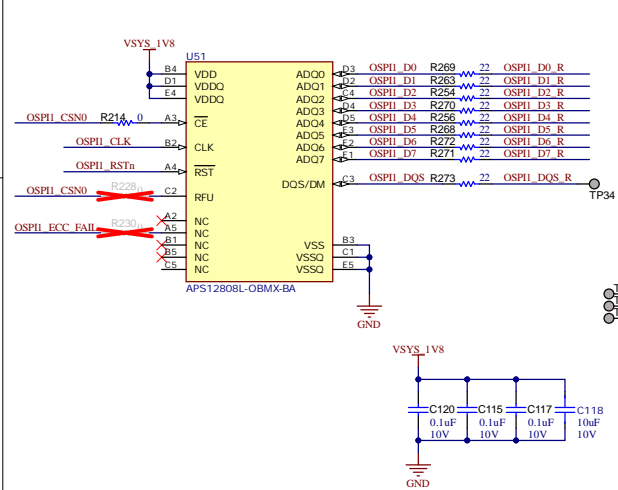
FOR2	TA_09103	Rootkit controls from Test Automation HUB
HIGH	LOW	Enabled
HIGH	HIGH	Disabled

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Orderable: LP-AM261	Designed for:	Mod. Date: 1/20/2025
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193	Rev: E2	Sheet Title: Clock Reset Boot JTAG
SVN Rev: 421	Assembly Variant: 001	Sheet 6 of 24
Drawn By: Vijetha J. Kiran	File: PROC193E2_06_Clock_Reset_Boot_JTAG_Sch	Size: B
Engineer: Vijetha J. Kiran	Contact:	





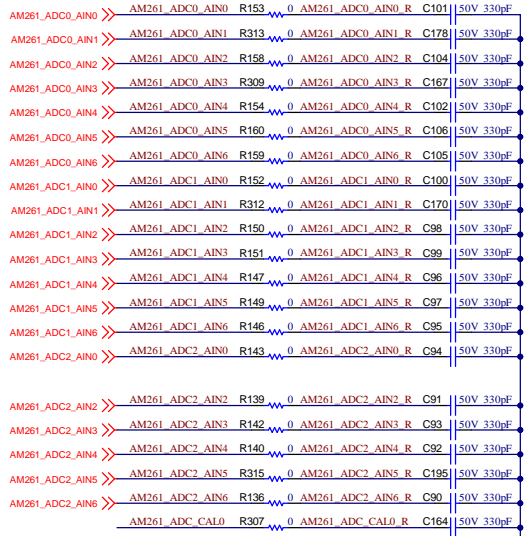


Note:

- RES R242, R291, R282, R294, R283, R292, R284, R295, R285, R293, R248, R249 and R247 will be mounted and R245, R286, R276, R289, R277, R287, R278, R290, R279, R288, R243, R244 and R241 will be DNI for the on-board OPSIS usage
- RES R242, R291, R282, R294, R283, R292, R284, R295, R285, R293, R248, R249 and R247 will be DNI and R245, R286, R276, R289, R277, R287, R278, R290, R279, R288, R243, R244 and R241 will be mounted for the External OPSIS usage

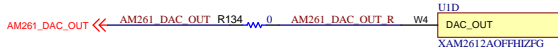
Layout Note: keep the common end of the resistors pads overlapped to avoid stub

SAR ADC RC Filtering

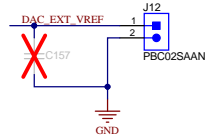


Layout Note: Place RC filters with minimal distance between components and close to MCU BGA.

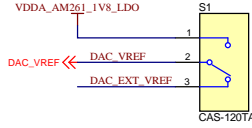
DAC Output



DAC External VREF Header

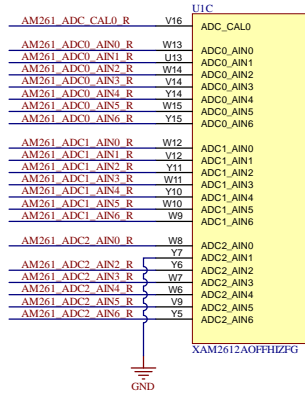


DAC VREF Select

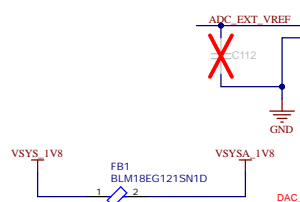


DAC VREF Switch Select - 1.8V VREF must be provided for AM261x comparators to function
- Select pins 1-2 select AM261x 1.8V analog LDO output as DAC VREF (default)
- Select pins 2-3 select external 1.8V VREF (if any provided)

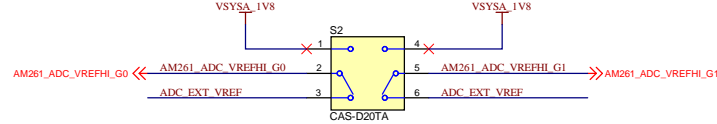
AM261x ADC and DAC



ADC External VREF Header

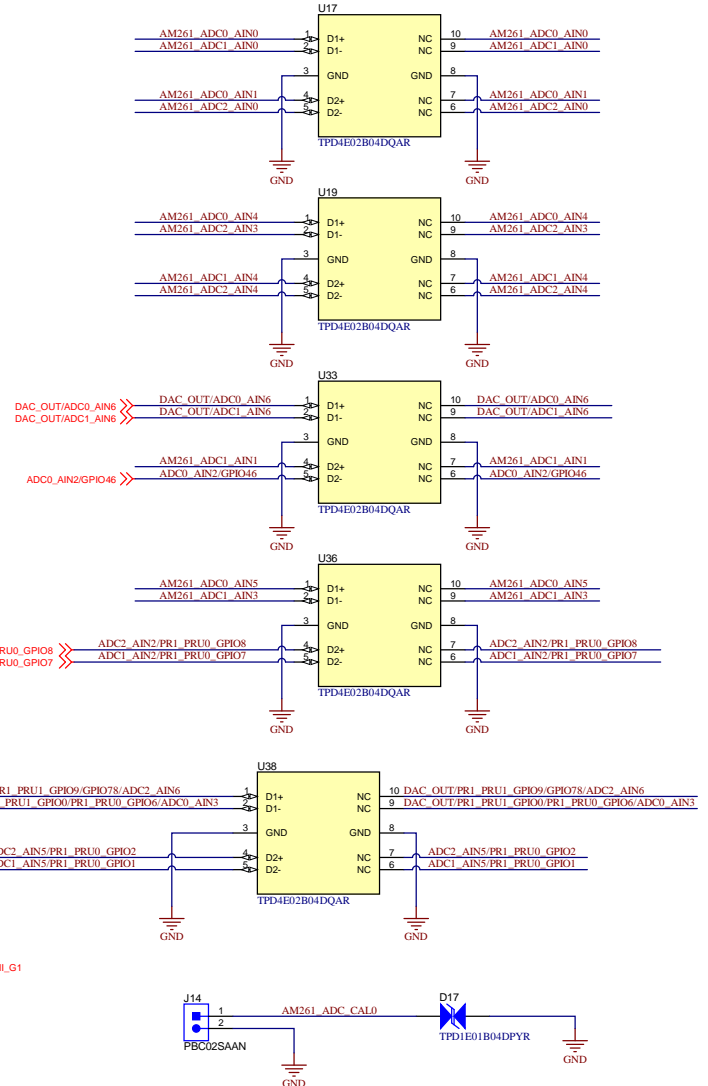


ADC VREF Select



ADC VREF Switch Select
- Switch in 1-2 position allows AM261x on-board 1.8 VREF (VSYSA_1V8) for VREFG0
- Switch in 2-3 position allows on-die ADC_VREF (default) or external 1.8V VREF (if any provided) for VREFG0
- Switch in 4-5 position allows AM261x on-board 1.8 VREF (VSYSA_1V8) for VREFG0/G1
- Switch in 5-6 position allows on-die ADC_VREF (default) or external 1.8V VREF (if any provided) for VREFG1

Layout Note: Place external ESD near connectors

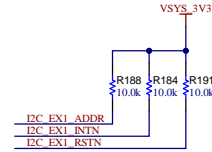
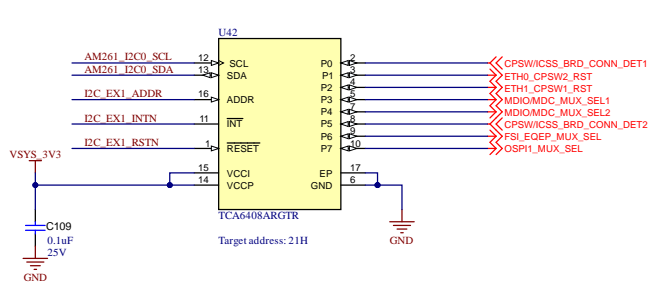


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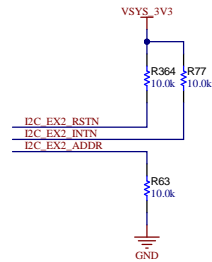
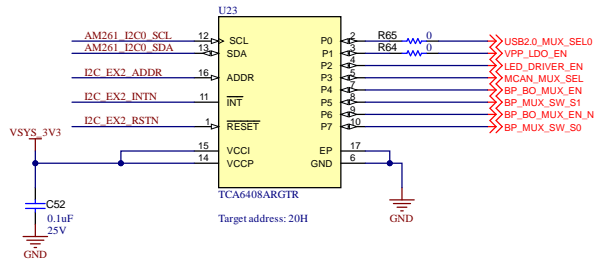
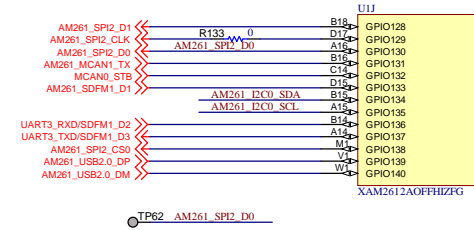
Orderable: LP-AM261	Designed for:	Mod. Date: 1/20/2025
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193	Rev: E2	Sheet Title: ADC_DAC
SVN Rev: 421	Assembly Variant: 001	Sheet: 9 of 24
Drawn By: Vijetha J. Kiran	File: PROC193E2_09_ADC_DAC_SchDoc	Size: B
Engineer: Vijetha J. Kiran	Contact:	

AM261x_USB2.0, I2C0 and IO expander

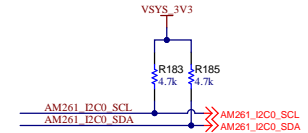
I2C IO Expanders



AM261x_USB2.0, I2C0



I2C0 Pull-Up

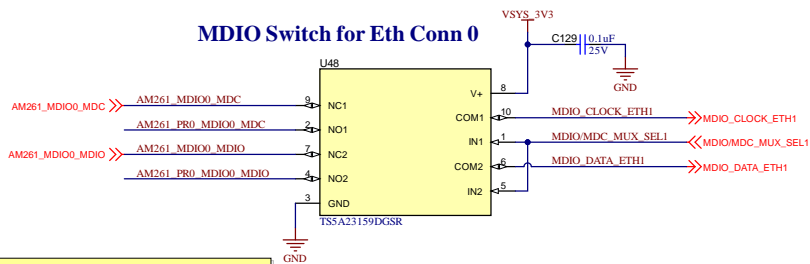


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Orderable: LP-AM261	Designed for: AM261 Launchpad	Mod. Date: 1/20/2025
TID #: N/A	Project Title: AM261x USB2.0 and I2C	
Number: PROC193	Rev: E2	Sheet: 10 of 24
SVN Rev: 423	Assembly Variant: 001	
Drawn By: Vijetha J. Kiran	File: PROC193E2_10_USB_I2C0_IOEXP_SchDoc	Size: B
Engineer: Vijetha J. Kiran	Contact:	

AM261x CPSW - RGMII and MDIO

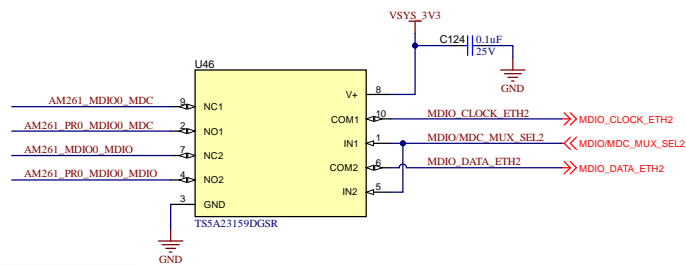
MDIO Switch for Eth Conn 0



MDIO Switch Modes
- IN1/2 pulled High by default - PR0_MDIO Selected

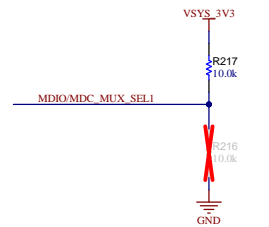
IN	NC to COM, COM to NC	NO to COM, COM to NO
L	ON	OFF
H	OFF	ON

MDIO Switch for Eth Conn 1

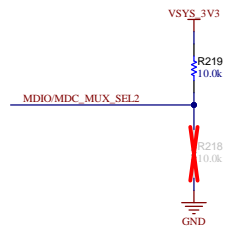
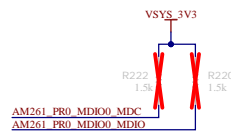


MDIO Switch Modes
- IN1/2 pulled High by default - PR0_MDIO Selected

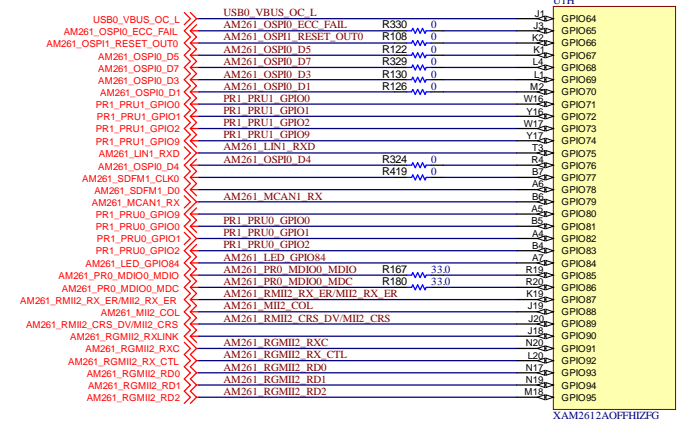
IN	NC to COM, COM to NC	NO to COM, COM to NO
L	ON	OFF
H	OFF	ON



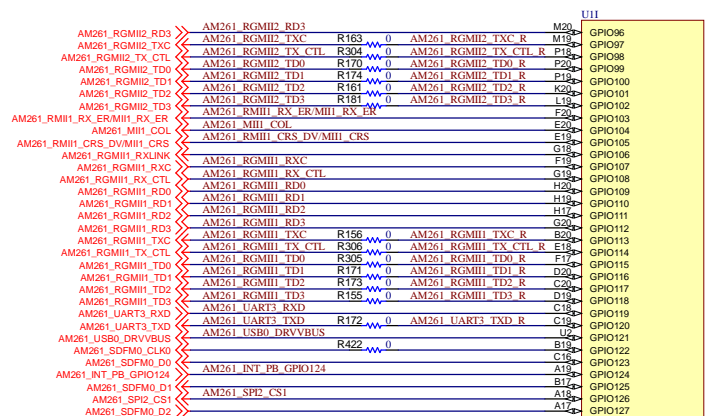
PR0 MDIO Pull-Up



- TP63 AM261_MII2_COL
- TP64 AM261_UART3_RXD
- TP65 AM261_INT_PB_GPIO124
- TP66 AM261_SP2_CS1



XAM2612A0FFHIZFG



XAM2612A0FFHIZFG

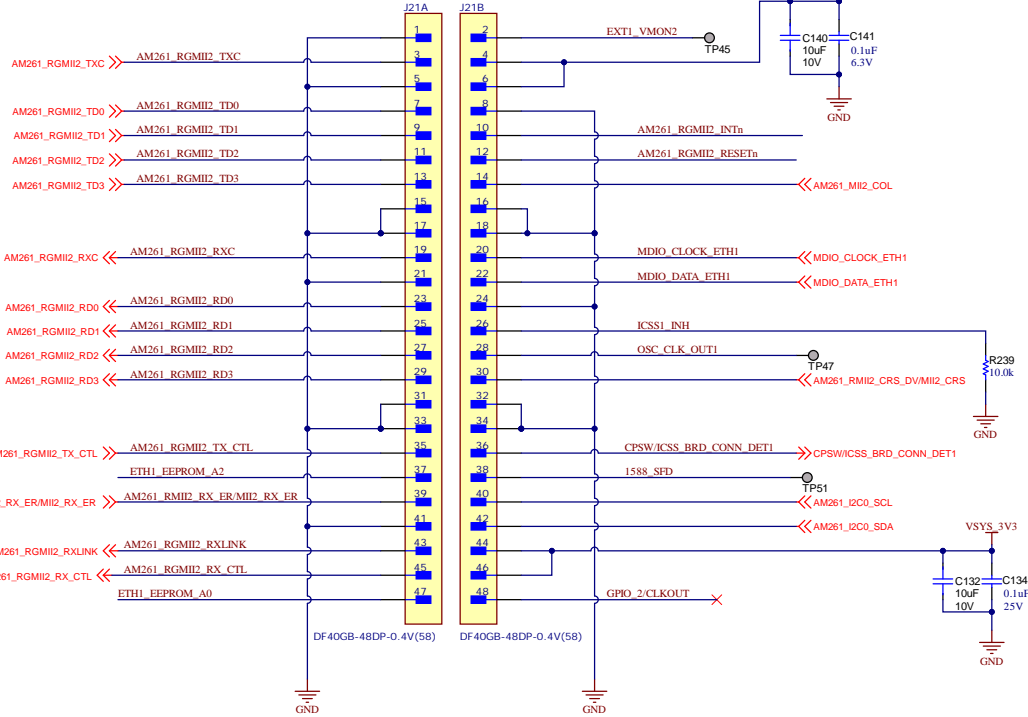
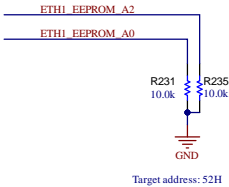
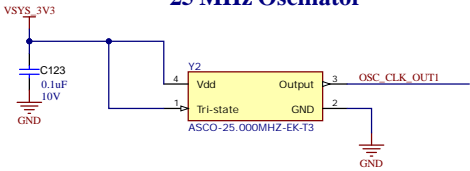
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Orderable: LP-AM261	Designed for:	Mod. Date: 1/20/2025
TID #:	Project Title: AM261 Launchpad	
Number: PROC193	Rev: E2	Sheet Title: RGMII, MDIO
SVN Rev: 423	Assembly Variant: 001	Sheet: 11 of 24
Drawn By: Vijetha J. Kiran	File: PROC193E2_11_RGMII_MII_SchDoc	Size: B
Engineer: Vijetha J. Kiran	Contact:	

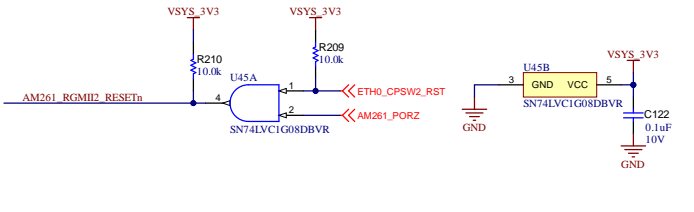
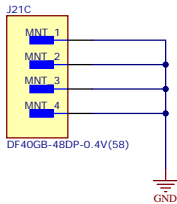
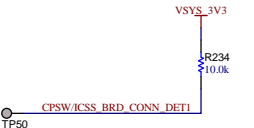
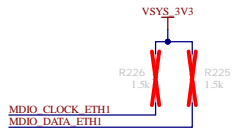
ETH PORT 0

AM261x PR0 PRU0 /CPSW2

25 MHz Oscillator



MDIO Pull-Up

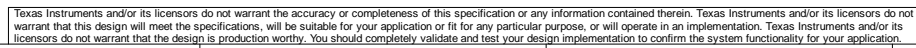


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Orderable: LP-AM261	Designed for: AM261 Launchpad	Mod. Date: 12/20/2024
TID #: N/A	Project Title: Ethernet Port 0	
Number: PROC193	Rev: E2	Sheet: 12 of 24
SVN Rev: 353	Assembly Variant: 001	
Drawn By: Vijetha J. Kiran	File: PROC193E2_12_Ethernet_Conn_0_SchDoc	Size: B
Engineer: Vijetha J. Kiran	Contact:	



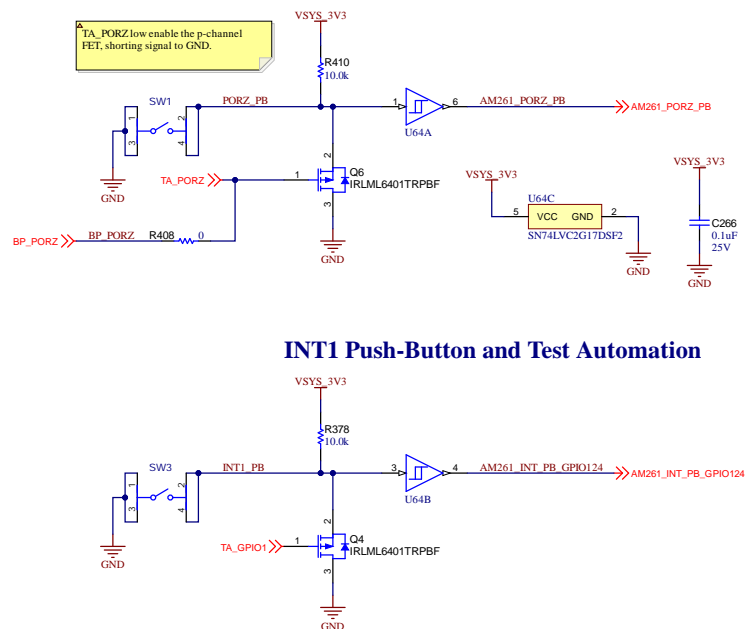
AM261x PR0 PRU1 /CPSW1



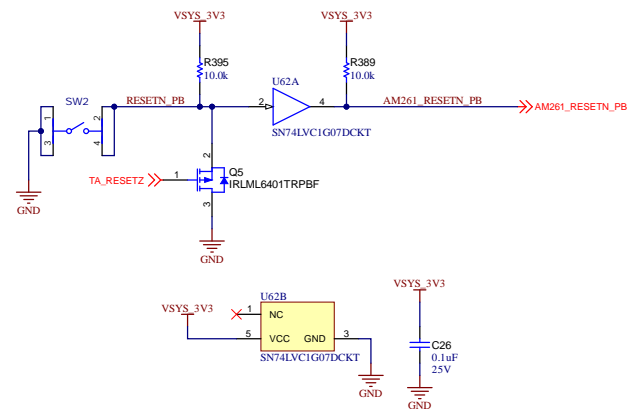
 **TEXAS
INSTRUMENTS**
<http://www.ti.com>
© Texas Instruments

Push-Buttons

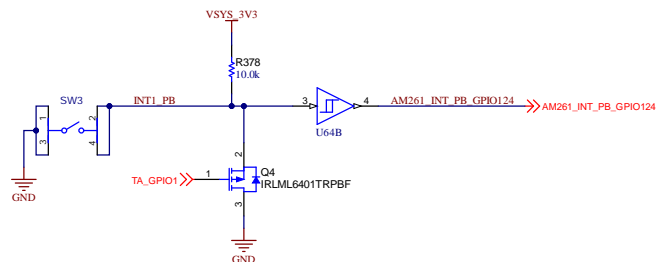
PORZ Push-Button and Test Automation




RESETZ Push-Button and Test Automation



INT1 Push-Button and Test Automation



Orderable: LP-AM261		Designed for: Mod. Date: 12/20/2024		 TEXAS INSTRUMENTS
TID: N/A		Project Title: AM261 Launchpad		
Number: PROC193	Rev: E2	Sheet Title: Push Button		
SVN Rev: 353	Assembly Variant: 001		Sheet: 14 of 24	
Drawn By: Vijetha J. Kiran	File: PROC193E2 14 Push Buttons_SchDoc		Size: B	
Engineer: Vijetha J. Kiran	CONTACT:		http://www.ti.com © Texas Instruments	

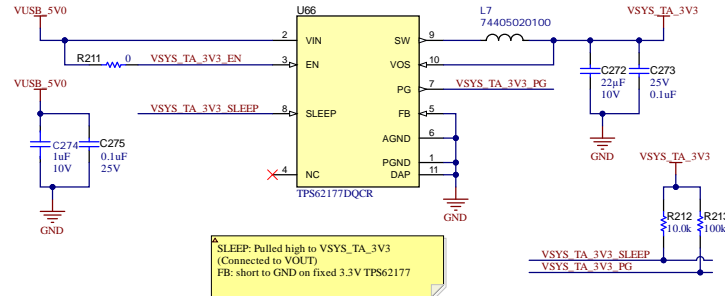
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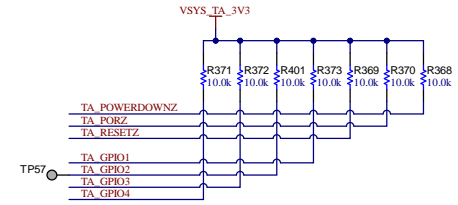
Test Automation Section

Test Automation 3.3V, 500mA Supply

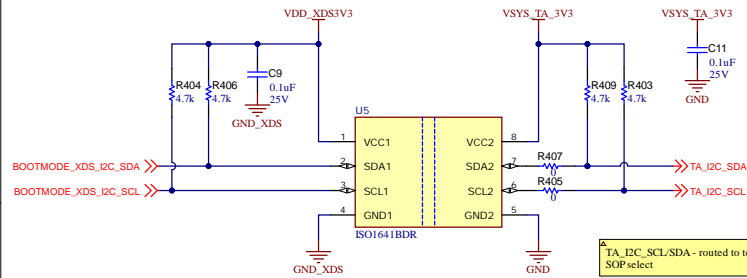
SIGNAL NAME	DESCRIPTION	Direction WRT CTRL	Internal/ External PULLUP states
TA_POWERDOWN	Used to Power down the system	OUTPUT	External Pullup
TA_PORZn	Used to Reset the SoC PORz	OUTPUT	External Pullup
TA_RESETz	SoC Warmreset	OUTPUT	External Pullup
TA_GP101	Interrupt to SOC	OUTPUT	External Pullup
TA_GP102	Used to Enable or Disable 1.2V Regulator	OUTPUT	External Pullup
TA_GP103	Used to Enable the BOOTMODE Buffer	OUTPUT	External Pullup
TA_GP104	Used Reset Bootmode IO Exp	OUTPUT	External Pullup



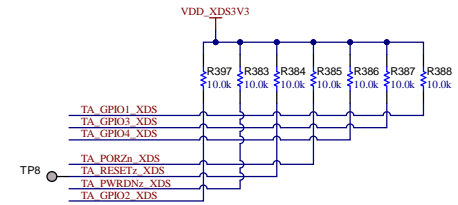
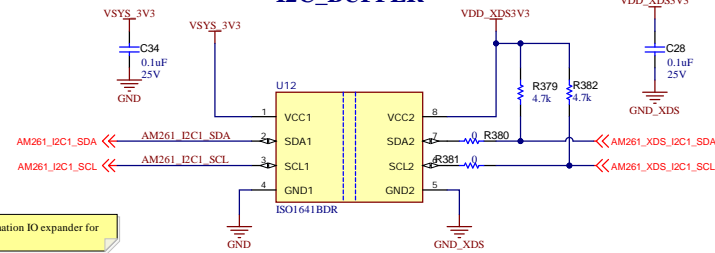
This Pulls provides a defined logic state to the Test Automation signals before XDS110 firmware is loaded



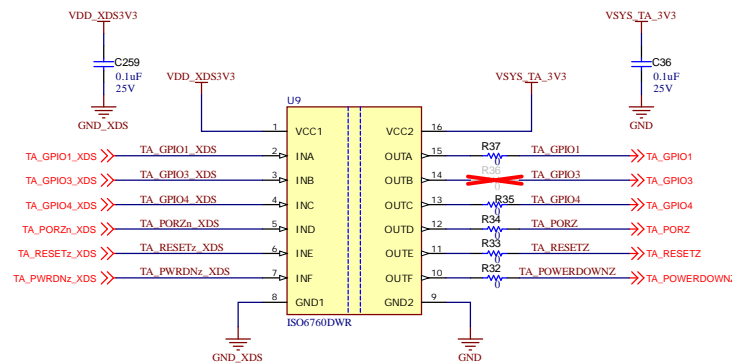
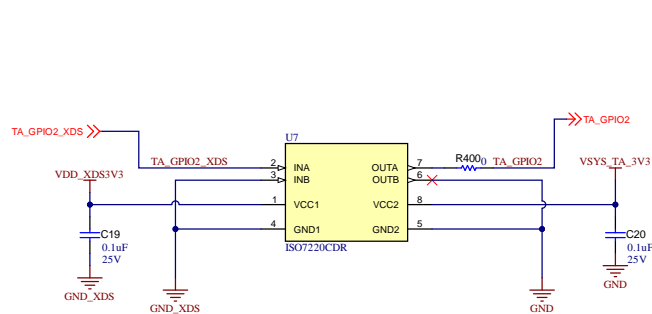
BOOTMODE_I2C_TA BUFFER



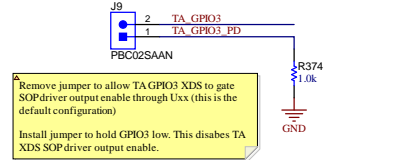
I2C_BUFFER



ISOLATION BUFFERS FOR TA SIGNALS



Test-Automation PORz Override

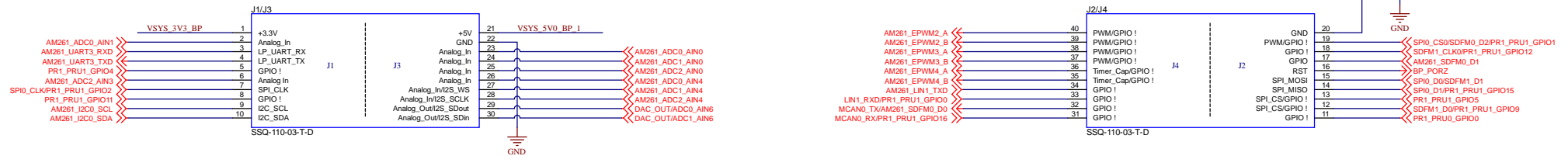


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Orderable: LP-AM261	Designed for: Project Title: AM261 Launchpad	Mod. Date: 12/20/2024
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193	Rev: E2	Sheet Title: Test Automation
SVN Rev: 353	Assembly Variant: 001	Sheet: 15 of 24
Drawn By: Vijetha J. Kiran	File: PROC193E2_15_Test_Automation_SchDoc	Size: B
Engineer: Vijetha J. Kiran	Contact:	

Boosterpack Headers

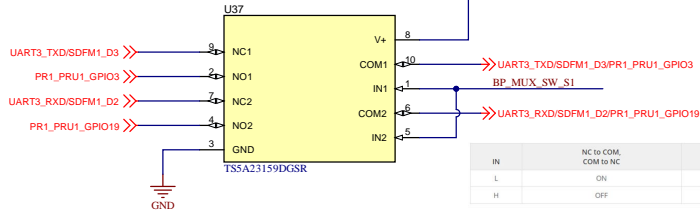
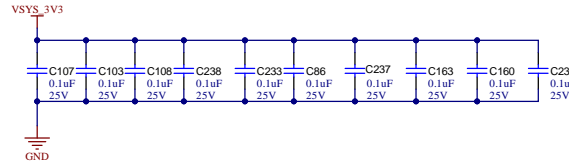
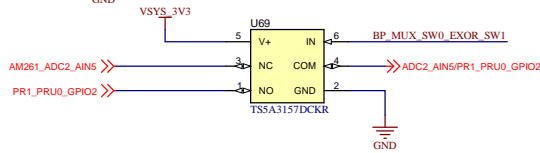
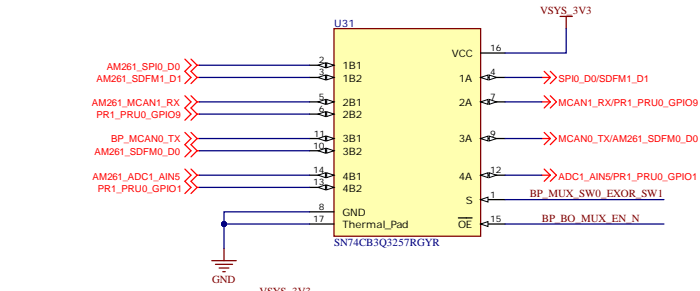
Boosterpack Site 1



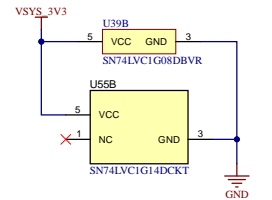
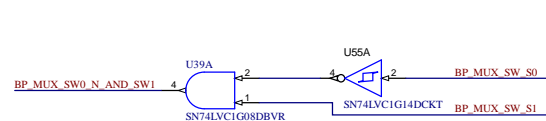
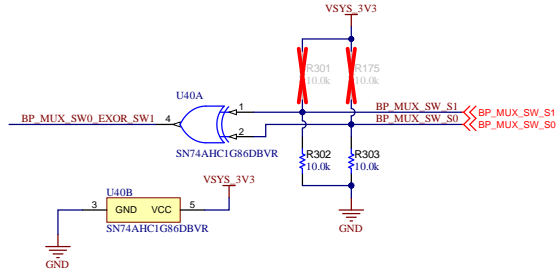
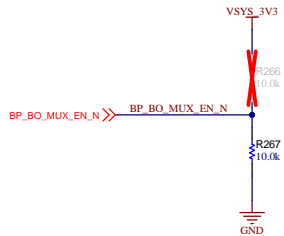
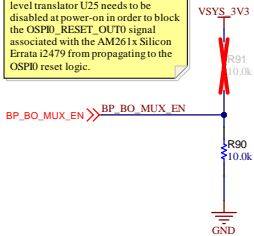
Alternate Boosterpack Function MUX

Boosterpack MUX modes

BP_MUX_SW_S1	BP_MUX_SW_S0	BP board Usage
LOW	LOW	Standard LP/BP
LOW	HIGH	BP-AM2BLDCSERVO
HIGH	LOW	IO-LINK
HIGH	HIGH	C2000 DRVx BP



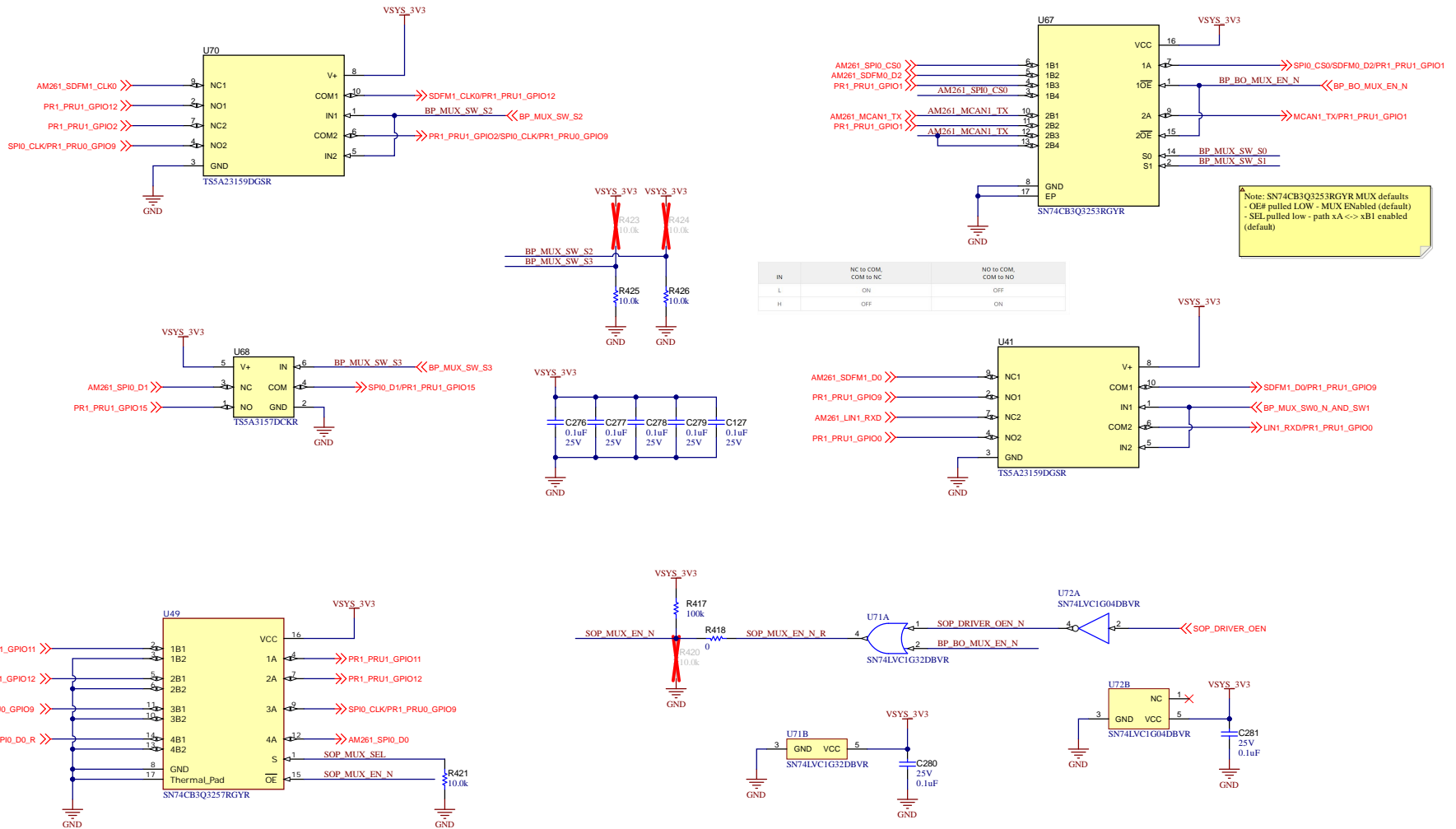
Note: R91 can be populated only if OSPI boot mode is not desired. The level translator U25 needs to be disabled at power-on in order to block the OSPI0_RESET_OUT0 signal associated with the AM261x Silicon Errata 12479 from propagating to the OSPI0 reset logic.



Orderable: LP-AM261	Designed for: AM261 Launchpad	Mod. Date: 4/24/2025
TID #: N/A	Project Title: BP Muxes	
Number: PROC193	Rev: E2	Sheet Title: BP Muxes
SVN Rev: 446	Assembly Variant: 001	Sheet: 17 of 23
Drawn By: Vijetha J. Kiran	File: PROC193E2_17_BPMuxes.SchDoc	Size: B
Engineer: Vijetha J. Kiran	Contact:	

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Alternate Boosterpack Function MUX



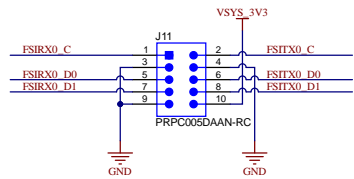
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Orderable: LP-AM261	Designed for:	Mod. Date: 1/21/2025
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193	Rev: E2	Sheet Title: BP Muxes1
SVN Rev: 424	Assembly Variant: 001	Sheet: 18 of 24
Drawn By: Vijetha J. Kiran	File: PROC193E2_18_BP Muxes1.SchDoc	Size: B
Engineer: Vijetha J. Kiran	Contact:	

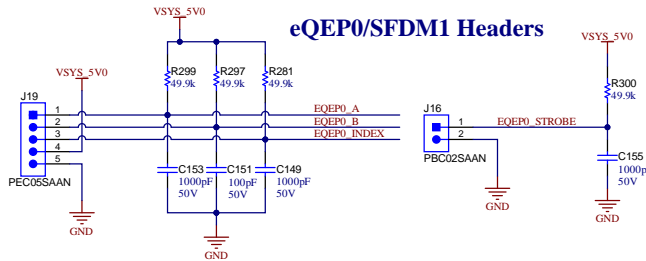
Breakout Headers

FSI Header

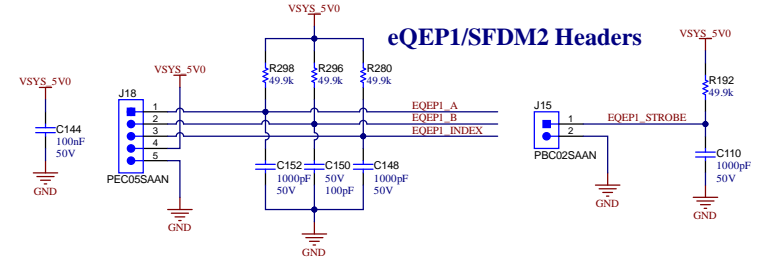
C2000 LP Style FSI Breakout



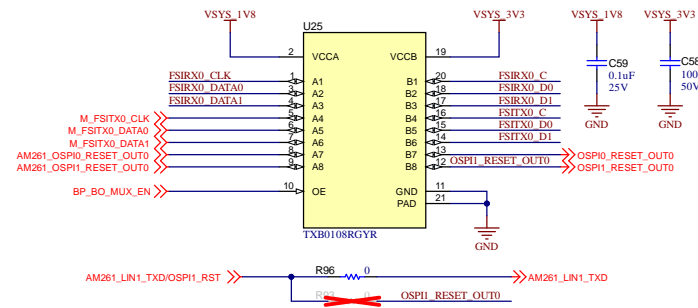
eQEP0/SFDM1 Headers



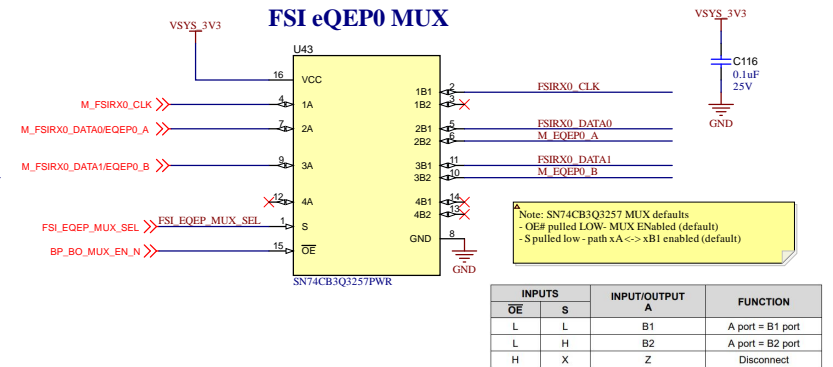
eQEP1/SFDM2 Headers



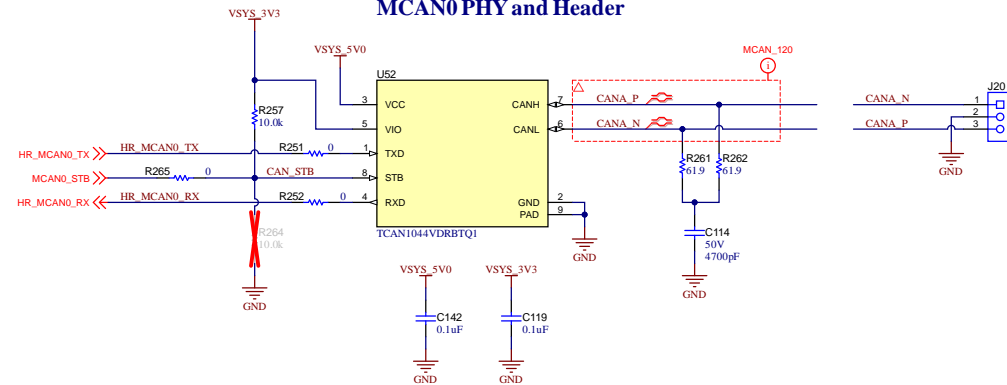
FSI Bi-Directional Level Translator



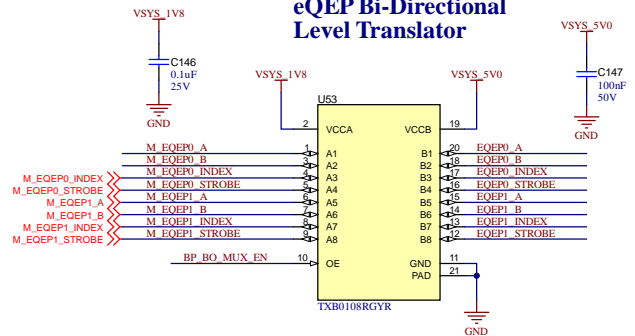
FSI eQEP0 MUX



MCAN0 PHY and Header



eQEP Bi-Directional Level Translator

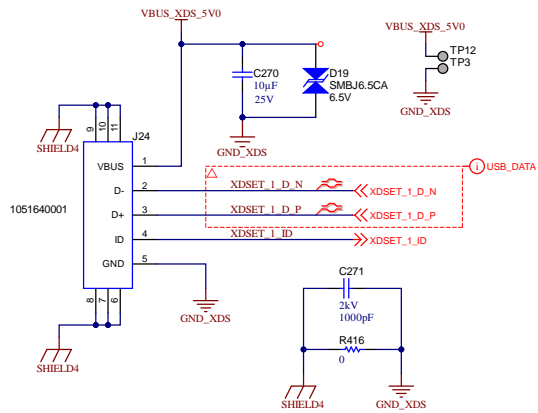


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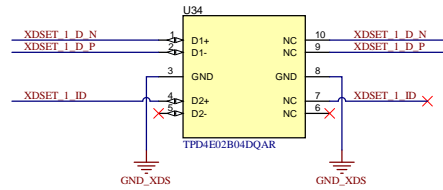
Orderable: LP-AM261	Designed for: AM261 Launchpad	Mod. Date: 12/20/2024
TID #: N/A	Project Title: Breakout Headers	
Number: PROC193	Rev: E2	Sheet Title: Breakout Headers
SVN Rev: 353	Assembly Variant: 001	Sheet: 19 of 24
Drawn By: Vijetha J. Kiran	File: PROC193E2_19_Breakout_Headers_SchDoc	Size: B
Engineer: Vijetha J. Kiran	Contact:	

XDS110 JTAG/USB-to-UART Bridge

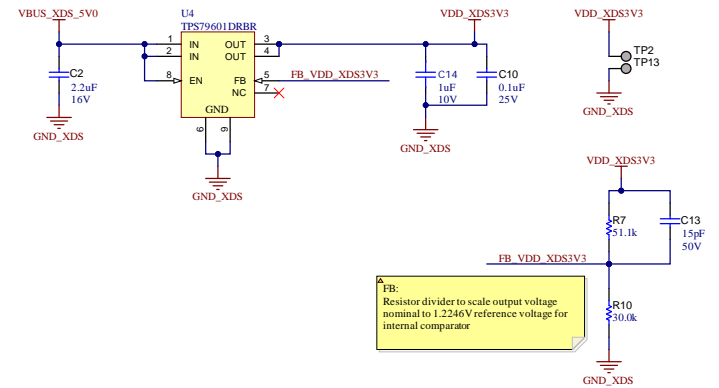
XDS110 USB Micro-B PORT



USB Mini-B ESD Protection



XDS110 3.3V LDO

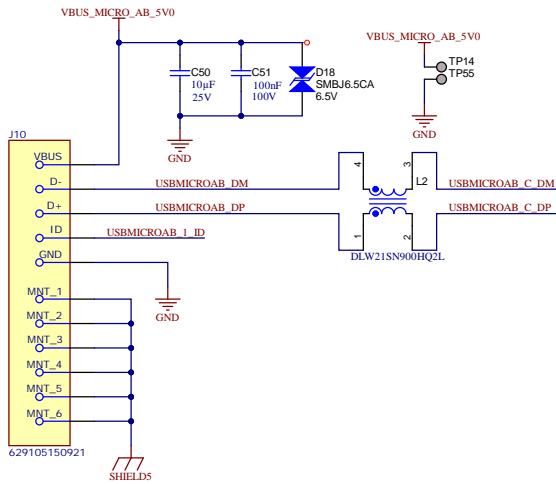


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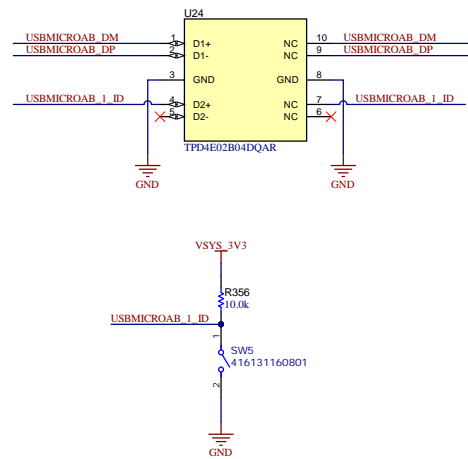
Orderable: LP-AM261	Designed for: AM261 Launchpad	Mod. Date: 1/20/2025
TID #: N/A	Project Title: XDS110 /USB2.0 Micro-B Port	
Number: PROC193	Rev: E2	Sheet: 20 of 24
SVN Rev: 421	Assembly Variant: 001	Size: B
Drawn By: Vijetha J. Kiran	File: PROC193E2_20_USB_XDS110.SchDoc	
Engineer: Vijetha J. Kiran	Contact:	

USB Micro-AB 2.0

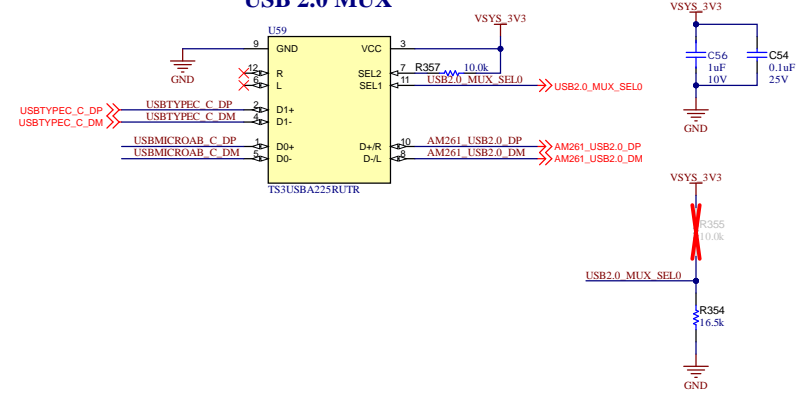
USB2.0 Micro_AB PORT



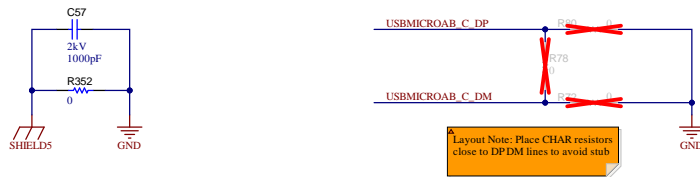
USB Micro-AB ESD Protection



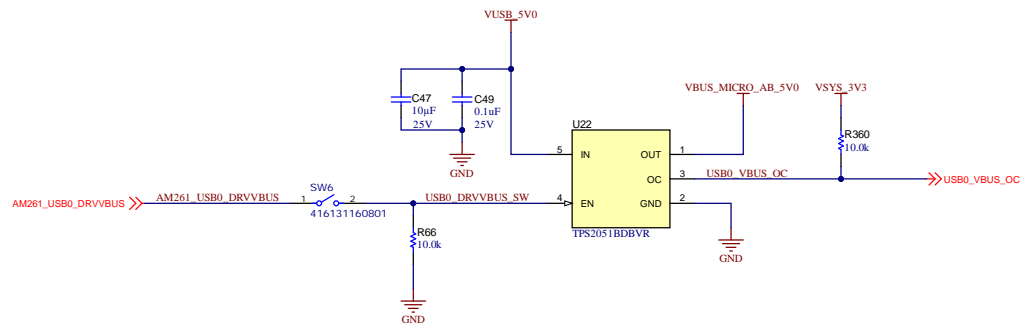
USB 2.0 MUX



USB 2.0 characterization Res



USB micro AB Power-Distribution Switch

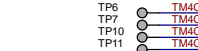
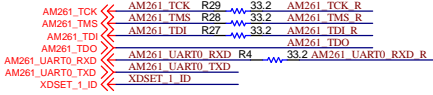


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Orderable: LP-AM261	Designed for: AM261 Launchpad	Mod. Date: 12/20/2024
TID #: N/A	Project Title: USB Micro-AB 2.0	
Number: PROC193	Rev: E2	Assembly Variant: 001
SVN Rev: 353	File: PROC193E2_21_USB_TYPE_MicroAB_SchDoc	Size: B
Drawn By: Vijetha J. Kiran	Engineer: Vijetha J. Kiran	Contact:

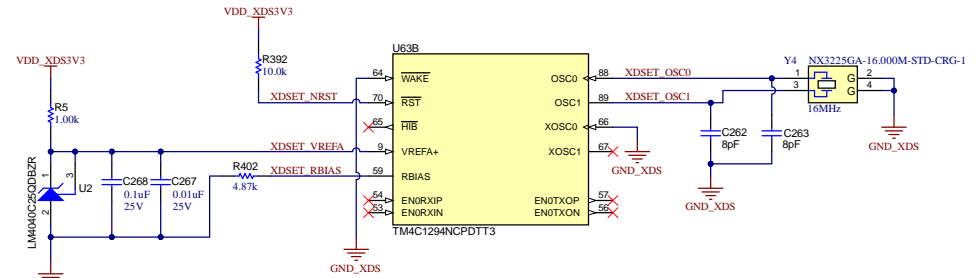
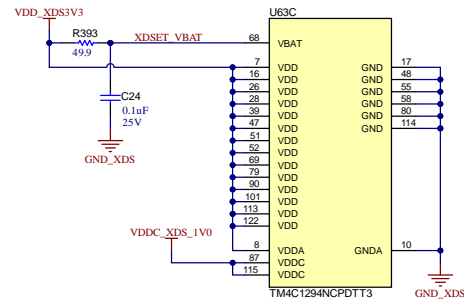
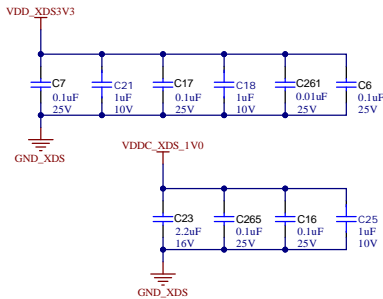
TM4C1294NCPDT Datasheet
XDS110 Collateral

XDS110 JTAG/USB-to-UART Bridge



Note: C264 shorts GND_XDS and GND.
This should be done close to the
ISO7721DR components, bridging the
air-gap between the two portions of the
PCB

XDS110 DECOUPLING CAPS

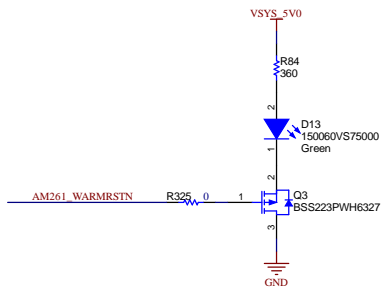


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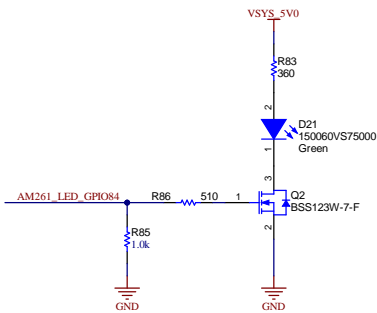
Orderable: LP-AM261	Designed for: AM261 Launchpad	Mod. Date: 12/20/2024
TID #: N/A	Project Title: XDS110 JTAG/USB-to-UART Bridge	Sheet: 22 of 24
Number: PROC193	Rev: E2	Assembly Variant: 001
SVN Rev: 353	File: PROC193E2_22_XDS110_2_SchDoc	Size: B
Drawn By: Vijetha J. Kiran	Engineer: Vijetha J. Kiran	Contact:

System LED Indicators

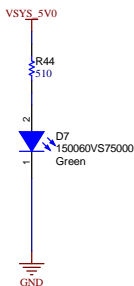
AM261_WARMRSTN



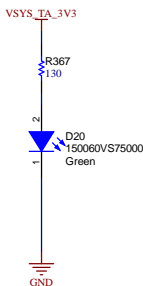
AM261 GPIO LED



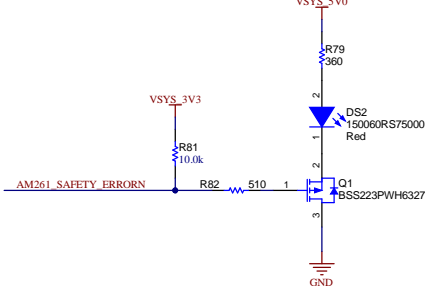
System 5.0V



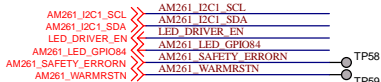
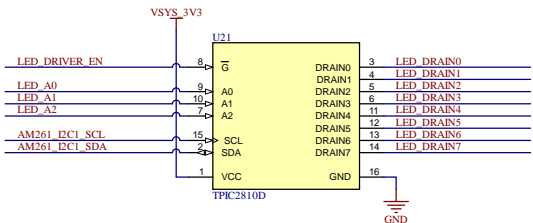
Test Automation 3.3V



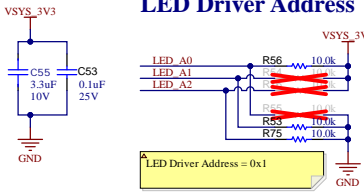
AM261x Safety Error



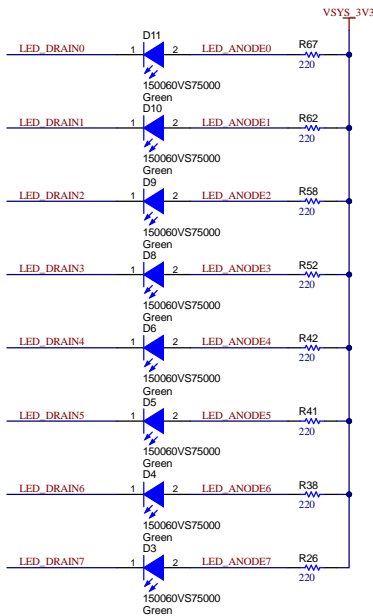
Industrial LED Driver



LED Driver Address



LED Driver Enable



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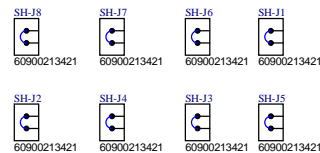
Orderable: LP-AM261	Designed for: AM261 Launchpad	Mod. Date: 12/20/2024
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193	Sheet Title: System LED Indicators	
SVN Rev: 373	Assembly Variant: 001	Sheet: 23 of 24
Drawn By: Vijetha J. Kiran	File: PROC193E2_23_LED_SchDoc	Size: B
Engineer: Vijetha J. Kiran	Contact:	

System Hardware, Notes, Labels

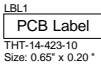
PCB Fiducials



Included Jumpers



PCB Labels and Silkscreen



PCB Labels and Silkscreen

ZZ4
Label Assembly Note
This Assembly Note is for PCB labels only


ZZ3
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ1
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: LP-AM261	Designed for:	Mod. Date: 12/20/2024
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193	Rev: E2	Sheet Title: System Hardware, Notes, Labels
SVN Rev: 353	Assembly Variant: 001	Sheet: 24 of 24
Drawn By: Vijetha J. Kiran	File: PROC193E2_24_Systemhardware_notes_labels_Sch06c	
Engineer: Vijetha J. Kiran	Contact:	

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