

MAXWELL INDUSTRIAL APPLICATION BOARD

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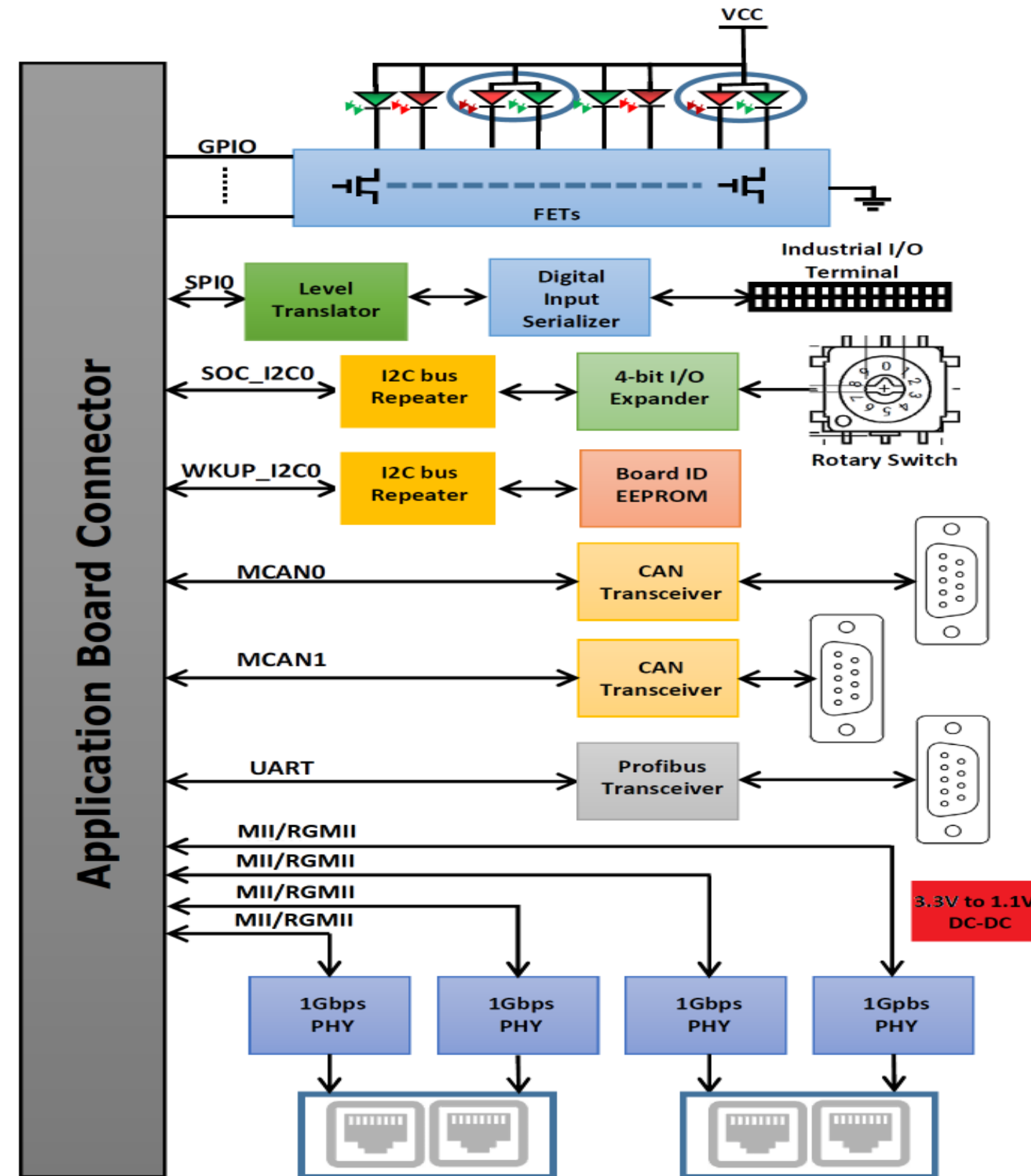
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REV	A
VER	1.0

REVISION HISTORY

VER #	DATE	DESCRIPTION OF CHANGES	AUTHOR	REVIEWED BY	APPROVED BY
0.1	30th JAN 2020	Drafted from Rev E4, Ver 1.0 schematics.	Mistral Design Team	AJIT MB	AJIT MB
0.2	30th JAN 2020	Installed R130 and R252	Mistral Design Team	AJIT MB	AJIT MB
0.3	20th FEB 2020	Updated alternate for 0.1uF capacitor	Mistral Design Team	AJIT MB	AJIT MB
1.0	20th FEB 2020	Baselined	Mistral Design Team	AJIT MB	AJIT MB

BLOCK DIAGRAM

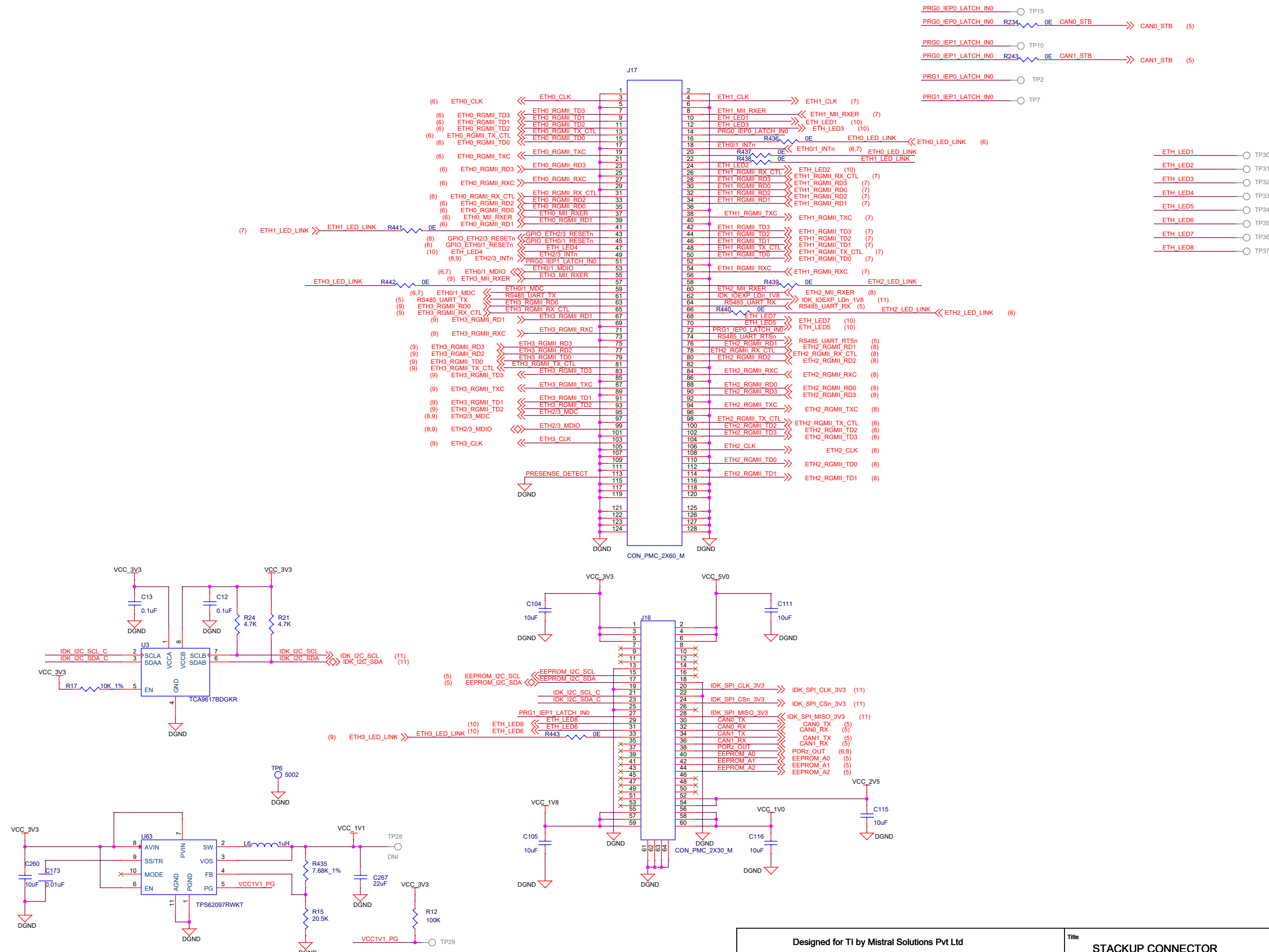


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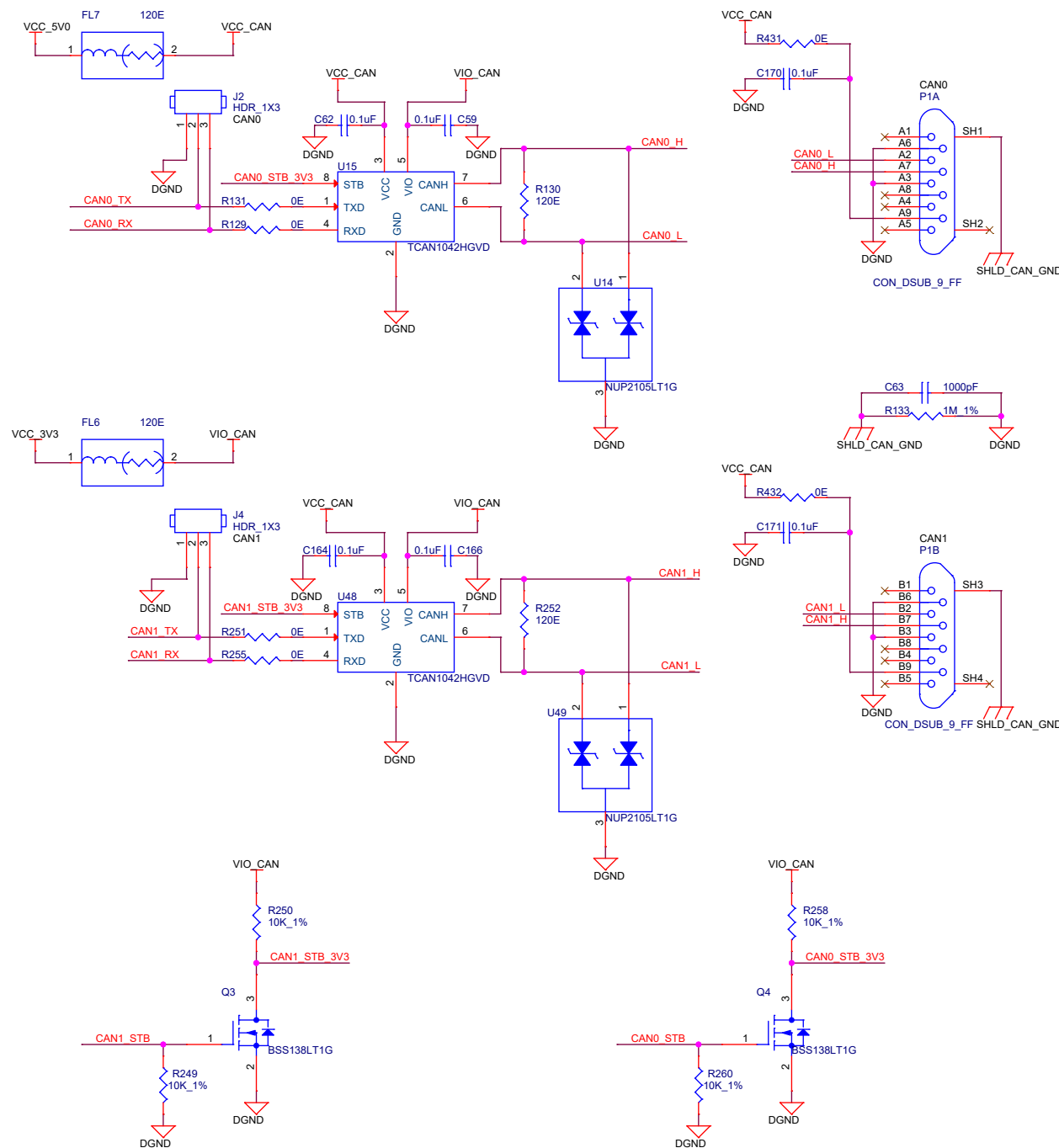


Title BLOCK DIAGRAM

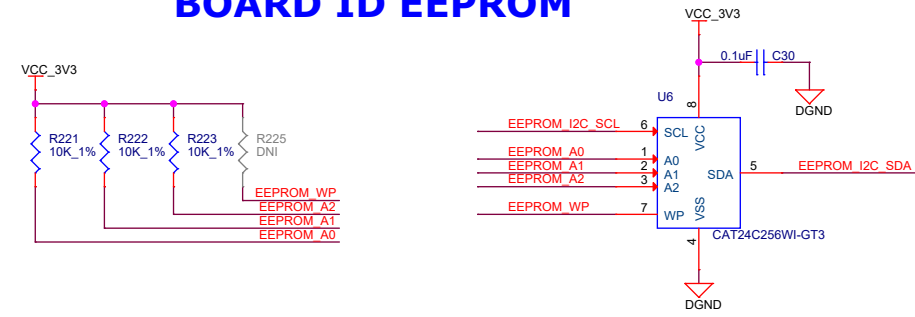
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CAN INTERFACE

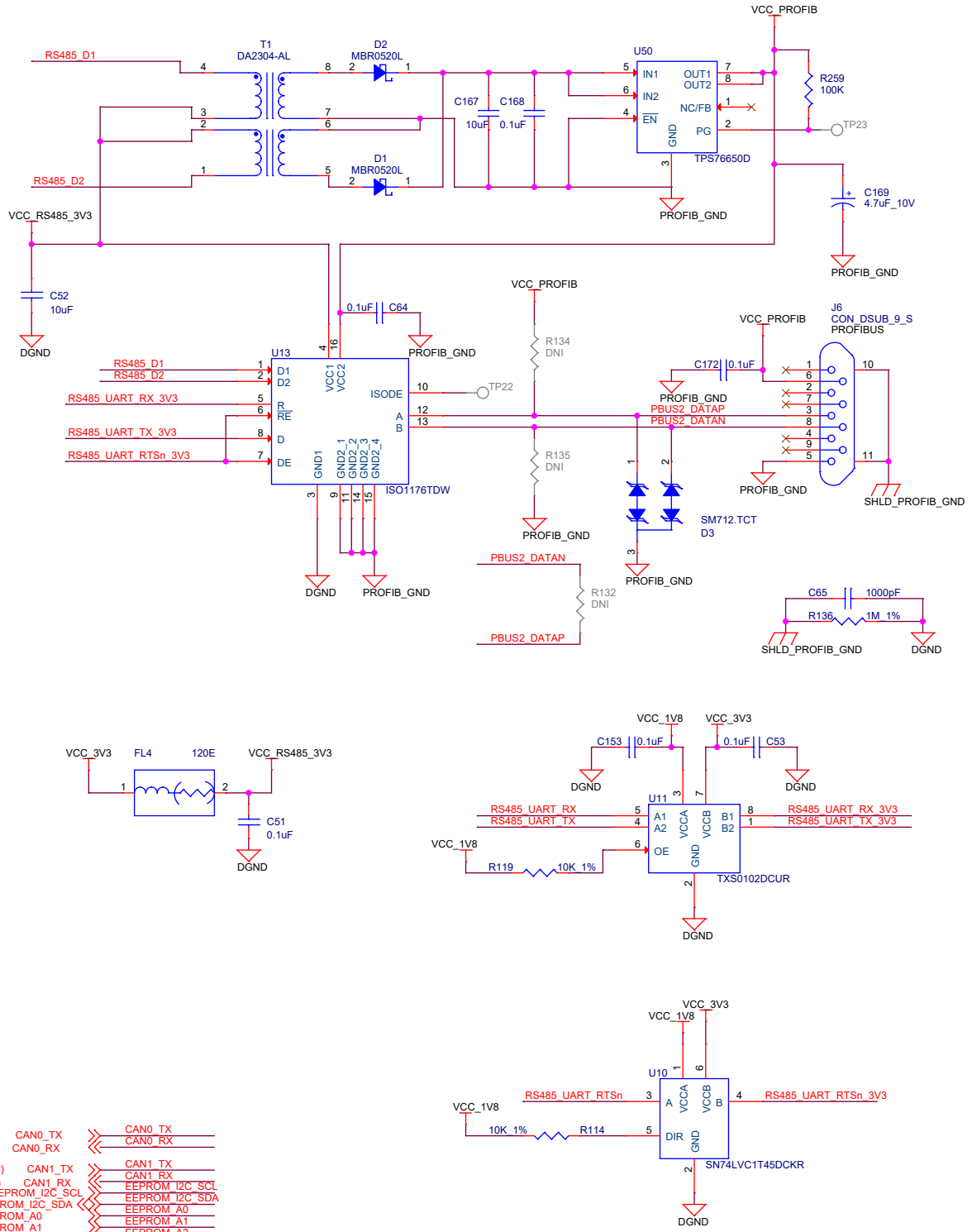


BOARD ID EEPROM



I2C address: 0x52h or Set by CP board

RS485 INTERFACE

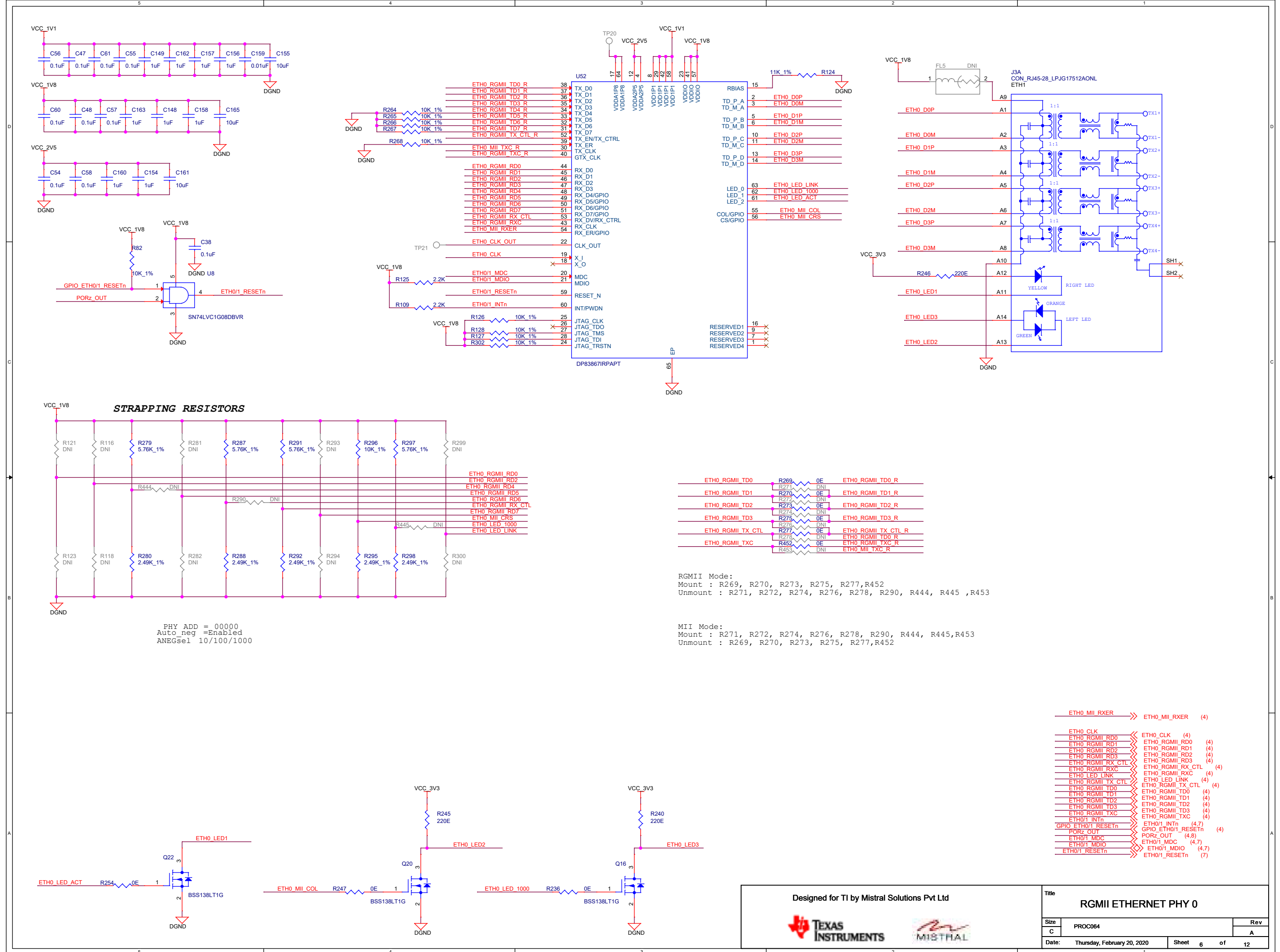


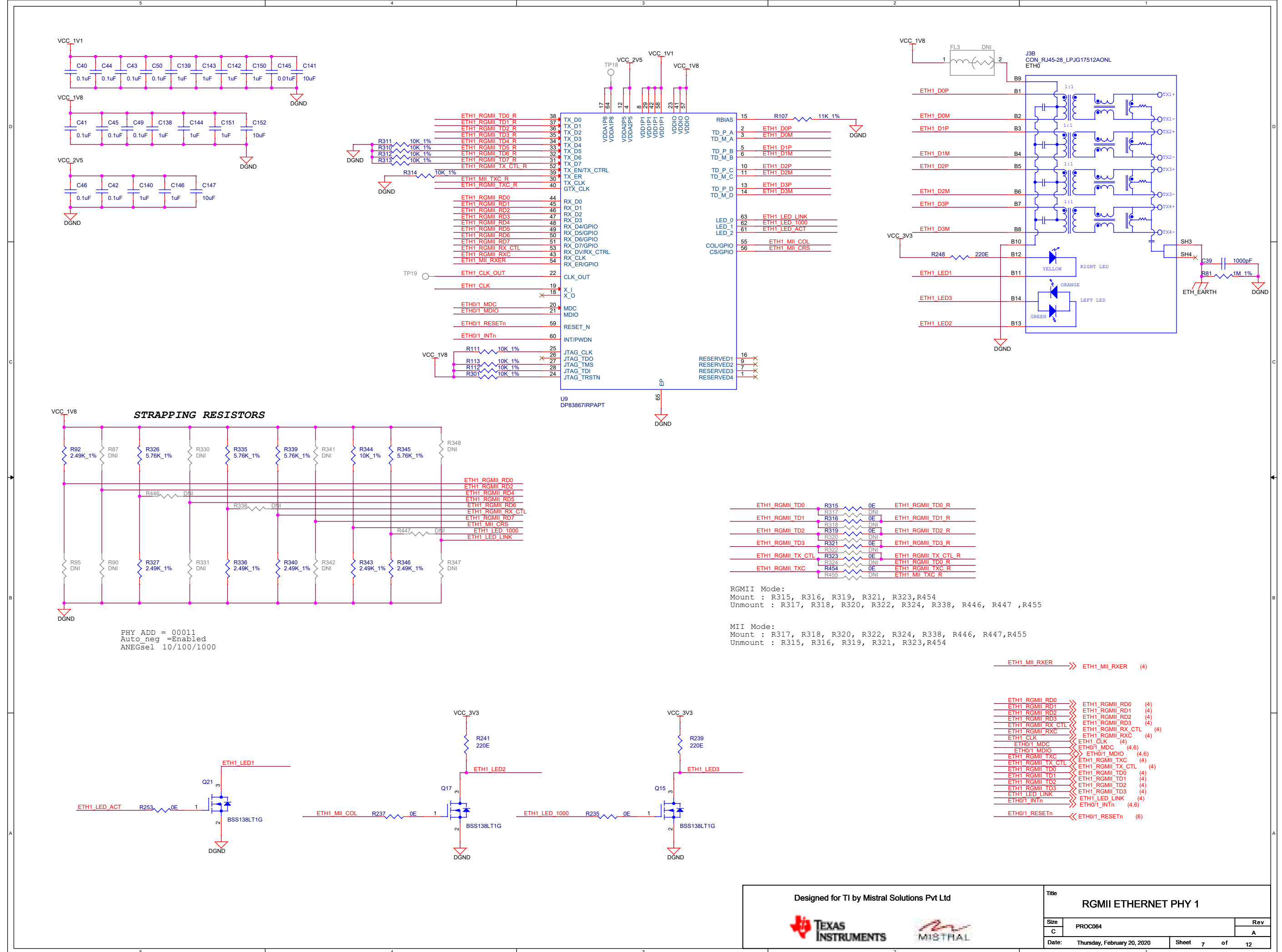
- (4) CAN0_TX >>> CAN0_TX
- (4) CAN0_RX >>> CAN0_RX
- (4) CAN1_TX >>> CAN1_TX
- (4) CAN1_RX >>> CAN1_RX
- (4) EEPROM_I2C_SCL >>> EEPROM_I2C_SCL
- (4) EEPROM_I2C_SDA >>> EEPROM_I2C_SDA
- (4) EEPROM_A0 >>> EEPROM_A0
- (4) EEPROM_A1 >>> EEPROM_A1
- (4) EEPROM_A2 >>> EEPROM_A2
- (4) RS485_UART_RX <<< RS485_UART_RX
- (4) RS485_UART_RTSn <<< RS485_UART_RTSn
- (4) RS485_UART_TX <<< RS485_UART_TX
- (4) CAN0_STB >>> CAN0_STB
- (4) CAN1_STB >>> CAN1_STB

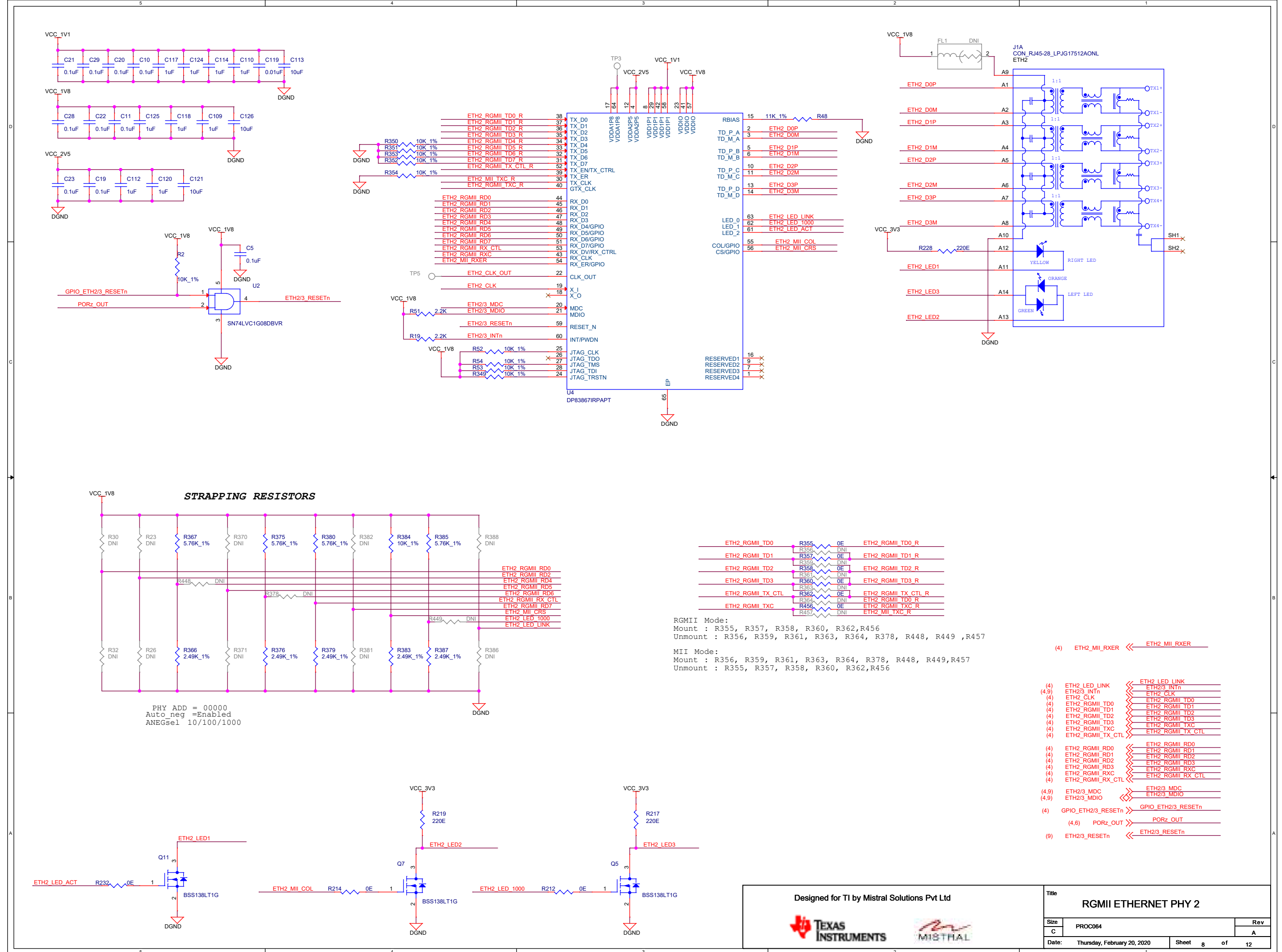
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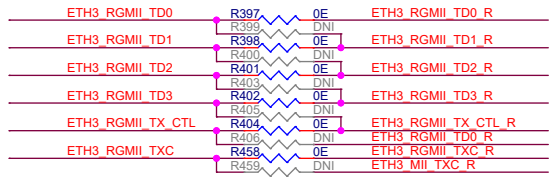
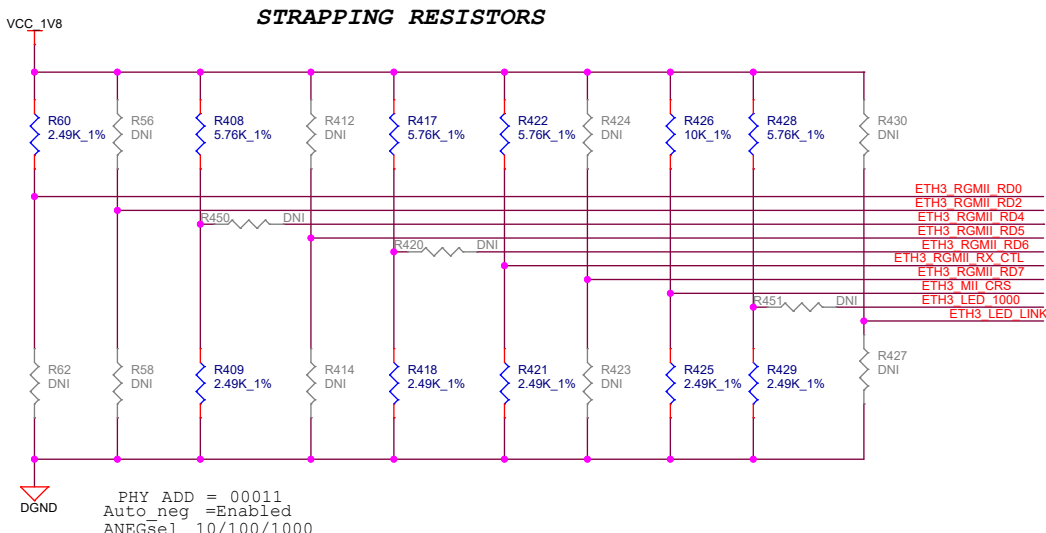
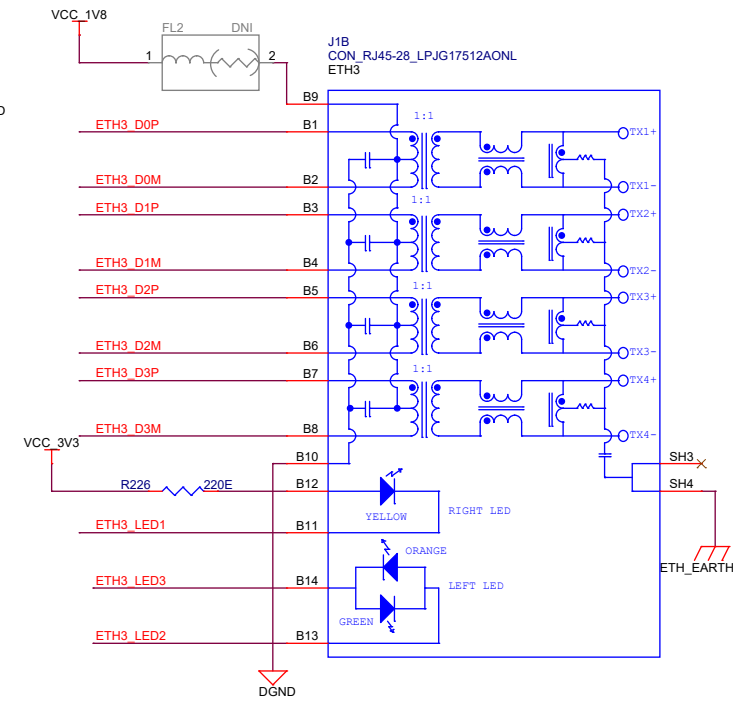
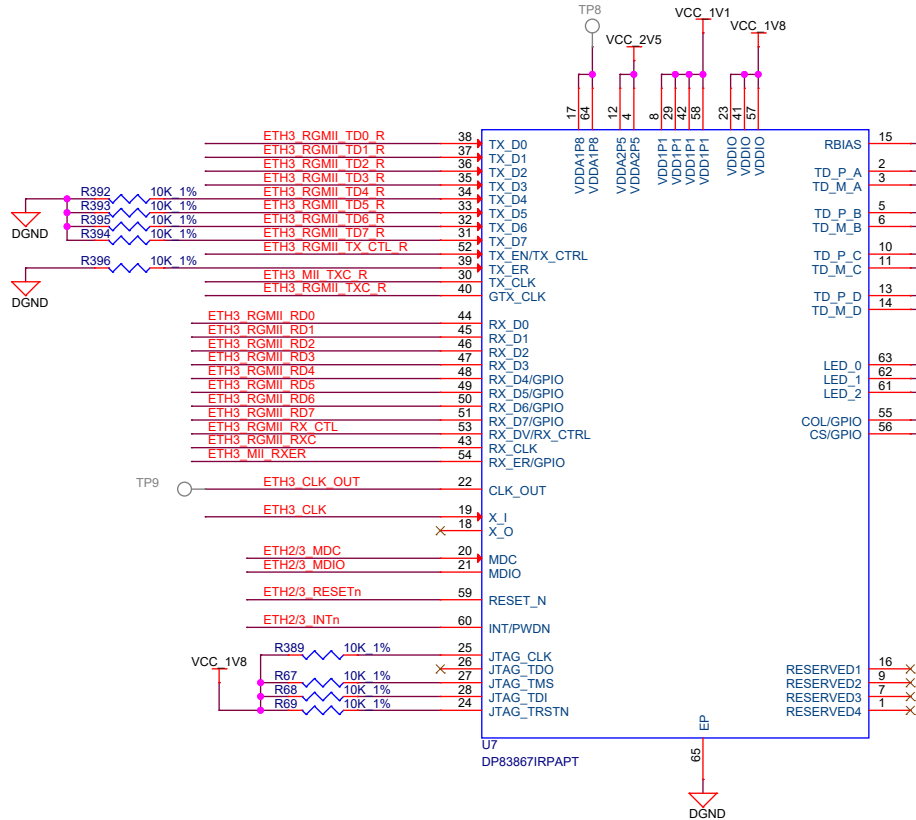
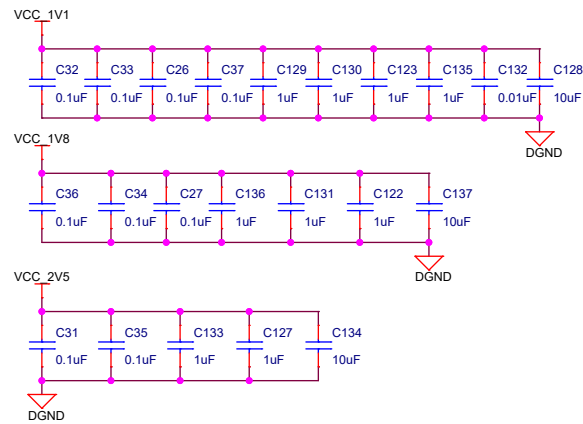


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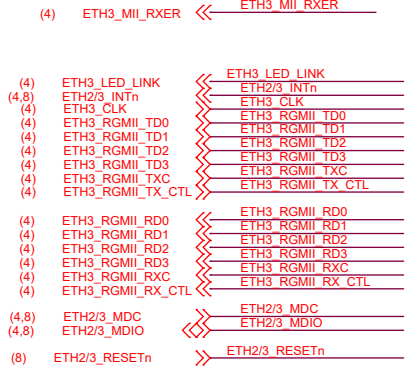
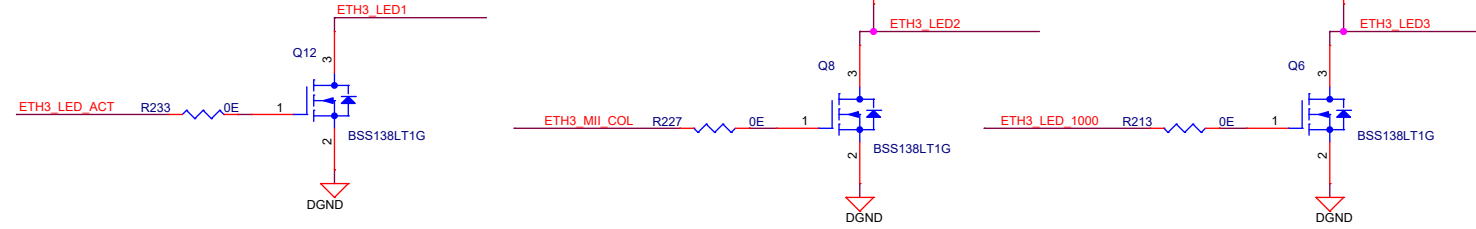


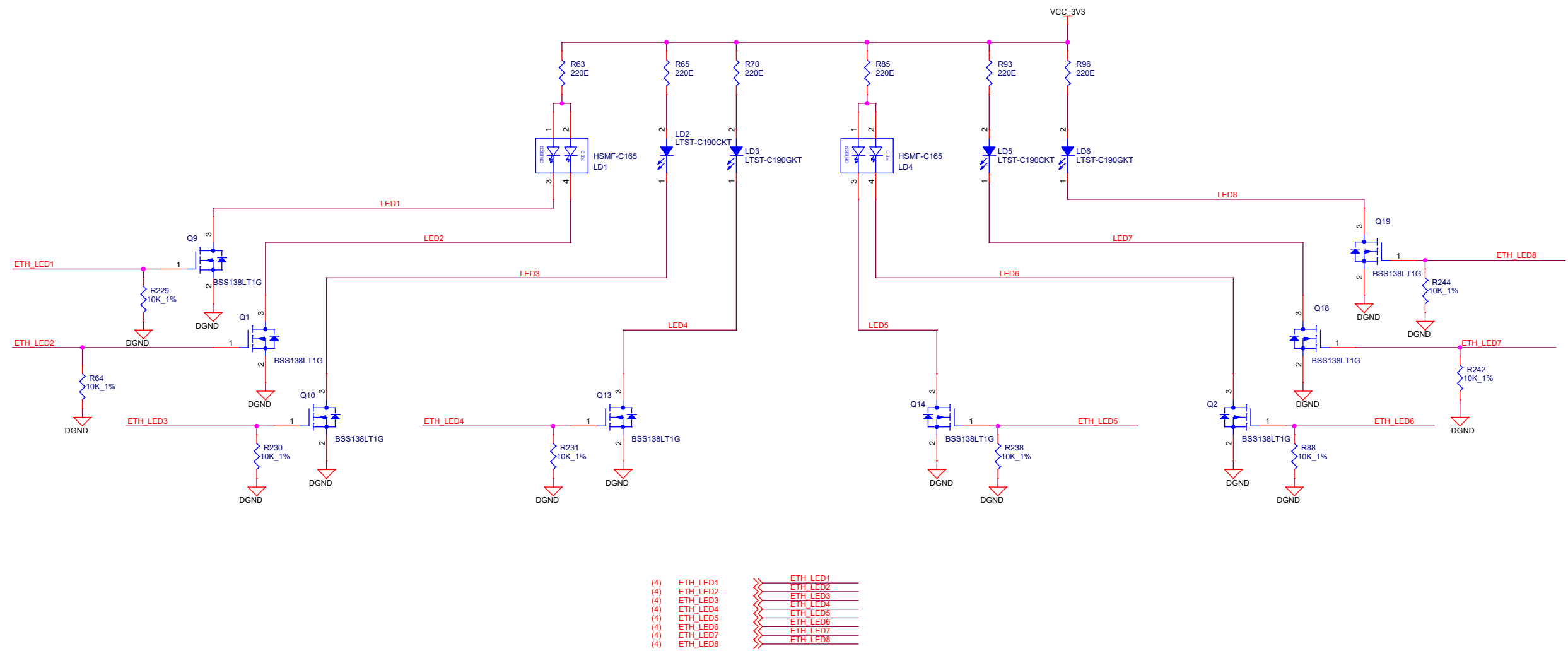




RGMII Mode:
Mount : R397, R398, R401, R402, R404,R458
Unmount : R399, R400, R403, R405, R406, R420, R450, R451,R459

MII Mode:
Mount : R399, R400, R403, R405, R406, R420, R450, R451,R459
Unmount : R397, R398, R401, R402, R404,R458

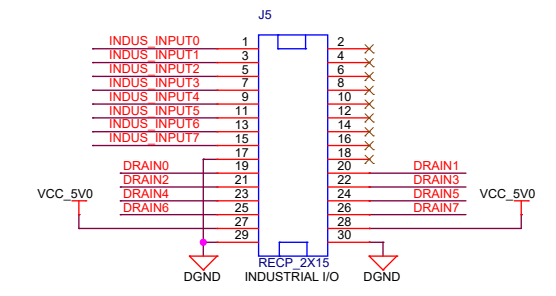
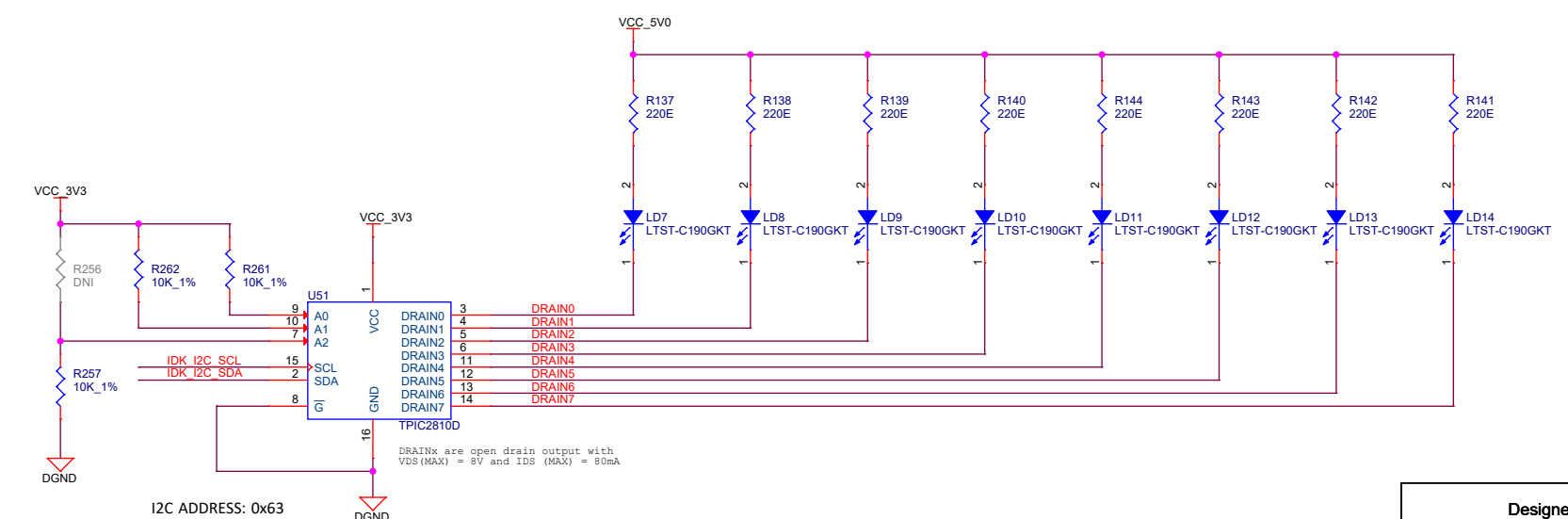
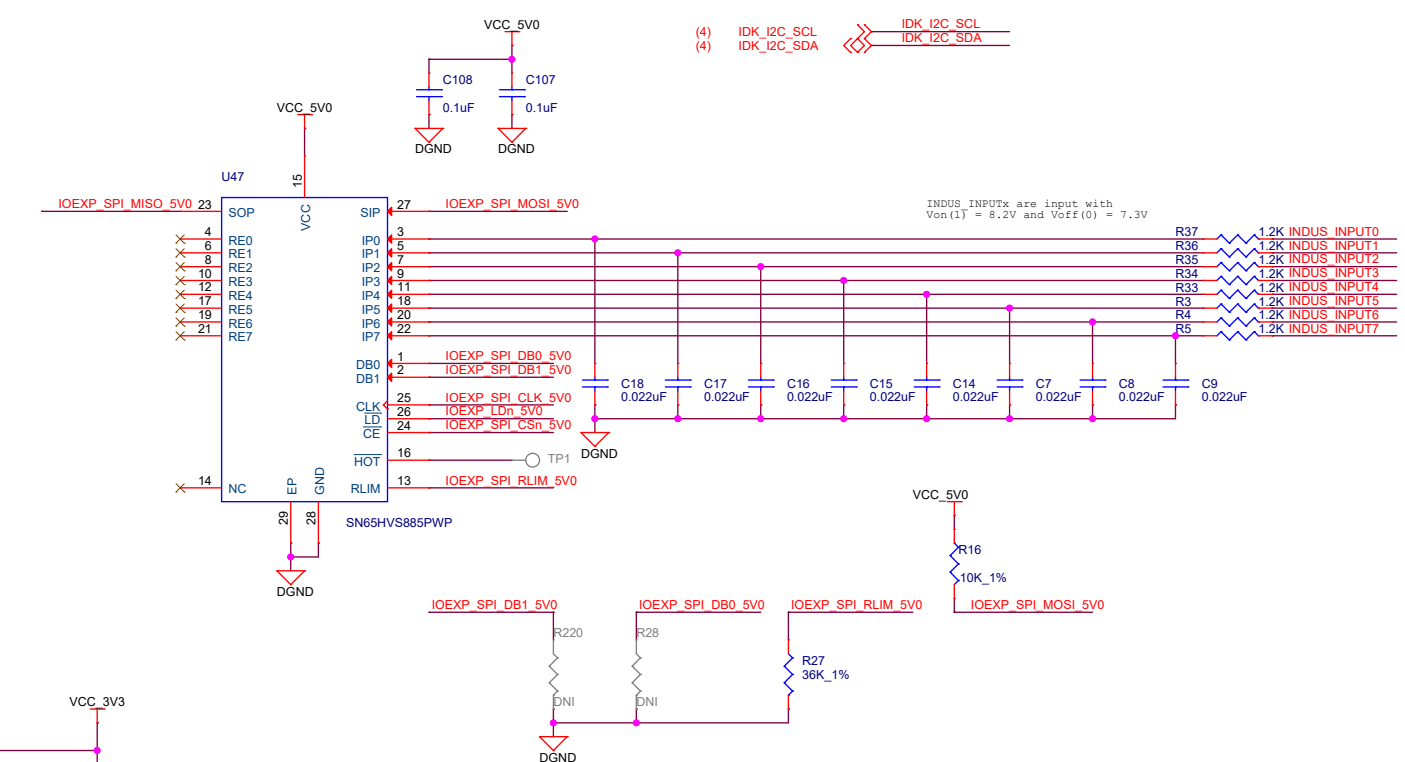
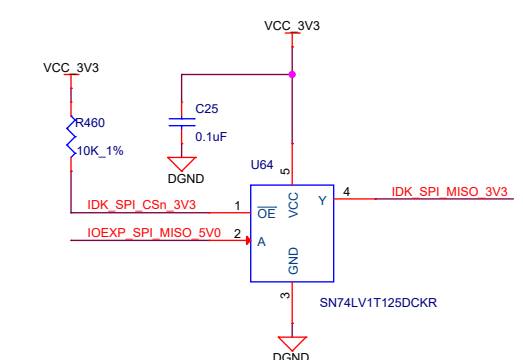
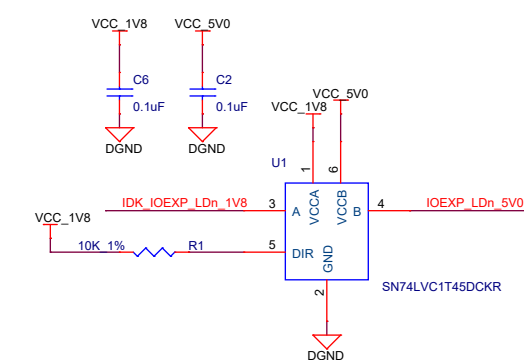
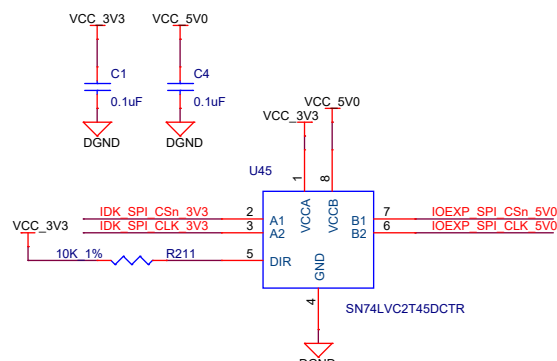
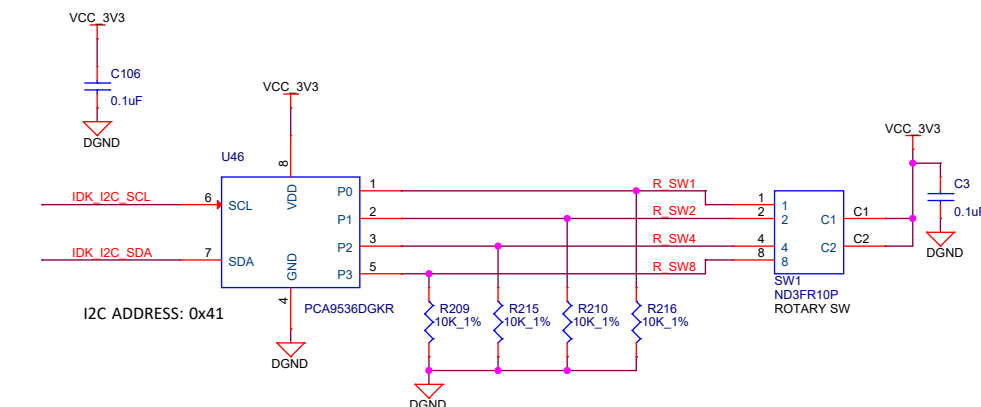




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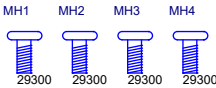


HARDWARE SCHEMATICS

ASSEMBLY NOTES

- 1. All MSL components should be baked as per JEDEC standard.
- 2. PCB should be baked at 120 degree for 8 hours.
- 3. Board assembly must comply with workmanship standards. IPC-A-610 Class 2, unless otherwise specified.
- 4. These assemblies are ESD sensitive, ESD precautions shall be observed.
- 5. These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
- 6. Provide serial numbers to the assembled boards for identification.
- 7. The assembled board are wrapped in ESD Covers(individual) and packed securely before shipment.

SCREWS

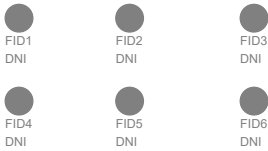


WASHER'S



These mechanicals will be used to secure this board to PROC062

FIDUCIALS



LABELS

Board Serial No.

Assembly Revision

LBL1

PCB LABEL

THT-103-423-10

LBL2

PCB LABEL

THT-103-423-10

BARE PCB



LOGOs

PCB LOGO

DNI

Texas Instruments

PCB LOGO

DNI

For Evaluation only; not FCC approved for resale

PCB LOGO

DNI

CE Mark

PCB LOGO

DNI

WEEE Mark