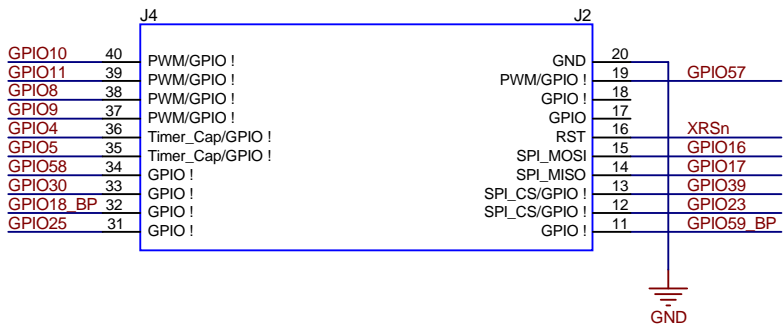
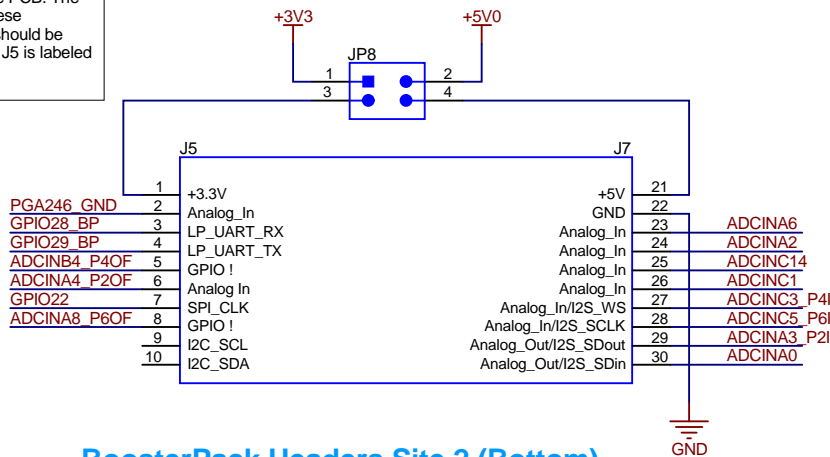


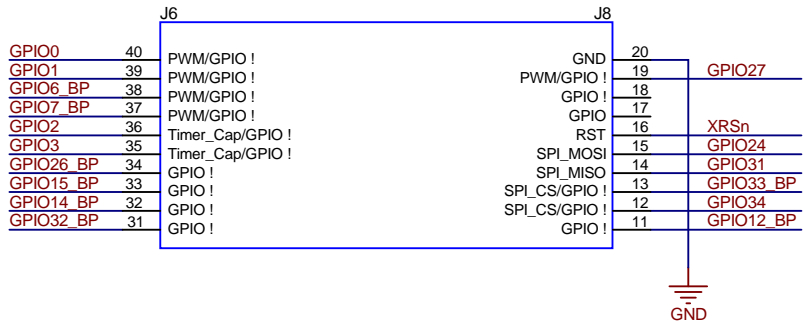
BoosterPack Headers Site 1 (Top)



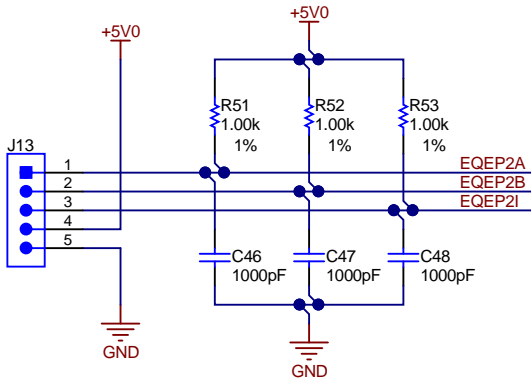
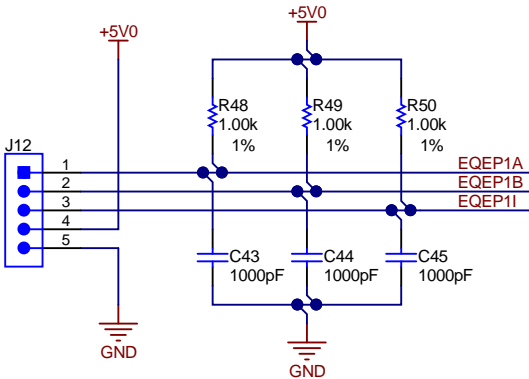
J5 - J8 are labeled on the PCB. The Pin numbers listed on these schematic components should be offset by 40. i.e. pin 1 on J5 is labeled as pin 40 on the PCB.



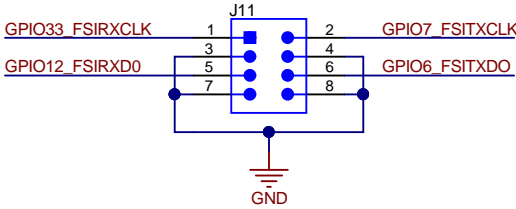
BoosterPack Headers Site 2 (Bottom)



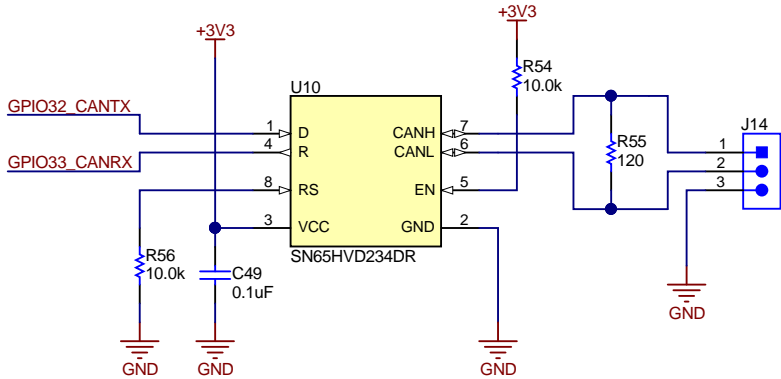
EQEP Connectors



FSI Connector

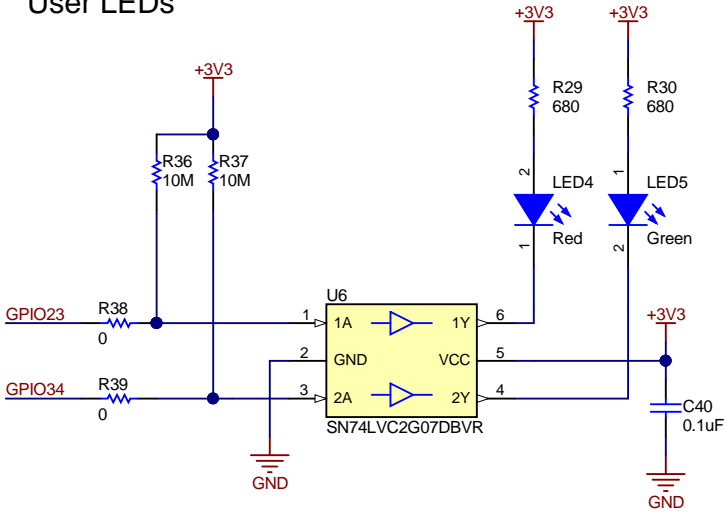


CAN Connector

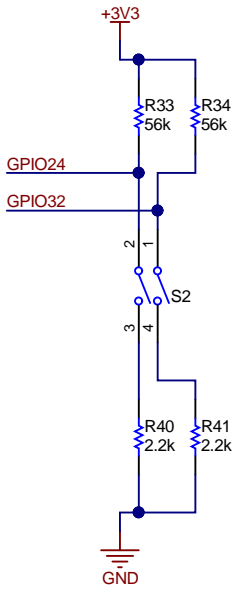


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User LEDs



Boot Mode Select

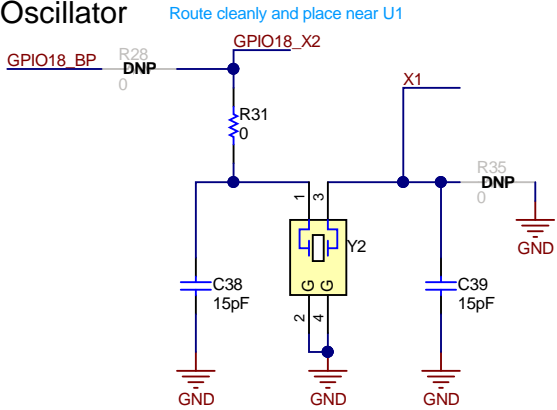


Selected Boot Mode Chart

S2 placed upside-down (so UP is open (1), DOWN is closed (0))

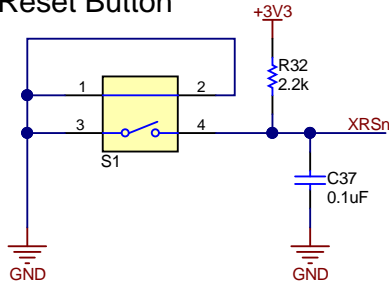
Mode #	GPIO24	GPIO32	Boot Mode
00	0	0	Boot from Parallel GPIO
01	0	1	Boot from SCI / Wait Mode
02	1	0	Boot from CAN
03	1	1	Boot from Flash

Oscillator

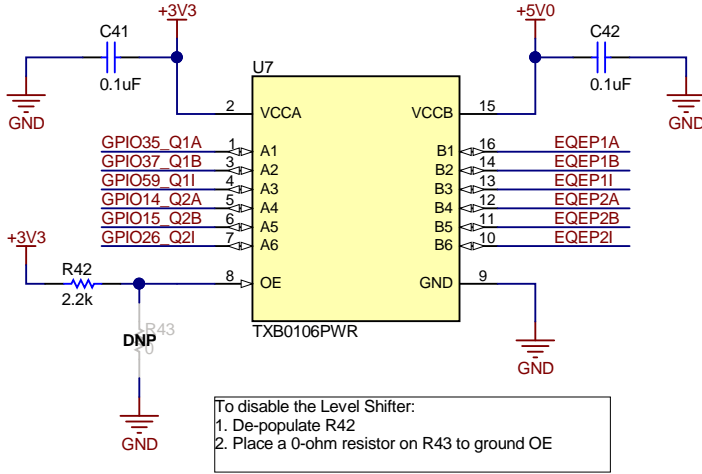


By default:  
- Crystal Y2 is connected between X1 and X2.  
- GPIO18\_BP is not connected to the BoosterPack header.  
  
If GPIO18 is needed at the Boosterpack Headers:  
- Remove R35  
- Place 0ohm resistors on both R31 and R38

Reset Button

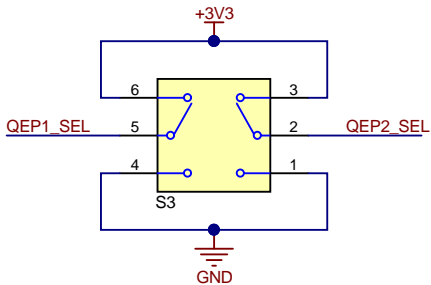
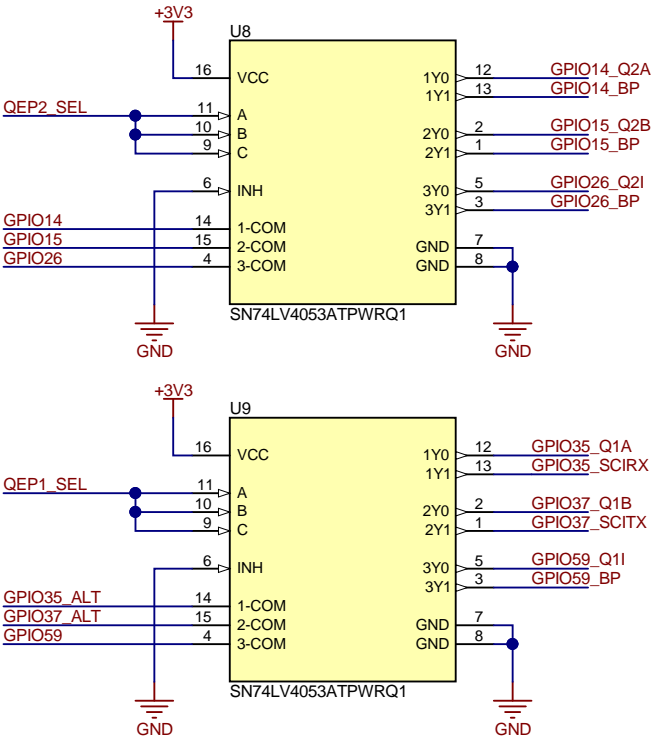


EQEP Level Shifter



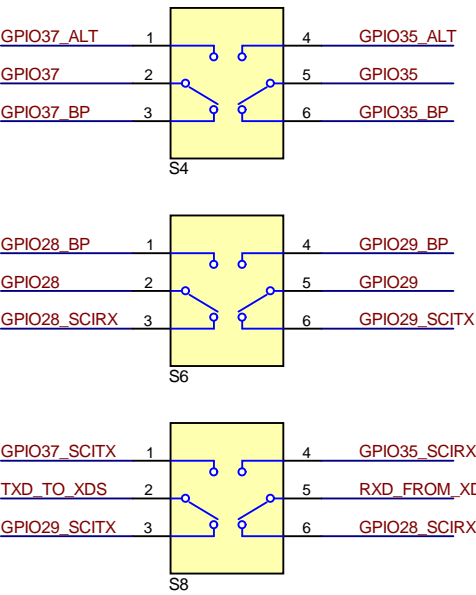
To disable the Level Shifter:  
1. De-populate R42  
2. Place a 0-ohm resistor on R43 to ground OE

EQEP Routing



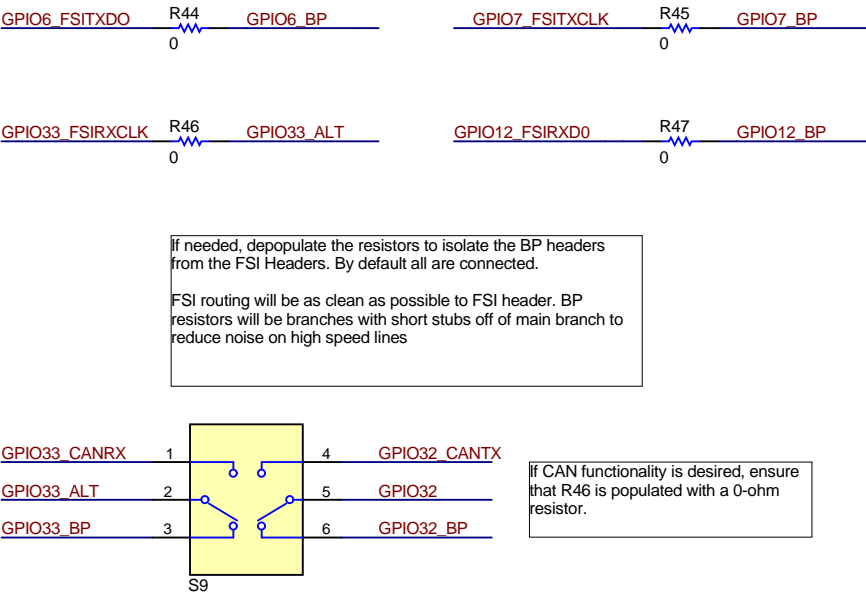
S3.x (1): QEP signals are routed to the BoosterPack Headers (default)  
S3.x (0): QEP signals are routed to the QEP Headers

UART Routing



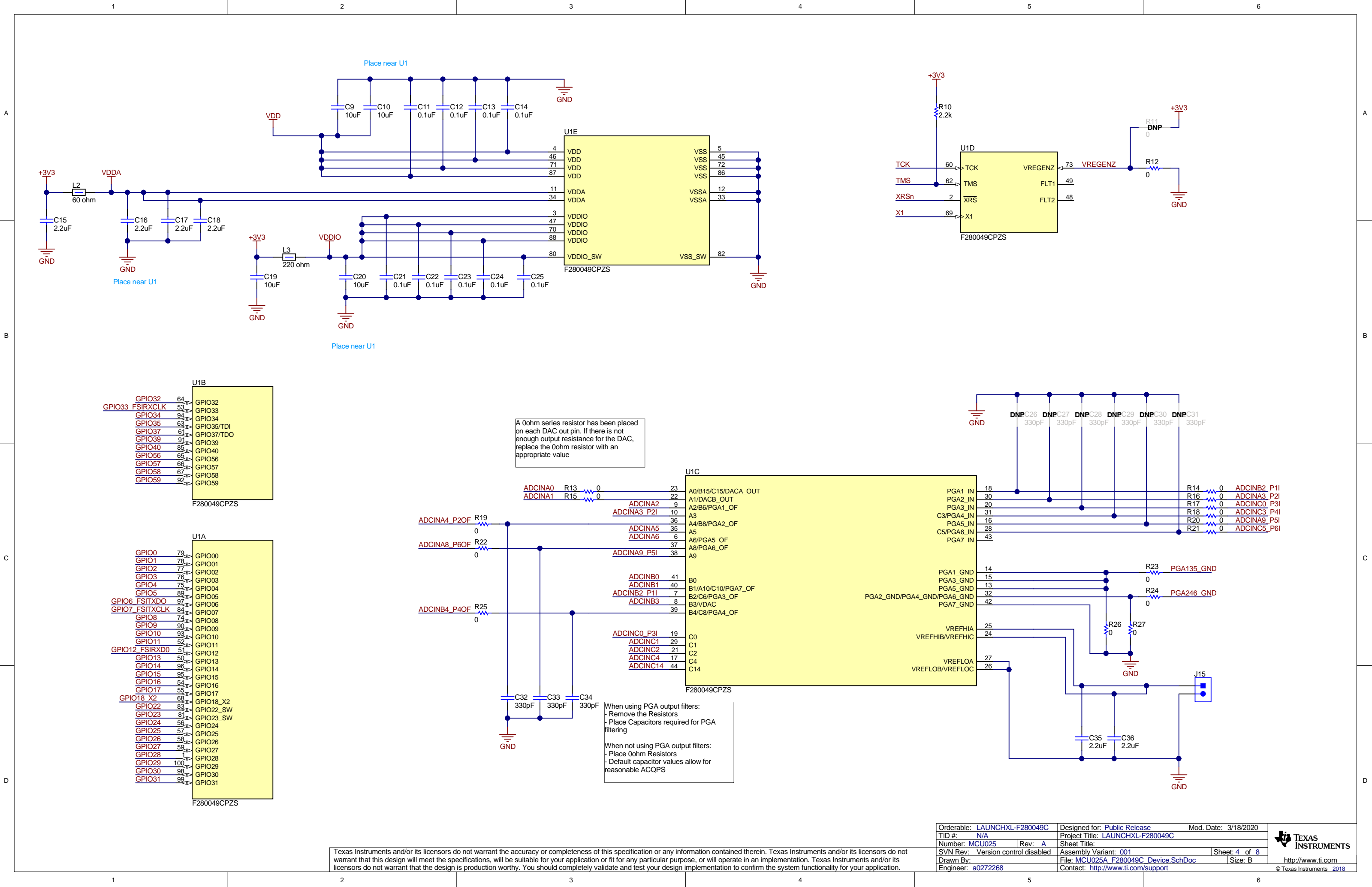
S6 is placed upside-down so the 0 position is 'up' towards the debug probe

FSI and CAN Routing



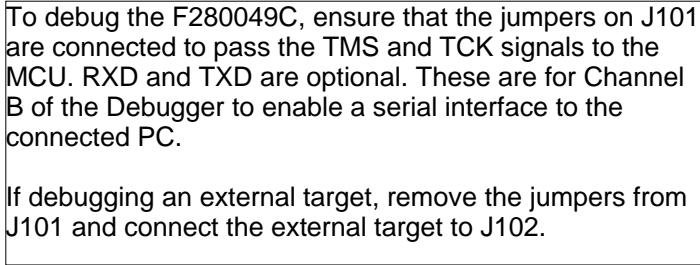
If needed, depopulate the resistors to isolate the BP headers from the FSI Headers. By default all are connected.  
  
FSI routing will be as clean as possible to FSI header. BP resistors will be branches with short stubs off of main branch to reduce noise on high speed lines

If CAN functionality is desired, ensure that R46 is populated with a 0-ohm resistor.

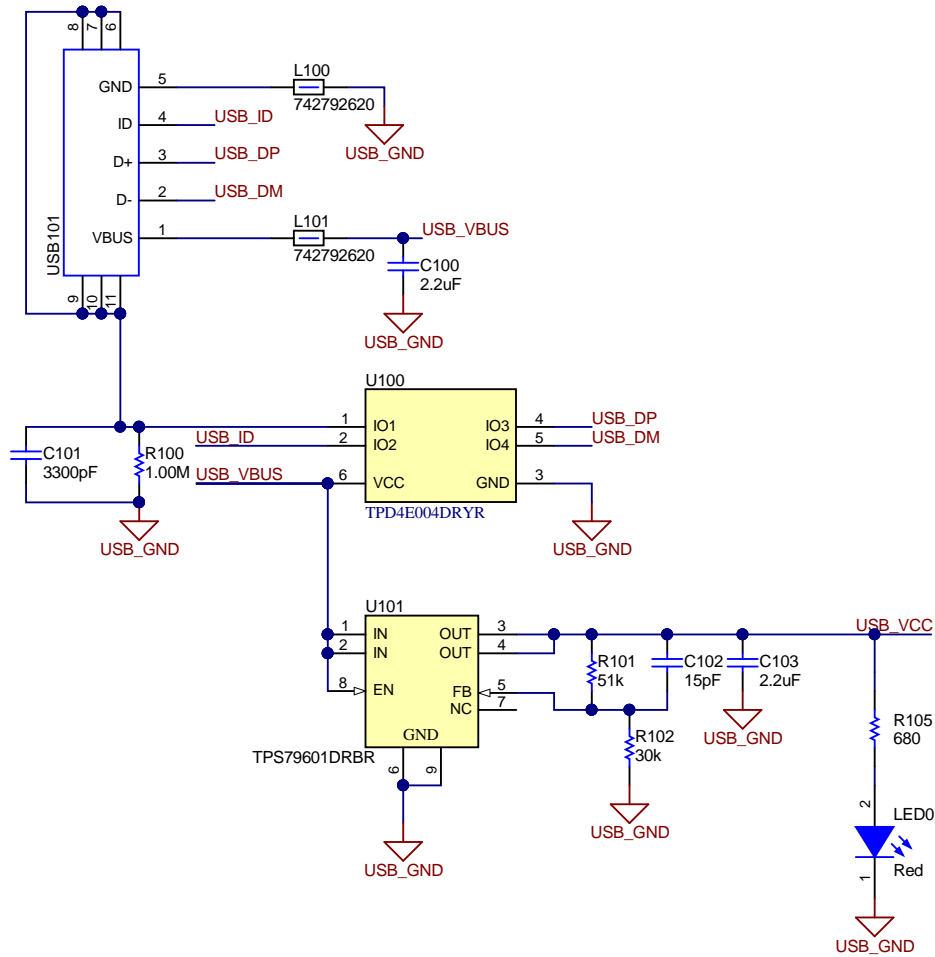


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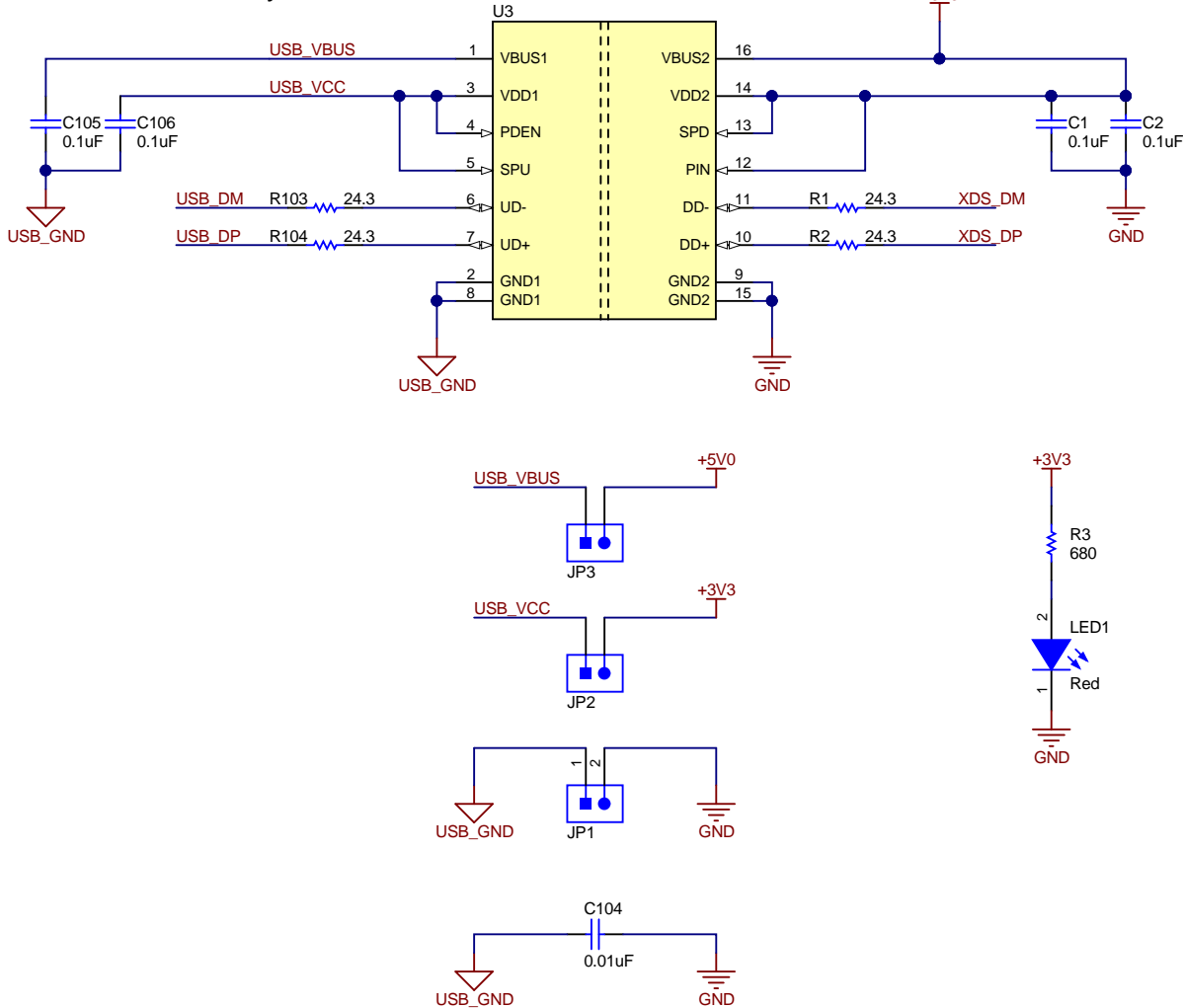
Orderable: LAUNCHXL-F280049C	Designed for: Public Release	Mod. Date: 3/18/2020
TID #: N/A	Project Title: LAUNCHXL-F280049C	
Number: MCU025	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 4 of 8
Drawn By:	File: MCU025A_F280049C_Device.SchDoc	Size: B
Engineer: a0272268	Contact: http://www.ti.com/support	



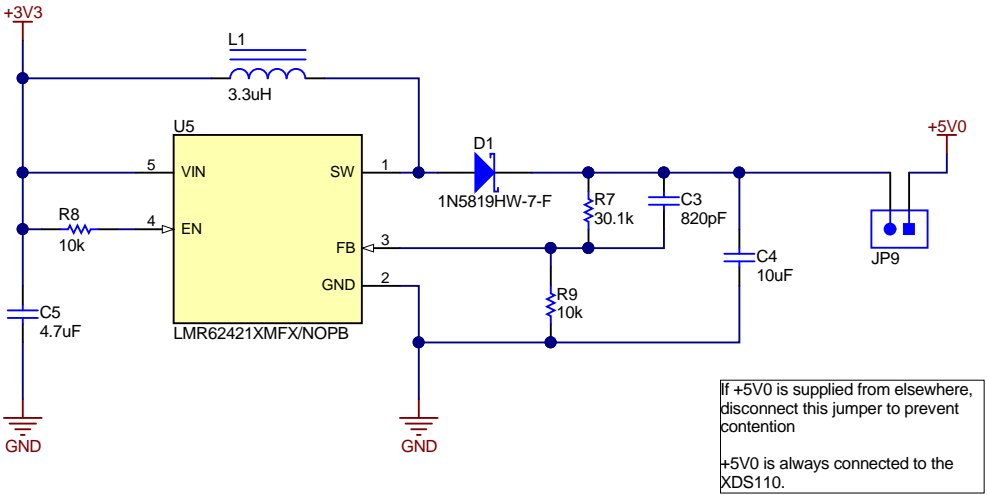
USB & XDS Power



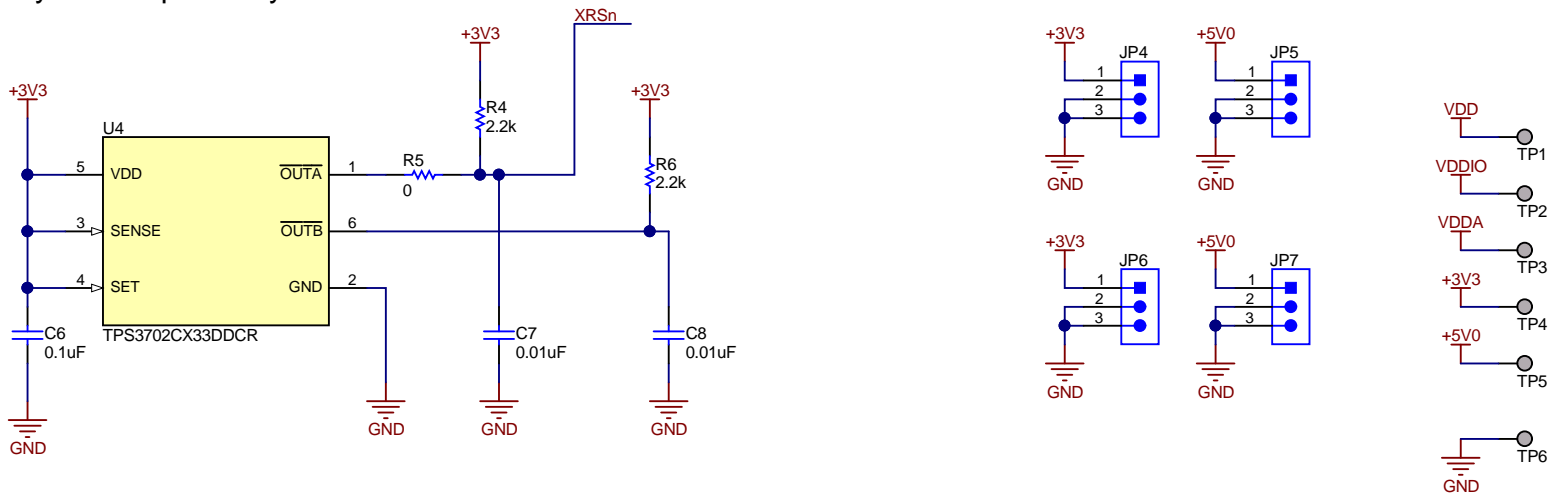
Isolation Boundary



3.3V to 5V Boost



System Supervisory Circuit



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